

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2023-2024 with effect from 20-09-2023

CLASS : B.Tech - III semester - Section A(Scheme-20)

LECTURE HALL : ECE-1

Day / Time	09.30 am - 10.30 am	10.30 am - 11.30 am	11.45 am - 12.45 pm	12.45 pm - 1.45 pm	1.45 pm -2.30 pm	2.30 pm - 3.30 pm	3.30 pm - 4.30 pm
MON	SAS ksr	PTSP ivrr	DSD pcpk	NTL msr	LUNCH BREAK	EDC(P)/DSD(P)/BS(P) Lab kvk/pcpk/as	
TUE	EDC(P)/DSD(P)/BS(P) Lab kvk/pcpk/as		DSD pcpk	NTL msr		PTSP ivrr	COI md
WED	MEFA ka	SAS ksr	PTSP ivrr	COI md		SAS ksr	BPP dr
THU	SAS ksr	PTSP ivrr	MEFA ka	DSD pcpk		BPP dr	BPP dr
FRI	MEFA ka	SAS ksr	NTL msr	PTSP ivrr		EDC(P)/DSD(P)/BS(P) Lab kvk/pcpk/as	
SAT	NTL msr	DSD pcpk	MOOCS wy	SEMINAR		Extra Academic Activities(EAA)	

NOTE: TEA BREAK 11.30 am TO 11.45am

Theory:

- | | | |
|---|--------|--|
| 1 | HU201 | Managerial Economics and Financial Accountancy(MEFA) |
| 2 | EE211 | Network Analysis and Transmission Lines(NTL) |
| 3 | EC201 | Digital System Design(DSD) |
| 4 | EC202 | Signals and Systems(SAS) |
| 5 | EC203 | Probability Theory and Stochastic processes(PTSP) |
| 6 | SCEC01 | Basic Programming in Python(BPP) |
| 7 | MC103 | Constitution of India(COI) |

Practicals

- | | | |
|----|-------|--|
| 8 | EC204 | Electronic Devices & Circuits Lab (EDC(P)) |
| 9 | EC205 | Digital System Design Lab (DSD(P)) |
| 10 | EC206 | Basic Simulations Lab (BS(P)) |
| 11 | MOOCS | Massive Open Online Course(Digital Learning) |

Faculty Member:

- | | |
|------|-------------------------------|
| ka | Dr K.Aparna |
| msr | Sri M.Srinivasa Reddy |
| pcpk | Dr.P.C.Praveen Kumar |
| ksr | Dr K.Suresh Reddy |
| ivrr | Sri. I.Venkata.Rameswar Reddy |
| drs | Dr D.R. Srinivas |
| md | Sri M.Devendra |

Faculty Member:

- | | |
|------|----------------------|
| kvk | Sri K.Vijay Kumar |
| pcpk | Dr.P.C.Praveen Kumar |
| as | Dr. A. Sowjanya |
| wy | Smt W.Yasmeen |

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2023-2024 with effect from 20-09-2023

CLASS : B.Tech - III semester - Section B(Scheme-20)

LECTURE HALL : ECE-2

Day / Time	09.30 am - 10.30 am	10.30 am - 11.30 am	11.45 am - 12.45 pm	12.45 pm - 1.45 pm	1.45 pm - 2.30 pm	2.30 pm - 3.30 pm	3.30 pm - 4.30 pm
MON	EDC(P)/DSD(P)/BS(P) Lab llpk/gdp/mvr		SAS snr	COI llm	LUNCH BREAK	NTL ban	MEFA pn
TUE	NTL ban	SAS snr	PTSP mvr	DSD gdp		EDC(P)/DSD(P)/BS(P) Lab llpk/gdp/mvr	
WED	BPP ssr	BPP ssr	SAS snr	PTSP mvr		MEFA pn	DSD gdp
THU	EDC(P)/DSD(P)/BS(P) Lab llpk/gdp/mvr		NTL ban	SAS snr		PTSP mvr	BPP ssr
FRI	MEFA pn	SAS snr	DSD gdp	NTL ban		PTSP mvr	COI llm
SAT	MOOCS	DSD gdp	PTSP mvr	SEMINAR		Extra Academic Activities(EAA)	

NOTE: TEA BREAK 11.30 am TO 11.45am

Theory:

- 1 HU201 Managerial Economics and Financial Accountancy(MEFA)
- 2 EE211 Network Analysis and Transmission Lines(NTL)
- 3 EC201 Digital System Design(DSD)
- 4 EC202 Signals and Systems(SAS)
- 5 EC203 Probability Theory and Stochastic processes(PTSP)
- 6 SCEC01 Basic Programming in Python(BPP)
- 7 MC103 Constitution of India(COI)

Practicals

- 8 EC204 Electronic Devices & Circuits Lab (EDC(P))
- 9 EC205 Digital System Design Lab
- 10 EC206 Basic Simulations Lab (BS(P))
- 11 MOOCS Massive Open Online Course(Digital Learning)

Faculty Member:

- pn Sri P.Nagaiah
ban Sri B. Amarnath Naidu
gdp Smt G.Divya Praneetha
snr Dr S.Nagaraja Rao
mvr Dr M.V.R.Vittal
ssr Dr S. Siva Reddy
mlm Smt M.Lalitha Meghana

Faculty Member:

- llpk Sri L.Lakshmi Prasanna Kumar
gdp Smt G.Divya Praneetha
mvr Dr M.V.R.Vittal
kvk Sri K. Vijay Kumar

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2023-2024 with effect from 20-09-2023

CLASS : B.Tech - III semester - Section C(Scheme-20)

LECTURE HALL : ECE-3

Day / Time	09.30 am - 10.30 am	10.30 am - 11.30 am	11.45 am - 12.45 pm	12.45 pm - 1.45 pm	1.45 pm - 2.30 pm	2.30 pm - 3.30 pm	3.30 pm - 4.30 pm
MON	DSD ts	PTSP drj	MEFA pn	SAS ksr	LUNCH BREAK	COI kld	NTL msr
TUE	PTSP drj	NTL msr	SAS ksr	DSD ts		SAS ksr	MEFA pn
WED	NTL msr	COI kld	DSD ts	PTSP drj		EDC(P)/DSD(P)/BS(P) Lab kr/ts/drj	
THU	BPP kr	BPP kr	NTL msr	DSD ts		EDC(P)/DSD(P)/BS(P) Lab kr/ts/drj	
FRI	EDC(P)/DSD(P)/BS(P) Lab kr/ts/drj		BPP kr	MEFA pn		SAS ksr	PTSP drj
SAT	SAS ksr	MOOCS	PTSP drj	SEMINAR		Extra Academic Activities(EAA)	

NOTE: TEA BREAK 11.30 am TO 11.45am

Theory:

- HU201 Managerial Economics and Financial Accountancy(MEFA)
- EE211 Network Analysis and Transmission Lines(NTL)
- EC201 Digital System Design(DSD)
- EC202 Signals and Systems(SAS)
- EC203 Probability Theory and Stochastic processes(PTSP)
- SCEC01 Basic Programming in Python(BPP)
- MC103 Constitution of India(COI)

Practicals

- EC204 Electronic Devices & Circuits Lab (EDC(P))
- EC205 Digital System Design Lab
- EC206 Basic Simulations Lab (BS(P))
- MOOCS Massive Open Online Course(Digital Learning)

Faculty Member:

- | | |
|-----|-----------------------|
| pn | Sri P.Nagaiah |
| msr | Sri M.Srinivasa Reddy |
| ts | Smt T.Swathi |
| ksr | Dr K.Suresh Reddy |
| drj | Dr D.Rajasekhar |
| kr | Sri K. Raju |
| kld | Smt K. Lakshmi Devi |

Faculty Member:

- | | |
|-----|---------------------|
| kr | Sri K. Raju |
| ts | Smt T.Swathi |
| drj | Dr D.Rajasekhar |
| kld | Smt K. Lakshmi Devi |

Prepared by Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2023-2024 with effect from 20-09-2023

CLASS : B.Tech - V semester - Section A

LECTURE HALL : ECE-4

Day / Time	8.30 am - 9.30am	09.30 am - 10.30am	10.45 am - 11.45 am	11.45 am - 12.45 pm	12.45 pm - 1.30 pm	1.30 pm - 2.30 pm	2.30 pm - 3.30 pm
MON	AWP llpk	LCS avkr	AWP(T) llpk	OS(T) ts	LUNCH BREAK	LDICA(T) ivrr	MPMC(T) wy
TUE	MPMC wy	LCS avkr	LDICA(T) ivrr	OS(T) ts		AWP(T) llpk	EITK(T) jstt
WED	PCB kuk	OE-I gdp	AWP llpk	EITK jstt		OS ts	MPMC wy
THU	LDICA ivrr	OS ts	AWP llpk	OE-I(IOT) gdp		MPMC(P)/LDICA(P) wy/ivrr	
FRI	MPMC(P)/LDICA(P) wy/ivrr		LDICA ivrr	PCB Design Lab kuk		PCB Design Lab kuk	
SAT	EITK jstt	LDICA ivrr	MPMC wy	OS ts		LCS avkr	OE-I gdp

NOTE: TEA BREAK 10.30 am TO 10.45am

Theory:

1	EC301	Microprocessors and Micro Controllers (MPMC)	ws
2	EC302	Linear and Digital IC Applications (LDICA)	ivrr
3	EC303	Antennas and Wave Propagation(AWP)	llpk
4	EE317	Linear Control Systems(LCS)	avkr
5	EC305	Professional Elective Course –I(PEC-1) (Operating Systems(OS))-	ts
6		Open Elective Course -I(OEC-I) (IOT)	gdp
7	MC	Essence of Indian Traditional Knowledge (EITK)	jstt

Practicals

7	EC307	Microprocessors and Micro Controllers Lab (MPMC Lab)	wy
8	EC308	Linear and Digital IC Applications Lab (LDICA Lab)	ivrr
9	SCEC03	PCB Design Lab	kuk

Faculty Member:

Smt. W.Yasmeen
Sri I.Venkata Rameswara Reddy
Sri L.Lakshmi Prasanna Kumar
Sri A.Vijay Kumar Reddy
Smt T.Swathi
Smt G.Divya Praneetha
Dr J.S.T.Thillagam

Smt W.Yasmeen
Sri I.Venkata Rameswara Reddy
Smt K.Udaya Kiran

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING TIME TABLE for the Academic year 2023-2024 with effect from 20-09-2023

CLASS : B.Tech - V semester - Section B

LECTURE HALL :ECE - 5

Day / Time	8.30 am - 9.30am	09.30 am - 10.30am	10.45 am - 11.45 am	11.45 am - 12.45 pm	12.45 pm - 1.30 pm	1.30 pm - 2.30 pm	2.30 pm - 3.30 pm
MON	MPMC(P)/LDICA(P) gr/dro		AWP(T) jstt	OS(T) dro	LUNCH BREAK	LDICA(T) sv	MPMC(T) gr
TUE	LCS knk	EITK jstt	MPMC(T) gr	OS(T) dro		AWP(T) jstt	LDICA(T) sv
WED	OS dro	OE-I wy	PCB gak	LCS knk		MPMC(P)/LDICA(P) gr/dro	
THU	AWP jstt	LDICA sv	MPMC gr	OE-I(IOT) wy		OS dro	AWP jstt
FRI	PCB Design Lab gak			EITK jstt		LDICA sv	MPMC gr
SAT	MPMC gr	OS dro	AWP jstt	LDICA sv		LCS knk	OE-I(IOT) wy

NOTE: TEA BREAK 10.30 am TO 10.45am

Theory:

1	EC301	Microprocessors and Micro Controllers (MPMC)	gr
2	EC302	Linear and Digital IC Applications (LDICA)	sv
3	EC303	Antennas and Wave Propagation(AWP)	jstt
4	EE317	Linear Control Systems(LCS)	knk
5	EC305	Professional Elective Course –I(PEC-1) (Operating Systems(OS))-	dro
6		Open Elective Course -I(OEC-I) IOT	wy
7	MC	Essence of Indian Traditional Knowledge (EITK)	jstt

Practicals

7	EC307	Microprocessors and Micro Controllers Lab (MPMC Lab)	gr
8	EC308	Linear and Digital IC Applications Lab (LDICA Lab)	dro
9	SCEC03	PCB Design Lab	gak

Faculty Member:

Dr G.Ramesh
Dr S.Vsyhali
Dr J.S.T.Thillagam
sri. K. Nitesh Kumar
Smt D.Rohini
Smt W. Yasmeen
Dr J.S.T.Thillagam

Dr G.Ramesh
Smt D.Rohini
Dr G.Amjad Khan

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2023-2024 with effect from 20-09-2023

CLASS : B.Tech - V semester - Section C

LECTURE HALL :ECE-6

Day / Time	8.30 am - 9.30am	09.30 am - 10.30am	10.45 am - 11.45 am	11.45 am - 12.45 pm	12.45 pm - 1.30 pm	1.30 pm - 2.30 pm	2.30 pm - 3.30 pm
MON	LDICA cr	AWP as	AWP(T) as	OS(T) gdp	LUNCH BREAK	LDICA(T) cr	MPMC(T) smsd
TUE	AWP as	EITK tsb	OS(T) gdp	LDICA(T) cr		MPMC(T) smsd	AWP(T) as
WED	LCS avkr	OE-I(IOT) drs	OS gdp	PCB Design Lab gak		PCB Design Lab gak	
THU	MPMC(P)/LDICA(P) smsd/ts		AWP as	OE-I(IOT) drs		MPMC smsd	LCS avkr
FRI	LDICA cr	MPMC smsd	MPMC(P)/LDICA(P) smsd/ts			EITK tsb	OS gdp
SAT	PCB gak	LDICA cr	LCS avkr	OS gdp		MPMC smsd	OE-I(IOT) drs

NOTE: TEA BREAK 10.30 am TO 10.45am

Theory:

1	EC301	Microprocessors and Micro Controllers (MPMC)	smsd
2	EC302	Linear and Digital IC Applications (LDICA)	cr
3	EC303	Antennas and Wave Propagation(AWP)	as
4	EE317	Linear Control Systems(LCS)	avkr
5	EC305	Professional Elective Course –I(PEC-1) (Operating Systems(OS))-	gdp
6		Open Elective Course -I(OEC-I) (IOT)	drs
7	MC	Essence of Indian Traditional Knowledge (EITK)	tsb

Practicals

7	EC307	Microprocessors and Micro Controllers Lab (MPMC Lab)	smsd
8	EC308	Linear and Digital IC Applications Lab (LDICA Lab)	tsb
9	SCEC03	PCB Design Lab	gak

Faculty Member:

Dr S.M.Shamsheer Daula
Smt.C.Rajeswari
Dr. A. Sowjanya
Sri A.Vijay Kumar Reddy
Smt G.Divya Praneetha
Dr.D.R. Srinivas
Sri. T. Sharat Babu

Dr S.M.Shamsheer Daula
Sri. T. Sharat Babu
Dr G.Amjad Khan

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2023-2024 with effect from 17-07-2023

CLASS : B.Tech - VII semester - Section A(Scheme-20)

LECTURE HALL : ECE - 7

Day / Time	9.30 am -10.30am	10.30 am - 11.30am	11.45 am -12.45pm	12.45pm -1.45pm	1.45 pm -2.30 pm	2.30 pm - 3.30pm	3.30 pm - 4.30pm	
MON	UHV-2 kuk	IP rsb	CMC as	CN tsb	LUNCH BREAK	DS(P) Lab ssr		
TUE	DES gr	IP rsb	DS(P) Lab ssr			UHV-2 kuk	CMC as	
WED	PW jstt		CMC as	DES gr		FPGA &ASIC mmr	CN tsb	
THU	PW jstt		UHV-2 kuk	FPGA &ASIC mmr		DES gr	CN tsb	
FRI	IP rsb	FPGA &ASIC mmr	DES(T) gr	IP(T) rsb				
SAT								

TEA BREAK 11.30 am TO 11.45am

LUNCH BREAK: 1.45pm to 2.30pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

1	CMC	Cellular and Mobile Communications (Professional Elective-III)	---
2	CN	Computer Networks(Professional Elective-IV)	---
3	FPGA & ASIC	FPGA and ASIC Architectures and Applications(Professional Elective-V)	---
4	DES	Distributed Embedded Systems(Open Elective-III)	---
5	IP	Image Processing(Open Elective-IV)	---
6	UHV2	Universal Human Valu -2	---

Practicals

7	DSL	Data Science Lab(DSL)	---
8	PW	Project Work	---

Faculty Member:

as	Smt A. Sowjanya
tsb	Sri T. Sarath Babu
mmr	Sri M. Madhusudhan Reddy
gr	Dr G. Ramesh
rsb	Dr R. Sudheer Babu
kuk	Smt K. Udaya Kiran
ssr	Dr S.Siva Reddy
jstt	Dr J.S.T.Thillagam

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

GPREC-F / CFD / TIME TABLE

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2023-2024 with effect from 17-07-2023

CLASS : B.Tech - VII semester - Section B(Scheme-20)

LECTURE HALL ECE - 8

Day / Time	9.30 am - 10.30am	10.30 am - 11.30am	11.45 am - 12.45pm	12.45pm - 1.45pm	1.45 pm -2.30 pm	2.30 pm - 3.30pm	3.30 pm -4.30pm
MON	DS(P) Lab drs		CMC mvr	DES smsd	LUNCH BREAK	PGA &ASIC gvrs	UHV-2 tsb
TUE	CMC mvr	DES smsd	CN ap	IP draj		DS(P) Lab drs	
WED	UHV-2 tsb	FPGA &ASIC gvrs	PW md			DES smsd	CN ap
THU	IP draj	FPGA &ASIC gvrs	CMC mvr	UHV-2 tsb		PW md	
FRI	CN ap	IP draj	DES(T) smsd	CN(T) ap			
SAT							

TEA BREAK 11.30 am TO 11.45am

LUNCH BREAK: 1.45pm to 2.30pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

1	CMC	Cellular and Mobile Communications (Professional Elective-III)	---
2	CN	Computer Networks(Professional Elective-IV)	---
3	FPGA &	FPGA and ASIC Architectures and Applications(Professional	---
4	DES	Distributed Embedded Systems(Open Elective-III)	---
5	IP	Image Processing(Open Elective-IV)	---
6	UHV2	Universal Human Va -2	---

Practicals

7	DSL	Data Science Lab(SC)	---
8	PW	Project Work	---

Faculty Member:

mvr	Dr M.V.R Vittal
ap	Smt A.Parvathi
gvr	Dr G.V.R Sagar
smsd	Dr S.M.Shamsheer Daula
drj	Dr D. Rajasekhar
tsb	Sri T. Sarath Babu
drs	Dr D.R.Srinivas
md	Sri M. Devendra

Prepared by Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

GPREC-F / CFD / TIME TABLE
G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2023-2024 with effect from 17-07-2023

CLASS : B.Tech - VII semester - Section C(Scheme-20)

LECTURE ECE - 9

Day / Time	9.30 am - 10.30am	10.30 am - 11.30am	11.45 am - 12.45pm	12.45pm - 1.45pm	1.45 pm -2.30 pm	2.30 pm - 3.30pm	3.30 pm -4.30pm
MON	UHV-2 kr	IP pak	DS(P) Lab avsr		LUNCH BREAK	CN dvsvb	FPGA &ASIC dro
TUE	DS(P) Lab avsr		DES krk	UHV-2 kr		IP pak	CMC pcpk
WED	CN dvsvb	DES krk	FPGA &ASIC dro	CMC pcpk		PW ivrr	
THU	FPGA &ASIC dro	CN dvsvb	PW ivrr			CMC pcpk	IP pak
FRI	DES krk	UHV-2 kr	DES(T) krk	CN(T) dvsvb			
SAT							

TEA BREAK 11.30 am TO 11.45am

LUNCH BREAK: 1.45pm to 2.30pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

1	CMC	Cellular and Mobile Communications (Professional Elective-III)	---
2	CN	Computer Networks(Professional Elective-IV)	---
3	FPGA & ASIC	FPGA and ASIC Architectures and Applications(Professional Elective-V)	---
4	DES	Distributed Embedded Systems(Open Elective-III)	---
5	IP	Image Processing(Open Elective-IV)	---
6	UHV2	Universal Human Va -2	---

Practicals

7	DSL	Data Science Lab(SC)	---
8	PW	Project Work	---

Faculty Member:

pcpk	Dr P.C. Praveen Kumar
dvsvb	Dr D. V. Srihari Babu
dro	Smt D. Rohini
krk	Sri K. Ramakrishna
pak	Dr P. Abdul Khayum
krk	Sri K. Raju
avsr	Sri A. Venkata Subba Reddy
ivrr	Sri I. Venkata Rameswar Reddy

Prepared by :Sri B. Siva Reddy

Verfied by: HOD (E.C.E.)

Approved by :PRINCIPAL

