

GPREC-F / CFD / TIME TABLE
G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2023-2024 with effect from 17-07-2023

CLASS : B.Tech - VII semester - Section A(Scheme-20)

LECTURE HALL : ECE - 7

Day / Time	9.30 am -10.30am	10.30 am -11.30am	11.45 am -12.45pm	12.45pm -1.45pm	2.30 pm -3.30pm	3.30 pm -4.30pm
MON	FPGA & ASIC mmr	IP rsb	CMC as	CN tsb	DS(P) Lab/PW ssr/jstt	
TUE	DES gr	IP rsb	DS(P) Lab/PW ssr/jstt		UHV-2 kuk	CN tsb
WED	DS(P) Lab/PW ssr/jstt		UHV-2 kuk	DES gr	FPGA & ASIC mmr	IP rsb
THU	DS(P) Lab/PW ssr/jstt		UHV-2 kuk	FPGA & ASIC mmr	DES gr	CMC as
FRI	CN tsb	CMC as	DES(T) gr	IP(T) rsb	FPGA & ASIC (T) mmr	UHV-2(T) kuk
SAT	IP(T) rsb	DES(T) gr	FPGA & ASIC (T) mmr	UHV-2(T) kuk	CN(T) tsb	CMC(T) as

TEA BREAK 11.30 am TO 11.45am

LUNCH BREAK: 1.45pm to 2.30pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

- | | | | | |
|---|-------------|---|-----|-----|
| 1 | CMC | Cellular and Mobile Communications (Professional Elective-III) | --- | as |
| 2 | CN | Computer Networks(Professional Elective-IV) | --- | tsb |
| 3 | FPGA & ASIC | FPGA and ASIC Architectures and Applications(Professional Elective-V) | --- | mmr |
| 4 | DES | Distributed Embedded Systems(Open Elective-III) | --- | gr |
| 5 | IP | Image Processing(Open Elective-IV) | --- | rsb |
| 6 | UHV2 | Universal Human Values -2 | --- | kuk |

Practicals

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|---|-----|----------------------|-----|------|
| 7 | DSL | Data Science Lab(SC) | --- | ssr |
| 8 | PW | Project Work | --- | jstt |

Faculty Member:

- | |
|--------------------------|
| Smt A. Sowjanya |
| Sri T. Sarath Babu |
| Sri M. Madhusudhan Reddy |
| Dr G. Ramesh |
| Dr R. Sudheer Babu |
| Smt K. Udaya Kiran |

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2023-2024 with effect from 17-07-2023

CLASS : B.Tech - VII semester - Section B(Scheme-20)

LECTURE HALL : ECE - 8

Day / Time	9.30 am -10.30am	10.30 am -11.30am	11.45 am -12.45pm	12.45pm -1.45pm	2.30 pm -3.30pm	3.30 pm -4.30pm
MON	DS(P) Lab/PW drs/md		FPGA &ASIC gvrs	DES smsd	CMC mvrsv	UHV-2 tsb
TUE	CMC mvrsv	DES smsd	CN ap	IP draj	DS(P) Lab/PW drs/md	
WED	UHV-2 tsb	FPGA &ASIC gvrs	DS(P) Lab/PW drs/md		DES smsd	CN ap
THU	FPGA &ASIC gvrs	IP draj	CMC mvrsv	UHV-2 tsb	DS(P) Lab/PW drs/md	
FRI	CN ap	IP draj	DES(T) smsd	CN(T) ap	CMC(T) mvrsv	FPGA &ASIC (T) gvrs
SAT	IP(T) draj	DES(T) smsd	FPGA &ASIC (T) gvrs	UHV-2(T) tsb	CN(T) ap	CMC(T) mvrsv

TEA BREAK 11.30 am TO 11.45am

LUNCH BREAK: 1.45pm to 2.30pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

- 1 CMC Cellular and Mobile Communications (Professional Elective-III)
- 2 CN Computer Networks(Professional Elective-IV)
- 3 FPGA & ASIC FPGA and ASIC Architectures and Applications(Professional Elective-V)
- 4 DES Distributed Embedded Systems(Open Elective-III)
- 5 IP Image Processing(Open Elective-IV)
- 6 UHV2 Universal Human Values -2

Practicals

- 7 DSL Data Science Lab(SC)
- 8 PW Project Work

Faculty Member:

- Dr M.V.R Vittal
Smt A.Parvathi
Dr G.V.R Sagar
Dr S.M.Shamsheer Daula
Dr D. Rajasekhar
Sri T. Sarath Babu

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2023-2024 with effect from 17-07-2023

CLASS : B.Tech - VII semester - Section C(Scheme-20)

LECTURE HALL : ECE - 9

Day / Time	9.30 am -10.30am	10.30 am -11.30am	11.45 am -12.45pm	12.45pm -1.45pm	2.30 pm -3.30pm	3.30 pm -4.30pm
MON	UHV-2 kr	IP pak	DS(P) Lab/PW avsr/ivrr		CN dvsb	FPGA &ASIC dro
TUE	DS(P) Lab/PW avsr/ivrr		DES krk	UHV-2 kr	IP pak	CMC pcpk
WED	CN dvsb	DES krk	FPGA &ASIC dro	CMC pcpk	DS(P) Lab/PW avsr/ivrr	
THU	FPGA &ASIC dro	CN dvsb	DS(P) Lab/PW avsr/ivrr		DES krk	UHV-2 kr
FRI	CMC pcpk	IP pak	DES(T) krk	CN(T) dvsb	FPGA &ASIC (T) dro	UHV-2(T) kr
SAT	IP(T) pak	DES(T) krk	FPGA &ASIC (T) dro	UHV-2 kr	CN(T) dvsb	CMC(T) pcpk

TEA BREAK 11.30 am TO 11.45am

LUNCH BREAK: 1.45pm to 2.30pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

- | | | | | |
|---|-------------|---|-----|------|
| 1 | CMC | Cellular and Mobile Communications (Professional Elective-III) | --- | pcpk |
| 2 | CN | Computer Networks(Professional Elective-IV) | --- | dvsb |
| 3 | FPGA & ASIC | FPGA and ASIC Architectures and Applications(Professional Elective-V) | --- | dro |
| 4 | DES | Distributed Embedded Systems(Open Elective-III) | --- | krk |
| 5 | IP | Image Processing(Open Elective-IV) | --- | pak |
| 6 | UHV2 | Universal Human Values -2 | --- | krk |

Practicals

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|---|-----|----------------------|-----|------|
| 7 | DSL | Data Science Lab(SC) | --- | avsr |
| 8 | PW | Project Work | --- | ivrr |

Faculty Member:

- | |
|-------------------------------|
| Dr P.C. Praveen Kumar |
| Dr D. V. Srihari Babu |
| Smt D. Rohini |
| Sri K. Ramakrishna |
| Dr P. Abdul Khayum |
| Sri K. Raju |
| Sri A. Venkata Subba Reddy |
| Sri I. Venkata Rameswar Reddy |

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL