G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - III semester - Section A (Scheme-23)

LECTURE HALL : EEE-1

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10 am	10.40 am - 11.30 am	11.30 am - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm - 3.00 pm	3.00 pm - 4:30 pm
MON	EMF ksg	DCMT(P)/ECA2&S(P) gsr/mss			LUNCH BREAK	CVNM krv	MOOCS br	Student Activities/ Remidial Classes/ Minor Degree/ Honours Degree/ GATE
TUE	ECA2 gk	DCMT(P)/ECA2&S(P) gsr/mss				DT&I avkr		
WED	DCMT knk	ECA2 gk	EMF ksg	CVNM krv		DS ps		
THU	DCMT knk	CVNM krv	ECA2 gk	EMF ksg		UHV UH mss	MOOCS br	
FRI	DS ps		DCMT knk	ECA2 gk		CVNM krv	UHV UH mss	
SAT	UHV UH mss	EMF ksg	ECA2 gk	DCMT knk		DT&I/Mentoring avkr	CVNM krv	

TEA BREAK 10.10 am to 10.40 am

LUNCH BREAK: 12.20 pm to 1.20 pm

Theory:

Complex Variables & Numerical Methods
Understanding Human Values-Understanding Harmony
Electromagnetic Field Theory
Electrical Circuit Analysis-II
DC Machines & Transformers

Practicals

Electrical Circuit Analysis-II and Simulation Lab
DC Machines & Transformers Lab
Data Structures (SOC)
Design Thinking & Innovation
MOOCS Massive Open Online Course (Digital Learning)

Faculty Member:

krv	Sri K. Raghavendra
mss	Dr. M. Sivasatyanarayan Varma
ksg	Dr. K. Sri Gowri
gk	Dr. G. Kishor
knk	Sri K. Niteesh Kumar

Faculty Member:

mss	Dr. M. Sivasatyanarayan Varma
gsr	Dr. G. Sreenivasa Reddy
ps	Smt. P. Sunanda
avkr	Sri. A. Vijay Kumar Reddy
br	Smt. B. Rajeswari

Prepared by: Dr. G. Sreenivasa Reddy

Verfied by: HOD (E.E.E.)

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - III semester - Section B (Scheme-23) **LECTURE HALL : EEE-2**

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10 am	10.40 am - 11.30 am	11.30 am - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm - 3.00 pm	3.00 pm - 4:30 pm
MON	CVNM mssr	DCMT tbr	ECA2 knk	ECA2 knk	LUNCH BREAK	DT&I bu		Student Activities/ Remidial Classes/ Minor Degree/ Honours Degree/ GATE
TUE	ECA2 knk	DCMT tbr	EMF chr	CVNM mssr		DS ssk		
WED	DT&I/Mentoring bu	DCMT(P)/ECA2&S(P) chr/msr				UHV UH mss	DCMT tbr	
THU	EMF chr	ECA2 knk	CVNM mssr	UHV UH mss		DS ssk		
FRI	UHV UH mss	ECA2 knk	EMF chr	CVNM mssr		MOOCS msr		
SAT	DCMT tbr	DCMT(P)/ECA2&S(P) chr/msr				CVNM mssr	EMF chr	

TEA BREAK 10.10 am to 10.40 am **LUNCH BREAK: 12.20 pm to 1.20 pm**

Theory:		**Faculty Member:**
Complex Variables & Numerical Methods	mssr	Dr. M. Siva Shankar Reddy
Understanding Human Values-Understanding Harmony	mss	Dr. M. Sivasatyanarayana Var
Electromagnetic Field Theory	chr	Dr. C. Harinatha Reddy
Electrical Circuit Analysis-II	knk	Sri K. Niteesh Kumar
DC Machines & Transformers	tbr	Dr. T. Bramhananda Reddy
Practicals		**Faculty Member:**
Electrical circuit Analysis-II and Simulation Lab	msr	Sri M. Sreenivasa Reddy
DC Machines & Transformers Lab	chr	Dr. C. Harinatha Reddy
Data Structures (SOC)	ssk	Smt. S. Sashikala
Design Thinking & Innovation	bu	Dr. B. Urmila
MOOCS Massive Open Online Course(Digital Learning)	msr	Sri M. Sreenivasa Reddy

Prepared by: Dr. G. Sreenivasa Reddy **Verified by: HOD (E.E.E.)** **Approved by: PRINCIPAL**

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - V semester - Section A (Scheme-20) **LECTURE HALL : EEE-3**

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10 am	10.40 am - 11.30 am	11.30 am - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm - 3.00 pm	3.00 pm - 4:30 pm
MON	NTSS ban	ICMP ivrr	ITCA		LUNCH BREAK	ITCA		Student Activities/ Remidial Classes/ Minor Degree/ Honours Degree/ GATE
TUE	COI mb	PS2 gvsr	ITCA			ITCA		
WED	OE1	ICMP ivrr	PEL1 mhvr	PS2 gvsr		COI mb	NTSS ban	
THU	EMC2(P)/PLC(P)/ICMP(P) val/nrr/krk		COI mb	PEL1 mhvr		PS2 gvsr	OE1	
FRI	EMC2(P)/PLC(P)/ICMP(P) val/nrr/krk		NTSS ban	PEL1 mhvr		ICMP ivrr	OE1	
SAT	PS2 gvsr	NTSS ban	PEL1 mhvr	ICMP ivrr		EMC2(P)/PLC(P)/ICMP(P) val/nrr/krk		

TEA BREAK 10.10 am to 10.40 am **LUNCH BREAK: 12.20 pm to 1.20 pm**

Theory:

Power Electronics-I
Power Systems-II
Integrated Circuits and Microprocessors
Network Theory and Signals & Systems
Open Elective-I
Constitution of India

Practicals

Electrical Machines-II
Integrated Circuits and Microprocessors
Programmable Logic Controllers

Faculty Member:

mhvr	Dr. M. Harsha Vardhan Reddy
gvsr	Dr. G.V. Subba Reddy
ivrr	Sri I.V. Rameshwar Reddy
ban	Sri. B. Amarnath Naidu
mb	Sri Masoom Basha

Faculty Member:

val	Dr. V. Anantha Lakshmi
krk	Sri K. Rama Krishna
nrr	Dr. N. Ravishankar Reddy

Prepared by: Dr. G. Sreenivasa Reddy **Verified by: HOD (E.E.E.)** **Approved by: PRINCIPAL**

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - V semester - Section B (Scheme-20) **LECTURE HALL : EEE-4**

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10 am	10.40 am - 11.30 am	11.30 am - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm - 3.00 pm	3.00 pm - 4:30 pm
MON	PS2 gvsr	PEL1 mhvr	ITCA		LUNCH BREAK	ITCA		Student Activities/ Remidial Classes/ Minor Degree/ Honours Degree/ GATE
TUE	NTSS ban	ICMP avsr	ITCA			ITCA		
WED	OE1	COI avp	PS2 gvsr	ICMP avsr		EMC2(P)/PLC(P)/ICMP(P) gvsr/gs/wy		
THU	ICMP avsr	PS2 gvsr	EMC2(P)/PLC(P)/ICMP(P) gvsr/gs/wy			PEL1 mhvr	OE1	
FRI	PEL1 mhvr	NTSS ban	EMC2(P)/PLC(P)/ICMP(P) gvsr/gs/wy			COI/Mentoring avp	OE1	
SAT	COI avp	ICMP avsr	NTSS ban	PEL1 mhvr		PS2 gvsr	NTSS ban	

TEA BREAK 10.10 am to 10.40 am **LUNCH BREAK: 12.20 pm to 1.20 pm**

Theory:

Power Electronics-I
Power Systems-II
Integrated Circuits and Microprocessors
Network Theory and Signals & Systems
Open Elective-I
Constitution of India

Practicals

Electrical Machines-II
Integrated Circuits and Microprocessors
Programmable Logic Controllers

Faculty Member:

mhvr	Dr. M. Harsha Vardhan Reddy
gvsr	Dr. G.V. Subba Reddy
avsr	Sri A. V. Subba Reddy
ban	Sri. B. Amarnath Naidu
avp	Sri Anjaneya Vara Prasad

Faculty Member:

gvsr	Dr. G.V. Subba Reddy
wy	Smt. W. Yasmeen
gs	Dr. G. Sateesh

Prepared by: Dr. G. Sreenivasa Reddy **Verfied by: HOD (E.E.E.)** **Approved by: PRINCIPAL**

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - VII semester - Section A (Scheme-20) **LECTURE HALL : EEE-5**

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10 am	10.40 am - 11.30 am	11.30 am - 12.20 pm	12.20 pm - 1.20 pm	1.20 pm - 2.10 pm	2.10 pm - 3.00 pm	3.00 pm - 4:30 pm
MON	PSOC yvsr	AIOT(P)/PROJECT ban/jd			LUNCH BREAK	EDSP apky	PSOC yvsr	Student Activities/ Remidial Classes/ Minor Degree/ Honours Degree/ GATE
TUE	UEP bvak	A&C gs	PSOC yvsr	EDSP apky		UHV2 nrr	SGT gsr	
WED	EDSP apky	AIOT(P)/PROJECT ban/vp				UEP bvak	UHV2 vrr	
THU	SGT gsr	A&C gs	EDSP apky	PSOC yvsr		UHV2 nrr	SGT gsr	
FRI	PSOC yvsr	UEP bvak	Project sak/vp			A&C gs	EDSP apky	
SAT	A&C gs	UEP bvak	SGT gsr	Project/Mentoring sak		ITCA		

TEA BREAK 10.10 am to 10.40 am **LUNCH BREAK: 12.20 pm to 1.20 pm**

Theory:

PE3	Power System Operation & Control
PE4	Elements of Digital Signal Processing
PE5	Utilization of Electrical Power
OE3	Smart Grid Technologies
OE4	Automation & Control
	Universal Hun Universal Human Values 2

Practicals

Applications of IOT

Faculty Member:

yvsr	Dr. Y.V. Siva Reddy
apky	Sri A. Pradeep Kumar Yadav
bvak	Dr. B. V. Arun Kumar
gsr	Dr. G. Sreenivasa Reddy
gs	Dr. G. Sateesh
nrr	Dr. N. Ravishankar Reddy

Faculty Member:

ban	Sri B. Amarnath Naidu

Prepared by: Dr. G. Sreenivasa Reddy **Verfied by: HOD (E.E.E.)** **Approved by: PRINCIPA**

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TIME TABLE for the Academic year 2024-2025 with effect from 07-08-2024

CLASS : B.Tech - VII semester - Section B (Scheme-20) **LECTURE HALL : EEE-6**

Day / Time	08.30 am - 9.20 am	9.20 am - 10.10 am	10.40 am - 11.30 am	11.30 am - 12.20 pm	12.20 pm -1.20 pm	1.20 pm - 2.10 pm	2.10 pm - 3.00 pm	3.00 pm - 4:30 pm
MON	EDSP jd	A&C gs	UHV2 nrr	UEP bvak	LUNCH BREAK	SGT gsr	Project/Mentoring gh/mb	Student Activities/ Remidial Classes/ Minor Degree/ Honours Degree/ GATE
TUE	UHV2 nrr	AIOT(P)/PROJECT bvak/jd/mhvr				A&C gs	PSOC vp	
WED	PSOC vp	UHV2 nrr	SGT gsr	A&C gs		EDSP jd	PSOC vp	
THU	UEP bvak	AIOT(P)/PROJECT bvak/avkr				SGT gsr	EDSP jd	
FRI	PSOC vp	EDSP jd	UEP bvak	PSOC vp		PROJECT gh/mb		
SAT	EDSP jd	SGT gsr	A&C gs	UEP bvak		ITCA		

TEA BREAK 10.10 am to 10.40 am **LUNCH BREAK: 12.20 pm to 1.20 pm**

Theory:

PE3	Power System Operation & Control
PE4	Elements of Digital Signal Processing
PE5	Utilization of Electrical Power
OE3	Smart Grid Technologies
OE4	Automation & Control
	Universal I Universal Human Values 2

Practicals

Applications of IOT

Faculty Member:

vp	Sri Vannurappa
jd	Sri Jagadeesh
bvak	Dr. B.V. Arun Kumar
gsr	Dr. G. Sreenivasa Reddy
gs	Dr. G. Sateesh
nrr	Dr. N. Ravishankar Reddy

Faculty Member:

bvak	Dr. B.V. Arun Kumar

Prepared by: Dr. G. Sreenivasa Reddy **Verfied by: HOD (E.E.E.)** **Approved by: PRINCIPAL**