

GPREC-F / CFD / TIME TABLE

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
 TIME TABLE for the Academic year 2023-2024 with effect from 18-04-2024
 VLSI & Embedded Systems(VLSI & ES)

CLASS : M.Tech - II semester(Scheme-2022)

LECTURE HALL: PG4

Day / Time	08.15 am -9.00 am	9.00 am -9.45am	9.45am -10.15am	10.15am-11.00am	11.00am - 11.45pm	11.45am - 12.30pm	12.30 pm - 1.15pm
MON	ESIOT smsd	AID kr	B R E A K F A S T	CVD mlm	AESL Lab smsd,mmsr		
TUE	ESIOT smsd	VDFT mmsr		MESD wy	AEDAL Lab mmsr,gvrs		
WED	MESD wy	VDFT mmsr		ESIOT smsd	CVD mlm	AID kr	AID kr
THU	CVD mlm	VDFT mmsr		MESD wy			
FRI	SMY gs			LIB			
SAT							

NOTE: BREAK FAST: 9.45 am TO 10:15 am

	Theory:	Faculty Member:
1	AID	Sri K. Raju Dr .S.M.Shamsheer Daula
2	ESIOT	
3	CAD for VLSI Design (Professional Elective-III)	Smt M. Lalitha Meghana
4	MESD	Smt W.Yasmeen
5	VDFT	Dr. .M.Madhu Sudhan Reddy
6	SMY	Dr G. Shankaraiah

Practicals

8	AEDA	Advanced Electronic Design Automation Lab	-----	Dr.M.Madhu Sudhan Reddy, Dr G.V.R Sagar
9	AESL	Embeded IOT Lab	-----	Dr .S.M.Sham Dr.M.Madhu Sudhan Reddy

Prepared by : Sri B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL