

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS : B.Tech - IV semester - Section A(Scheme-23)

LECTURE HALL : ECE - 1

Day / Time	9.00 am - 9.50am	9.50 am - 10.40am	11.00am - 11.50pm	11.50pm - 12.40pm	12.40 pm - 1.40 pm	1.40 pm - 2.30pm	2.30 pm - 3.20pm	3.20 pm to 5:00 pm
MON	MEFA kap	ECA llpk	ADC gvrs	ECA(P)/ADC(P)	LUNCH BREAK	ECA(P)/ADC(P) llpk-ssb,bsr/gvrs-ts,nk		Student activitites/ Remedial classes
TUE	EMTL ksr	ECA llpk	SOFT SKILLS aj,ys			CS bvak	ADC gvrs	Student activitites/ Remedial classes
WED	SOFT SKILLS aj,ys	CS bvak	EMTL ksr	MEFA kap		MEFA kap	IEI/MENTORING sv	Student activitites/ Remedial classes
THU	ES kskr	EMTL ksr	CS(T) bvak	EMTL(T) ksr		CS(T) bvak	ECA(T) llpk	Student activitites/ Remedial classes
FRI	ECA llpk	EMTL ksr	ADC(T) gvrs	ECA(T) llpk		ADC(T) gvrs	MEFA(T) kap	Student activitites/ Remedial classes
SAT	ECA(P)/ADC(P) llpk-pspr,kld/gvrs-pak,tsb			ADC gvrs		ES kskr	CS bvak	Student activitites/ Remedial classes

TEA BREAK 10.40 am to 11.00am

LUNCH BREAK: 12.40pm to 1.40pm

Theory:

1	MEFA	Managerial Economics and Financial Analysis	---
2	CS	Control Systems	---
3	EMTL	EM Waves and Transmission Lines	---
4	ECA	Electronic Circuit Analysis	---
5	ADC	Analog and Digital Communications	---

kap	Dr. K. Aparna
bvak	Dr B.V. Arun Kumar
ksr	Dr K. Suresh Reddy
llpk	Dr.L.L.Prasanna Kumar
gvrs	Dr G.V R Sagar

Practicals:

6	ECA(P)	Electronic Circuit Analysis Lab	---
7	ADC(P)	Analog and Digital Communications Lab	---
8	SS(P)	Soft Skills Lab	---
9	ES	Environmental Science(Audit Course)	---

llpk	Dr.L.L.Prasanna Kumar, Dr.S.Saheb Basha Sri.B.Siva Reddy ,Sri.P.Surya Prakash Reddy Smt.K.lakshmi Devi
gvrs	Dr.G V R Sagar,Dr.P.Abdul Khayum Dr.T Sarath Babu,Dr.T.Swathi Smt.N.Kavitha
aj,ys	Smt A.Jerusha,Smt Y.Sumalatha
kskr	Sri K. Sharath Kumar Reddy

Prepared by : Sri B. Siva Reddy

Verified by: HOD-ECE

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS : B.Tech - IV semester - Section B(Scheme-23)

LECTURE HALL : ECE - 2

Day / Time	9.00 am - 9.50am	9.50 am - 10.40am	11.00am - 11.50pm	11.50pm - 12.40pm	12.40 pm - 1.40 pm	1.40 pm - 2.30pm	2.30 pm - 3.20pm	3.20 pm to 5:00 pm
MON	ADC _{ts}	CS _{nrr}	ES _{yrk}	MEFA _{kāp}	LUNCH BREAK	ECA _{snr}	EMTL _{mrvv}	Student activitites/ Remedial classes
TUE	ECA _{snr}	ECA(P)/ADC(P) bsr-ssb,pak/ts-kuk,kr				SOFT SKILLS aj,ys		Student activitites/ Remedial classes
WED	EMTL _{mrvv}	ECA(P)/ADC(P) bsr-md,kr/ts-kuk,sv				EMTL _{mrvv}	IEI/MENTORING kr	Student activitites/ Remedial classes
THU	CS _{nrr}	SOFT SKILLS aj,ys	MEFA(T) kap	CS(T) nrr		ADC(T) ts	ECA(T) snr	Student activitites/ Remedial classes
FRI	MEFA _{kap}	ADC _{ts}	CS(T) nrr	ADC(T) ts		ECA(T) snr	EMTL(T) mrvv	Student activitites/ Remedial classes
SAT	ES _{yrk}	EMTL _{mrvv}	CS _{nrr}	ADC _{ts}		ECA _{snr}	MEFA _{kap}	Student activitites/ Remedial classes

TEA BREAK 10.40 am to 11.00am

LUNCH BREAK: 12.40pm to 1.40pm

Theory:

- | | | | |
|---|------|---|-----|
| 1 | MEFA | Managerial Economics and Financial Analysis | --- |
| 2 | CS | Control Systems | --- |
| 3 | EMTL | EM Waves and Transmission Lines | --- |
| 4 | ECA | Electronic Circuit Analysis | --- |
| 5 | ADC | Analog and Digital Communications | --- |

Practicals:

- | | | | |
|---|--------|---------------------------------------|-----|
| 6 | ECA(P) | Electronic Circuit Analysis Lab | --- |
| 7 | ADC(P) | Analog and Digital Communications Lab | --- |
| 8 | SS(P) | Soft Skills Lab | --- |
| 9 | ES | Environmental Science(Audit Course) | --- |

Faculty Member:

- | | |
|-------|------------------------------------|
| kāp | Dr. K. Aparna |
| nrr | Dr N. Ravishankar Reddy |
| mrvv | Dr M.V. R.Vittal |
| snr | Dr.S.Nagaraja rao |
| ts | Dr. T.Swathi |
| bsr | Sri.B.Siva Reddy,Dr.P.Abdul Khayum |
| | Dr.S.Saheb basha,Sri.M.Devendra |
| | Sri.K.Raju |
| ts | Dr.T.Swati,Smt.K.Udaya kiran |
| | Dr.S.Vyshali,Sri.K.Raju |
| aj,ys | Smt A.Jerusha,Smt Y.Sumalatha |
| yrk | Sri.Y.Ravi Kumar |

Prepared by : Sri B. Siva Reddy

Verified by: HOD-ECE

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS : B.Tech - IV semester - Section C(Scheme-23)

LECTURE HALL : ECE - 3

CLASS : B.Tech - IV semester - Section C(Scheme-23)					LECTURE HALL : ECE - 5			
Day / Time	9.00 am -9.50am	9.50 am - 10.40am	11.00am -11.50pm	11.50pm -12.40pm	12.40pm - 1.40pm	1.40 pm -2.30pm	2.30 pm - 3.20pm	3.20 pm to5:00 pm
MON	ECA(P)/ADC(P) pspr-md,kuk/mvr-v-pak,pan			EMTL pcpk	LUNCH BREAK	ECA pspr	MEFA kap	Student activitites/ Remedial classes
TUE	ECA pspr	ADC mvr-v	ES yrk	EMTL pcpk		ADC mvr-v	MEFA kap	Student activitites/ Remedial classes
WED	CS kr	ECA pspr	SOFT SKILLS aj,ys			EMTL pcpk	IEI/MENTORING grk	Student activitites/ Remedial classes
THU	ADC mvr-v	MEFA kap	ECA(T) pspr	CS(T) kr		ADC(T) mvr-v	ECA(T) pspr	Student activitites/ Remedial classes
FRI	ES yrk	CS kr	ADC(T) mvr-v	MEFA(T) kap		CS(T) kr	EMTL(T) pcpk	Student activitites/ Remedial classes
SAT	EMTL pcpk	SOFT SKILLS aj,ys	CS kr	ECA(P)/ADC(P)		ECA(P)/ADC(P) pspr-kuk,nk/mvr-v-kld,msn		Student activitites/ Remedial classes

TEA BREAK 10.40 am to 11.00am

LUNCH BREAK: 12.40pm to 1.40pm

Theory:

- | | | | | | | | |
|---|------|---|-----|-----|-----|-----|-----|
| 1 | MEFA | Managerial Economics and Financial Analysis | --- | --- | --- | --- | --- |
| 2 | CS | Control Systems | --- | --- | --- | --- | --- |
| 3 | EMTL | EM Waves and Transmission Lines | --- | --- | --- | --- | --- |
| 4 | ECA | Electronic Circuit Analysis | --- | --- | --- | --- | --- |
| 5 | ADC | Analog and Digital Communications | --- | --- | --- | --- | --- |

Practicals:

- | | | | | | | | |
|---|--------|---------------------------------------|-----|-----|-----|-----|-----|
| 6 | ECA(P) | Electronic Circuit Analysis Lab | --- | --- | --- | --- | --- |
| 7 | ADC(P) | Analog and Digital Communications Lab | --- | --- | --- | --- | --- |
| 8 | SS(P) | Soft Skills' Lab | --- | --- | --- | --- | --- |
| 9 | ES | Environmental Science(Audit Course) | --- | --- | --- | --- | --- |

kap
kr
pcpk
pspr
mvr-v

Faculty Member:

Dr. K. Aparna
Dr K. Rajesh
Dr. P.C. Praveen Kumar
Sri P Surya Prakash Reddy
Dr M.V. R.Vittal

pspr

mvr-v

aj,ys
yrk

Sri.P.Surya Prakash Reddy
Sri.M.Devendra, Smt K. Uday Kiran
Smt.N. Kavitha

Dr.M.V R Vittal, Dr.P.Abdul Khayum
Dr.P.Anil Kumar, Smt.K.lakshmi Devi
Smt.M.Sameena Nazeer

Smt A.Jerusha,Smt Y.Sumalatha
Sri.Y.Ravi Kumar

Prepared by : Sri B. Siva Reddy

K. S. Reddy
Verified by: HOD-ECE

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS :B.Tech - VI semester - Section A(Scheme-23)

LECTURE HALL : ECE - 4

Day / Time	9.00 am -9.50am	9.50 am -10.40am	11.00am -11.50pm	11.50pm -12.40pm	12.40 pm -1.40 pm	1.40 pm - 2.30pm	2.30 pm -3.20pm	3.20 pm to 5:00 pm
MON	DSP drj	ES&IOT drs	DSP(T) drj	VLSID(T) mmsr	LUNCH BREAK	MWOC(T) drj	ES&IOT(T) drs	Student activitites/ Remedial classes
TUE	OE-II	MWOC pcpk	DSP(T) drj	ES&IOT(T) drs		VLSID(T) mmsr	MWOC(T) pcpk	Student activitites/ Remedial classes
WED	ES&IOT drs	DSJ ksr	MWOC(P)/VLSID(P)/ML&DSP(P) pcpk-dvsb,srb/dro-gvrs,nk/llpk-drj,tsb			DSP drj	VLSID mmsr	Student activitites/ Remedial classes
THU	MWOC(P)/VLSID(P)/ML&DSP(P) pcpk-md,grk/dro-ap,wy/llpk-tsb,drj		ES&IOT drs	DSP drj		MWOC pcpk	OE-II	Student activitites/ Remedial classes
FRI	MWOC pcpk	TP & IPR kr	DSJ ksr	IEI/MENTORING sv		ML& DSP llpk	OE-II	Student activitites/ Remedial classes
SAT	VLSID mmsr	TP & IPR kr	VLSID mmsr	DSJ ksr		MWOC(P)/VLSID(P)/ML&DSP(P) pcpk-drs,pan/dro-mmsr,ivrr/llpk-wy,kr		Student activitites/ Remedial classes

TEA BREAK 10.40 am to 11.00am

LUNCH BREAK: 12.40pm to 1.40pm

Theory:

- | | | | |
|---|--------|---------------------------------------|------|
| 1 | DSP | Digital Signal Processing | drj |
| 2 | MWOC | Microwave and Optical Communications | pcpk |
| 3 | VLSID | VLSI Design | mmsr |
| 4 | ES&IOT | Embedded Systems & IOT-(PE -II) | drs |
| 5 | DSJ | Data Structures through JAVA-(PE-III) | ksr |
| 6 | OE | Open Elective Course-II(OEC-II) | |
| 7 | ML&DSP | Machine Learning and DSP | llpk |

- | | | | |
|---|--------|-------------------------------|----|
| 8 | TP&IPR | Technical Paper Writing & IPR | kr |
|---|--------|-------------------------------|----|

Practicals:

- | | | | |
|----|----------|--|------|
| 9 | MWOC(P) | Microwave and Optical Communications Lab | pcpk |
| 10 | VLSID(P) | VLSI Design Lab | dro |

Faculty Member:

Dr D. Rajasekhar
 Dr P.C.Praveen Kumar
 Dr M. Madhusudhan Reddy
 Dr D R Srinivas
 Dr K. Srinivasa Rao

 Dr.L.L.Prasanna Kumar,Dr D. Rajasekhar,
 Dr T. Sarath Babu,Smt W.Yasmeen, Sri K. Raju

 Sri K. Raju

Dr P.C.Praveen Kumar, Dr D R Srinivas,
 Dr D V Sri Hari Babu, Sri S. Ramesh babu
 Sri M Devendra, Dr G Rajesh Krishna,
 Dr P. Anil Kumar

Smt D. Rohini, Dr GVR Sagar,
 Dr M. Madhusudhan Reddy, Smt A.Parvathi,
 Sri I.V. Rameswar Reddy,Smt N Kavitha

Prepared by : Sri B. Siva Reddy

Verified by: HOD-ECE

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS: B.Tech - VI semester - Section B (Scheme-23)

LECTURE HALL :ECE-5

Day / Time	9.00 am - 9.50am	9.50 am - 10.40am	11.00am - 11.50pm	11.50pm - 12.40pm	12.40 pm - 1.40 pm	1.40 pm - 2.30pm	2.30 pm - 3.20pm	3.20 pm to 5:00 pm
MON	DSP snr	DSJ ksr	MWOC(T) jstt	DSP(T) snr	LUNCH BREAK	ESIOT(T) smsd	VLSI(T) gdp	Student activities/ Remedial classes
TUE	OE-II	MWOC jstt	VLSI(T) gdp	DSP(T) snr		ESIOT(T) smsd	MWOC(T) jstt	Student activities/ Remedial classes
WED	VLSI gdp	DSP snr	ESIOT smsd	DSJ ksr		TP & IPR grk	MWOC jstt	Student activities/ Remedial classes
THU	MWOC jstt	ESIOT smsd	MWOC(P)/VLSID(P)/ML&DSP(P) jstt-bsr,kld/gdp-wy,grk/rsb-gak,pan			DSJ ksr	OE-II	Student activities/ Remedial classes
FRI	MWOC(P)/VLSID(P)/ML&DSP(P) jstt-bsr,dvsb/gdp-gvrs,wy/rsb-drs,gak		VLSI gdp	IEI/MENTORING kr		TP & IPR grk	OE-II	Student activities/ Remedial classes
SAT	ML&DSP rsb	DSP snr	MWOC(P)/VLSID(P)/ML&DSP(P) jstt-grk,sv/gdp-dro,ap/rsb-drj,gak			ESIOT smsd	VLSI gdp	Student activities/ Remedial classes

TEA BREAK 10.40 am to 11.00am

LUNCH BREAK: 12.40pm to 1.40pm

Theory:

Faculty Member:

- DSP Digital Signal Processing
- MWOC Microwave and Optical Communications
- VLSID VLSI Design
- ESIOT Embedded Systems & IOT-(PE-II)
- DSJ Data Structures through JAVA-(PE-III)
- OE Open Elective Course-II(OEC-II)
- ML&DSP Machine Learning and DSP

snr
jstt
gdp
smsd
ksr

Dr S.Nagaraja Rao
Dr J.S.T.Thillagam
Smt G. Divya Praneetha
Dr S.M.Shamsheer Daula
Dr K. Srinivasa Rao

- TP&IPR Technical Paper Writing & IPR

grk

Dr R. Sudheer Babu, Dr G.Amjad Khan,
Dr R Srinivas,Dr D. Rajasekhar,
Dr P.Anil Kumar,
Dr G. Rajesh Krishna

Practicals:

- MWOC(P) Microwave and Optical Communications Lab
- VLSID(P) VLSI Design Lab

jstt

Dr J.S.T.Thillagam,Sri B. Siva Reddy,
Dr D V Srihari Babu, Dr S. Vyshali,
Smt.K.lakshmi Devi,Dr G. Rajesh Krishna

gdp

Smt G. Divya Praneetha, Dr GVR Sagar,
Smt W.Yasmeen, Smt D Rohini,
Smt.A. Parvathi, Dr G. Rajesh Krishna.

Prepared by : Sri B. Siva Reddy

Verified by: HOD-ECE

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS: B.Tech - VI semester - Section C (Scheme-23)

LECTURE HALL :ECE-6

Day / Time	9.00 am -9.50am	9.50 am -10.40am	11.00am -11.50pm	11.50pm -12.40pm	12.40 pm - 1.40 pm	1.40 pm -2.30pm	2.30 pm -3.20pm	3.20 pm to 5:00 pm
MON	MWOC(P)/VLSID(P)/ML&DSP(P) cr-jstt,dvsb/srb-kr,mmsr/gak-tsb,smsd		ESIOT(T) krk	DSP(T) rsb	LUNCH BREAK	MWOC(T) cr	VLSID(T) srb	Student activities/ Remedial classes
TUE	OE-II	DSJ mfz	VLSID(T) srb	ESIOT(T) krk		DSP(T) rsb	MWOC(T) cr	Student activities/ Remedial classes
WED	DSJ mfz	DSP rsb	MWOC cr	TP & IPR jstt		MWOC(P)/VLSID(P)/ML&DSP(P) cr-drs,pan/srb-ap,msn/gak-avsr,wy		Student activities/ Remedial classes
THU	MWOC cr	ML&DSP gak	ESIOT krk	VLSID srb		DSP rsb	OE-II	Student activities/ Remedial classes
FRI	VLSID srb	VLSID srb	DSP rsb	IEI/MENTORING grk		ESIOT krk	OE-II	Student activities/ Remedial classes
SAT	MWOC(P)/VLSID(P)/ML&DSP(P) cr-md,msn/srb-dro,ivrr/gak-wy,grk		DSJ mfz	ESIOT krk		MWOC cr	TP & IPR jstt	Student activities/ Remedial classes

TEA BREAK 10.40 am to 11.00am

LUNCH BREAK: 12.40pm to 1.40pm

Theory:

- | | | | |
|---|--------|---------------------------------------|-----|
| 1 | DSP | Digital Signal Processing | rsb |
| 2 | MWOC | Microwave and Optical Communications | cr |
| 3 | VLSID | VLSI Design | srb |
| 4 | ESIoT | Embedded Systems & IOT-(PE-II) | krk |
| 5 | DSJ | Data Structures through JAVA-(PE-III) | mfz |
| 6 | OE | Open Elective Course-II(OEC-II) | |
| 7 | ML&DSP | Machine Learning and DSP | gak |

Faculty Member:

Dr R. Sudheer Babu
Smt C. Rajeswari
Sri S. Ramesh Babu
Sri K. Ramakrishna
Sri MD Fayaz

- | | | | |
|---|--------|-------------------------------|------|
| 8 | TP&IPR | Technical Paper Writing & IPR | jstt |
|---|--------|-------------------------------|------|

Dr G. Amjad Khan, Dr S M Shamsheer Daula
Dr T. Sarth Babu, Sri A.V Subba Reddy,
Smt W. Yasmeen,Dr G.Rajesh Krishna
Dr J.S.T.Thillagam

Practicals:

- | | | | |
|----|----------|--|-----|
| 9 | MWOC(P) | Microwave and Optical Communications Lab | cr |
| 10 | VLSID(P) | VLSI Design Lab | srb |

Smt C. Rajeswari, Dr. J S T Thilagam,
Dr D V Sri Hari Babu, Dr P. AnilKumar,
Dr D R Srinivas, Sri M. Devendra,
Smt M. Sameena Nazeer

Sri S. Ramesh Babu, Sri K.Raju,
Dr M.Madhu Sudhan Reddy,Smt D. Rohini,
Sri I V Rameswar Reddy,Smt M. Sameena Nazeer

Prepared by : Sri B. Siva Reddy

Verified by: HOD -ECE

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS : B.Tech - IV Semester - Honors in ECE(Scheme-23)

Lecture Hall :ECE-3

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 4:10 pm	4:10 pm to 5:00 pm
MON					LUNCH BREAK			AICD ivrr	
TUE									
WED								AICD ivrr	
THU									
FRI								AICD ivrr	
SAT									

Theory:

1 AICD Analog IC Design

Faculty Member:

Sri I.Venkata Rameswar Reddy

Prepared by : B. Siva Reddy

Verified by: HOD -ECE

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS : B.Tech - VI Semester - Honors in ECE(Scheme-23)

Lecture Hall :ECE-4

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 4:10 pm	4.10 pm to 5.00 pm	6:00 pm to 7:00 pm (Online mode)
MON					LUNCH BREAK					TV ap
TUE										ALPV wy
WED										TV ap
THU										ALPV wy
FRI								PDA Lab mmsr		TV ap
SAT								PDA Lab mmsr		ALPV wy

Theory:

- 1 ALPV Advanced Low Power VLSI
- 2 TV Testing & Verification
- 3 PDA(P) Physical Design & Automation Lab

wy
ap
mmsr

Faculty Member:

Smt W.Yasmeen
Smt A. Parvathi
Dr M. Madhu Sudhan Reddy

Prepared by : B. Siva Reddy

Verified by: HOD -ECE

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS : B.Tech - IV semester - Minor in Embedded Systems & IOT (EIOT)-(Scheme-23)

Lecture Hall :ECE-1

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 4:10 pm	4:10 pm to 5:00 pm
MON					LUNCH BREAK				
TUE									
WED								EST krk	
THU								EST krk	
FRI								EST krk	
SAT									

Theory:

Faculty Member:

1 EST Embedded SystemsTechnology(EST)

krk

Sri K. Ramakrishna

Prepared by : B. Siva Reddy

Verified by: HOD-ECE

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2025-2026 with effect from 01-12-2025

CLASS : B.Tech - IV semester - Minor in INDUSTRIAL ROBOTICS(Scheme-23)

Lecture Hall :ECE-2

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 4:10 pm	4:10 pm to 5:00 pm
MON					LUNCH BREAK				
TUE									
WED								IR gr	
THU								IR gr	
FRI								IR gr	
SAT									

Theory:

1 ITR Introduction to Robotics

Faculty Member:

Dr.G. Ramesh

Prepared by : B. Siva Reddy

Verified by: HOD -ECE

Approved by :PRINCIPAL