

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2021-22 with effect from 04-10-2021

CLASS : B.Tech - VII semester - Section A

LECTURE HALL : ECE - 7

Day / Time	09.30 am - 10.20 am	10.20 am - 11.10 am	11.30 am - 12.20 pm	12.20 pm - 1.10 pm	1.10 pm - 2.00 pm	2.00 pm - 2.50 pm	2.50 pm - 3.40 pm	3.40 pm - 4.30 pm
MON	DSDV DSBR	MWE LLPK	DIP PAK	FOC TSB	LUNCH BREAK	MWC(P) / DSD(P) Lab llpk/dsbr		
TUE	DIP PAK	BME SV	FOC TSB	MENTORING		VSP MMSR	DSDV DSBR	LIBRARY
WED	BME SV	MWC(P) / DSD(P) Lab llpk/dsbr				DSDV DSBR	MWE LLPK	LIBRARY
THU	FOC TSB	VSP MMSR	BME SV	LIBRARY		Miniproject 2 knkr		
FRI	MWE LLPK	VSP MMSR	DIP PAK	LIBRARY		Miniproject 2 knkk		
SAT	LIBRARY	LIBRARY	Minor Degree Course			Minor Degree Course		LIBRARY

NOTE: TEA BREAK 11.10 am TO 11.30am

Revision : 05

Theory:

- 1 EC401 Microwave Engineering (MWE)
- 2 EC402 Digital System Design using Verilog (DSDV)
- 3 ECEL9 Professional Elective –III (Digital Image Processing(DIP))
- 4 ECEL10 Professional Elective –IV(Fiber Optic Communications(FOC))
- 5 ECOE2 Open Elective-III (Bio-Medical Electronics(BME))
- 6 ECOE4 Open Elective-IV (VLSI Signal Processing(VSP))

Practicals

- 7 EC403 Microwave Communications Lab(MWC(P))
- 8 EC404 Digital System Design Lab (DSD(P))
- 9 ECMP2 Mini Project2

Faculty Member:

- | | |
|------|-------------------------------|
| LLPK | Sri. L Lakshmi Prasanna Kumar |
| DSBR | Sri. D Sarath Babu Rao |
| PAK | Dr. P Abdul Khayum |
| TSB | Sri. T Sharath Babu |
| SV | Dr. S. Vyshali |
| MMSR | Sri M Madhusudhan Reddy |
| LLPK | Sri. L Lakshmi Prasanna Kumar |
| DSBR | Sri. D Sarath Babu Rao |
| KNRK | Sri. K Nagakoushil Reddy |

Prepared by : Verified by :Sri L. Laxmi Prasanna Kumar

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2021-22 with effect from 04-10-2021

CLASS : B.Tech - VII semester - Section B

LECTURE HALL : ECE - 8

Day / Time	09.30 am - 10.20 am	10.20 am - 11.10 am	11.30 am - 12.20 pm	12.20 pm -1.10 pm	1.10 pm - 2.00 pm	2.00 pm - 2.50 pm	2.50 pm - 3.40 pm	3.40 pm - 4.30 pm
MON	DIP RSB	MWE JSTT	VSP DSBR	LIBRARY	LUNCH BREAK	Miniproject 2 gdp		
TUE	BME TSB	VSP DSBR	DSDV PBS	MENTORING		MWC(P) / DSD(P) Lab jstt/pbs		
WED	MWE JSTT	BME TSB	FOC DVSHB	DIP RSB		Miniproject 2 gdp		
THU	VSP DSBR	MWC(P) / DSD(P) Lab jstt/pbs				DSDV PBS	FOC DVSHB	LIBRARY
FRI	DSDV PBS	DIP RSB	FOC DVSHB	LIBRARY		MWE JSTT	BME TSB	LIBRARY
SAT	LIBRARY	LIBRARY	Minor Degree Course			Minor Degree Course		LIBRARY

NOTE: TEA BREAK 11.10 am TO 11.30am

Revision : 05

Theory:

- EC401 Microwave Engineering (MWE)
- EC402 Digital System Design using Verilog (DSDV)
- ECEL9 Professional Elective –III (Digital Image Processing(DIP))
- ECEL10 Professional Elective –IV(Fiber Optic Communications(FOC))
- ECOE2 Open Elective-III (Bio-Medical Electronics(BME))
- ECOE4 Open Elective-IV (VLSI Signal Processing(VSP))

Practicals

- EC403 MicroWave Communications Lab(MWC(P))
- EC404 Digital System Design Lab (DSD(P))
- ECMP2 Mini Project2

Faculty Member:

- | | |
|-------|------------------------|
| JSTT | Dr. J S T Thillagam |
| PBS | Smt. P Bindu Swetha |
| RSB | Dr. R Sudheer Babu |
| DVSHB | Dr. D V Sri Hari Babu |
| TSB | Sri. T Sharath Babu |
| DSBR | Sri. D Sarath Babu Rao |
| JSTT | Dr. J S T Thillagam |
| PBS | Smt. P Bindu Swetha |
| GDP | Smt G. Divya Praneetha |

Prepared by Sri L. Laxmi Prasanna Kumar

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2021-22 with effect from 04-10-2021

CLASS : B.Tech - VII semester - Section C

LECTURE HALL : ECE - 9

Day / Time	09.30 am - 10.20 am	10.20 am - 11.10 am	11.30 am - 12.20 pm	12.20 pm - 1.10 pm	1.10 pm - 2.00 pm	2.00 pm - 2.50 pm	2.50 pm - 3.40 pm	3.40 pm - 4.30 pm
MON	VSP KR	BME MVRV	DSDV DLC	FOC KCR	LUNCH BREAK	DIP KUK	MWE CR	LIBRARY
TUE	MWE CR	FOC KCR	VSP KR	MENTORING		Miniproject 2 kcr		
WED	FOC KCR	DSDV DLC	BME MVRV	LIBRARY		MWC(P) / DSD(P) Lab bsrt/ttr		
THU	BME MVRV	Miniproject 2 kcr				MWE CR	DIP KUK	LIBRARY
FRI	DIP KUK	MWC(P) / DSD(P) Lab bsrt/ttr				VSP KR	DSDV DLC	LIBRARY
SAT	LIBRARY	LIBRARY	Minor Degree Course			Minor Degree Course		LIBRARY

NOTE: TEA BREAK 11.10 am TO 11.30am

Revision : 05

Theory:

- EC401 Microwave Engineering (MWE)
- EC402 Digital System Design using Verilog (DSDV)
- ECEL9 Professional Elective –III (Digital Image Processing(DIP))
- ECEL10 Professional Elective –IV(Fiber Optic Communications(FOC))
- ECOE2 Open Elective-III (Bio-Medical Electronics(BME))
- ECOE4 Open Elective-IV (VLSI Signal Processing(VSP))

Practicals

- EC403 MicroWave Communications Lab(MWC(P))
- EC404 Digital System Design Lab (DSD(P))
- ECMP2 Mini Project2

Faculty Member:

- | | |
|------|-------------------------|
| CR | Smt. C Rajeswari |
| DLC | Dr. D Lakshmi Chaitanya |
| KUK | Smt. K Udaya Kiran |
| KCR | Sri. K C Raghavendra |
| MVRV | Dr. M V R Vittal |
| KR | Sri K Raju |
| BSR | Sri. B Shiva Reddy |
| TTR | Sri. T Thammi Reddy |
| KCR | Sri. K C Raghavendra |

Prepared by : L.Laxmi Prasanna Kumar

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL