

G. PULLA REDDY ENGINEERING COLLEGE (AUTONOMOUS): KURNOOL

Department of Electronics and Communication Engineering

IEEE Communications society

IEEE Information Theory society

CIRCULAR

Date: 10/08/2023

The Department of Electronics and Communication Engineering ,IEEE Communications society and IEEE Information Theory society in association with IEEE Sensors council and MTT Society is organizing a Five Day National Level workshop “**SILICON TO SYSTEM DESIGN**” during 18th – 23rd August 2023.This workshop includes discussion on various topics such as Physical Design, Analog Design, Digital Design, Design For Testability, High speed digital circuits and FinFET Technology. This workshop also conducts Hands on Session on various EDA Tools for students and Faculty. In this regard, it is requested to all the students and Faculty members to utilize the opportunity and get benefited. Registration details and Event Schedule are given below:

Registration Link: https://bit.ly/si_to_design

S.No	Event Name	Date	Time	Venue	Fee Details
1.	Workshop on SILICON TO SYSTEM DESIGN	18 th to 23 rd August 2023	FN - 10:00 AM to 1:00 PM AN - 2:00 PM to 4:00 PM	ECE BLOCK 1 VLSI LAB	Rs.400/- for IEEE Student members Rs.500/- for Non-IEEE student members Rs.700/- for IEEE Faculty members Rs.800/- for Non-IEEE Faculty/PG student/Reserch Scholar

Further Details Contact

Event Coordinators:

1. Sri M. Madhu Sudhan Reddy, Asst..Prof. Dept of ECE – 83320 28884.
2. Smt.D.Rohini , Asst.Prof,Dept of ECE – 7095667770.
3. Smt.G.Divya Praneetha, Asst.Prof,Dept of ECE – 89196 04265 .


PRINCIPAL

Enclosure

Brochure of the event

Copy to: All Department Main Notice board's

Sri.Rameshwar Reddy(Website)

Smt.D.Rohini, Faculty Advisor of IEEE Communications Society, ECE Dept.

Smt.G.Divya praneetha, Faculty Advisor of IEEE Information Theory Society, ECE Dept.

To be circulated among the students of B.Tech IV year of all branches.