

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - III semester - Section A (Scheme-23)

LECTURE HALL : ECE-I

CLASS : B.Tech - III semester - Section A (Scheme-23)					LECTURE HALL : ECE-1				
Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 5:00 pm	
MON	EDC md	SSSP ksr	DTI(P) tsb-dvsb,drj		LUNCH BREAK	UHV mrvv	PCV ekb	Student activities/ Remedial classes	
TUE	PCV ekb	EDC(P)/DCSS(P) md-llpk,grk/gvrs-dro,nkv				EDC md	SEMINAR pan	Student activities/ Remedial classes	
WED	EDC md	SSSP ksr	PCV ekb	DCD gvrs		Python Programming kr-drj,grk		Student activities/ Remedial classes	
THU	UHV mrvv	SSSP ksr	PCV ekb	EDC(P)/DCSS(P)		EDC(P)/DCSS(P) md-cr,dvsb/gvrs-dro,ap		Student activities/ Remedial classes	
FRI	SSSP ksr	PCV ekb	DCD gvrs	EDC md		UHV mrvv	DCD gvrs	Student activities/ Remedial classes	
SAT	PP kr	DCD gvrs	DTI tsb	MENTORING		MOOCS kld, pspr		Student activities/ Remedial classes	

TEA BREAK 10.40 am TO 11.00 am

LUNCH BREAK: 12.40 pm to 1.40 pm

Theory:

- BS204 Probability and Complex Variables(PCV)
- HSM201 Universal Human Values(UHV)
- EC201 Signals, Systems and Stochastic Processes(SSSP)
- EC202 Electronic Devices & Circuits (EDC)
- EC203 Digital Circuit Design (DCD)
- SCCM02 Python Programming (PP)
- DTI Design Thinking and Innovation (DTI)

- | | |
|-----|---|
| ekb | Dr.E. Kiran Babu |
| mvr | Dr M.V.R.Vittal |
| ksr | Dr K.Suresh Reddy |
| md | Sri M. Devendra |
| gvr | Dr G.V.R Sagar |
| kr | Sri K. Raju, D Rajasekhar, -G Rajesh Krishna |
| ts | Dr T. Sarath Babu, D V Srihaibabu, D.Rajasekhar |

Faculty Member:

Practicals

- EC204 Electronic Devices & Circuits Lab (EDC(P))
- EC205 Digital Circuits & Signal Simulation Lab (DCSS(P))
- MOOCS Massive Open Online Course(Digital Learning)

- | | |
|------|---|
| md | Sri M. Devendra, C Rajeswari, LL Prasanna Kumar, D V SriHariBabu, Grajesh krishna |
| gvrs | Dr G.V.R Sagar, D Rohini, A Parvathi, N Kavitha |
| kld | Smt K. Lakshmidevi , Sri P. Surya Prakash Reddy |

Faculty Member:

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by : PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS :

B.Tech - III semester - Section B (Scheme-23)

LECTURE HALL : ECE-2

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 5:00 pm
MON	SSSP snr	EDC(P)/DCSS(P) pspr-ssb,kld/ap-gvrs,nk			LUNCH BREAK	PCV gvr	MENTORING	Student activities/ Remedial classes
TUE	PCV gvr	SSSP snr	DTI(P) drj-drs,kr			UHV pcpk	SEMINAR gv	Student activities/ Remedial classes
WED	DTI drj	DCD ap	UHV pcpk	SSSP snr		EDC pspr	PCV gvr	Student activities/ Remedial classes
THU	DCD ap	SSSP snr	EDC pspr	PCV gvr		Python Programming avsr-grk,drj		Student activities/ Remedial classes
FRI	EDC(P)/DCSS(P) pspr-md,kuk/ap-krk,grk,			EDC pspr		PCV gvr	DCD ap	Student activities/ Remedial classes
SAT	EDC pspr	UHV pcpk	MOOCS gdp,gv			DCD ap	PP avsr	Student activities/ Remedial classes

TEA BREAK 10.40 am TO 11.00 am

LUNCH BREAK: 12.40 pm to 1.40 pm

Theory:

- BS204 Probability and Complex Variables(PCV)
- HSM201 Universal Human Values(UHV)
- EC201 Signals, Systems and Stochastic Processes(SSSP)
- EC202 Electronic Devices & Circuits (EDC)
- EC203 Digital Circuit Design (DCD)
- SCCM02 Python Programming (PP)
- DTI Design Thinking and Innovation (DTI)

Practicals

- EC204 Electronic Devices & Circuits Lab (EDC(P))
- EC205 Digital Circuits & Signal Simulation Lab (DCSS(P))
- MOOCS Massive Open Online Course(Digital Learning)

Faculty Member:

- gvr Sri G. Venkatarami Reddy
 pcpk Dr.P.C.Praveen Kumar
 sn Dr S. Nagaraja Rao
 pspr Sri P. Surya Prakash Reddy
 ap Smt A. Parvathi
 avsr Sri A. Venkata Subba Reddy- D Rajasekhar, G R;
 drj Dr D. Rajasekhar, D.R.Srinivas, K.Raju

Faculty Member:

- pspr Sri P. Surya Prakash Reddy, S Saheb Basha,
 K. Uday Kiran, K Lakshmi Devi, M. Devendra
 ap Smt A. Parvathi, G VRSagar, K Ramakrishna,
 G Rajesh Krishna, N Kavitha
 gdp Smt G.Divya Praneetha, Sri G. Venkatesh

Prepared by : B. Siva Reddy

K. S. Reddy
 Verified by: HOD (E.C.E.)

Approved by : PRINCIPAL

GPREC-F / CFD / TIME TABLE

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025**

CLASS : B.Tech - III semester - Section C(Scheme-23)

LECTURE HALL : ECE-3

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 5:00 pm	
MON	PCV kj	DTI kr	SSSP snr	PP cr	LUNCH BREAK	Python Programming cr-md,pspr		Student activities/ Remedial classes	
TUE	UHV drj	EDC drs	DCD gdp	SSSP snr		PCV kj	SEMINAR sv	Student activities/ Remedial classes	
WED	SSSP snr	EDC(P)/DCSS(P) llpk-pak,kuk/gdp-avsr,grk				DCD gdp	PCV kj	Student activities/ Remedial classes	
THU	DCD gdp	EDC drs	UHV drj	SSSP snr		DCD gdp	EDC drs	Student activities/ Remedial classes	
FRI	EDC drs	UHV drj	PCV kj	EDC(P)/DCSS(P) llpk/gdp		EDC(P)/DCSS(P) llpk-gak,nk/gdp-wy,mv		Student activities/ Remedial classes	
SAT	MOOCS ts,srb		DTI(P) kr			PCV kj	MENTORING	Student activities/ Remedial classes	

TEA BREAK 10.40 am TO 11.00 am

LUNCH BREAK: 12.40 pm to 1.40 pm

Theory:

- BS204 Probability and Complex Variables(PCV)
- HSM201 Universal Human Values(UHV)
- EC201 Signals, Systems and Stochastic Processes(SSSP)
- EC202 Electronic Devices & Circuits (EDC)
- EC203 Digital Circuit Design (DCD)
- SCCM02 Python Programming (PP)
- DTI Design Thinking and Innovation (DTI)

Practicals

- EC204 Electronic Devices & Circuits Lab (EDC(P))
- EC205 Digital Circuits & Signal Simulation Lab (DCSS(P))
- MOOCS Massive Open Online Course(Digital Learning)

Faculty Member:

- | | |
|-----|---------------------------------------|
| kj | Dr K. Jyothi |
| drj | Dr D. Rajasekhar |
| sn | Dr S. Nagaraja Rao |
| drs | Dr D R Srinivas |
| gdp | Smt G.Divya Praneetha |
| cr | Smt C. Rajeswari, M Devendra, P Surya |
| kr | Sri K. Raju, D R Srinivas |

Faculty Member:

- | | |
|------|--|
| llpk | Dr L.Lakshmi Prasanna Kumar-P Abdul kh |
| gdp | K Uday kiran, G Amjad Khan, N Kavitha |
| ts | Smt G.Divya Praneetha, W Yasmeen, |
| | A V Subba Reddy, M Venkatesh, G Rajesh |
| | Dr T. Swathi, Sri S. Ramesh Babu |

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by : PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - V semester - Section A(Scheme-23)

LECTURE HALL : ECE-4

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 5:00 pm
MON	TL Lab(P) ivrr-gdp,mv		AWP(T) jstt	CAO(T) ts	LUNCH BREAK	MPMC(T) gr	ADICA(T) ivrr	Student activities/ Remedial classes
TUE	PCBD & PD gak	ADICA ivrr	CAO(T) ts	ADICA(T) ivrr		AWP(T) jstt	MPMC(T) gr	Student activities/ Remedial classes
WED	ADICA(P)/MPMC(P) ivrr-sv,msn/gr-wy,mv			MPMC gr		CAO ts	OE-I pan	Student activities/ Remedial classes
THU	CAO ts	AWP jstt	ADICA ivrr	PCBDesign&PD gak		PCBDesign & PD gak-grk		Student activities/ Remedial classes
FRI	ADICA ivrr	MPMC gr	AWP jstt	ADIC(P)/MPMC(P)		ADIC(P)/MPMC(P) ivrr-sv,msn/gr-rsb,pcpk		Student activities/ Remedial classes
SAT	AWP jstt	OE-I pan	ADICA ivrr	CAO ts		MPMC gr	OE-I pan	Student activities/ Remedial classes

TEA BREAK 10.40 am TO 11.00 am

LUNCH BREAK: 12.40 pm to 1.40 pm

Theory:

- 1 EC301 Analog and Digital IC Applications(ADICA)
- 2 EC302 Antennas and Wave Propagation(AWP)
- 3 EC303 Microprocessors and Micro Controllers (MPMC)
- 4 EE304 Professional Elective-I(Computer Architecture & Organization(CAO))
- 5 EC305 Open Elective Course -I(OEC-I)
- 6 EC306 PCB Design and Prototype Development

Practicals

- 7 EC308 Analog and Digital IC Applications Lab(ADICA Lab)
- 8 SCEC03 Microprocessors and Micro Controllers Lab (MPMC Lab)
- 9 TL Trinkering Lab

Faculty Member:

- | | |
|------|---|
| ivrr | Sri I.Venkata Rameswara Reddy |
| jstt | Dr J.S.T.Thillagam |
| gr | Dr G.Ramesh |
| ts | Dr. T.Swathi |
| pan | Dr.P.Anil Kumar |
| gak | Dr G.Amjad Khan,G Rajesh Krishna |
| ivrr | Sri I.Venkata Rameswara Reddy-S Vyshali,
M Sameena Nazeer |
| gr | Dr G.Ramesh,W.Yasmeen,M. Venkatesh, R Sdheer
Bab.PCPraveen Kumar |
| ivrr | Sri I.Venkata Rameswara Reddy
Smt G.Divya Praneetha,Sri M. Venkatesh |

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

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TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - V semester - Section B(Scheme-23)

LECTURE HALL :ECE-5

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 5:00 pm	
MON	MPMC smsd	AWP pcpk	MPMC(T) smsd	CAO(T) avsr	LUNCH BREAK	AWP(T) pcpk	ADICA(T) sv	Student activities/ Remedial classes	
TUE	ADICA sv	CAO avsr	PCBD &PD(T) gak	CAO(T) avsr		ADICA(T) sv	MPMC(T) smsd	Student activities/ Remedial classes	
WED	OE-I panPCB Design & PD gak-md					TL Lab(P) drs		Student activities/ Remedial classes	
THU	ADICA sv	ADICA(P)/MPMC(P) sv-cr,mv/smsd-wy,kl				AWP pcpk	OE-I pan	Student activities/ Remedial classes	
FRI	PCBD &PD gak	ADICA sv	AWP pcpk	MPMC smsd		CAO avsr	OE-I pan	Student activities/ Remedial classes	
SAT	CAO avsr	ADICA(P)/MPMC(P) sv-dro,pak/smsd-krk,jstt				ADICA sv	MPMC smsd	Student activities/ Remedial classes	

TEA BREAK 10.40 am TO 11.00 am

LUNCH BREAK: 12.40 pm to 1.40 pm

Theory:

1	EC301	Analog and Digital IC Applications(ADICA)	sv
2	EC302	Antennas and Wave Propagation(AWP)	pcpk
3	EC303	Microprocessors and Micro Controllers (MPMC)	smsd
4	EE304	Professional Elective-I(Computer Architecture & Organization(COA))	avsr
5	EC305	Open Elective Course -I(OEC-I)	pan
6	EC306	PCB Design and Prototype Development	gak

Practicals

7	EC308	Analog and Digital IC Applications Lab(ADICA Lab)	sv
8	SCEC03	Microprocessors and Micro Controllers Lab (MPMC Lab)	smsd
9	TL	Trinkering Lab	drs

Faculty Member:

Dr S.Vsyhali
Dr P.C. Praveen Kumar
Dr S.M.Shamsheer Daula
Sri A. Venkata Subba Reddy
Dr.P.Anil Kumar
Dr G.Amjad Khan, M Devendra
Dr S.Vsyhali.C Rajeswari,M.Venkatesh,D Rohini,P Abdul khyum
Dr S.M.Shamsheer Daula,W Yasmeen,
K Lakshmidivi,JSTTilagam,K Ramakrishna
Dr D.R.Srinivas
Smt A.Parvathi,Sri S. Ramesh Babu,

Prepared by : **B. Siva Reddy**

Verified by: **HOD (E.C.E.)**

Approved by : **PRINCIPAL**

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - V semester - Section C(Scheme-23)

LECTURE HA ECE - 6

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 5:00 pm
MON	AWP pan	ADICA cr	CAO(T) ap	ADICA(T) cr	LUNCH BREAK	AWP(T) pan	MPMC(T) rsb	Student activities/ Remedial classes
TUE	MPMC rsb	CAO ap	AWP(T) pan	ADICA(T) cr		CAO(T) ap	MPMC(T) rsb	Student activities/ Remedial classes
WED	ADICA cr	OE-I rsb	CAO ap	ADIC(P)/MPMC(P)		ADIC(P)/MPMC(P) tsb-ivrr,gak/rsb-smsd,kld		Student activities/ Remedial classes
THU	PCB Design & PD kuk-gak,			MPMC rsb		TL Lab(P) smsd		Student activities/ Remedial classes
FRI	ADICA(P)/MPMC(P) tsb-cr,mv/rsb-drs,kld			OE-I rsb		AWP pan	ADICA cr	Student activities/ Remedial classes
SAT	CAO ap	PCBD&PD kuk	AWP pan	ADICA cr		MPMC rsb	OE-I rsb	Student activities/ Remedial classes

TEA BREAK 10.40 am TO 11.00 am

Theory:

- | | | | |
|---|-------|---|-----|
| 1 | EC301 | Analog and Digital IC Applications(ADICA) | cr |
| 2 | EC302 | Antennas and Wave Propagation(AWP) | pan |
| 3 | EC303 | Microprocessors and Micro Controllers (I) | rsb |
| 4 | | Professional Elective-II(Computer | ap |
| 5 | EE317 | Open Elective Course -I(OEC-I) | rsb |
| 6 | EC305 | PCB Design and Prototype Development | kuk |

Practicals

- | | | | |
|---|--------|--|-------------|
| 7 | EC307 | Analog and Digital IC Applications Lab(ADICA Lab) | tsb |
| 8 | EC308 | Microprocessors and Micro Controllers Lab (MPMC Lab) | rsb |
| 9 | SCEC03 | Trinkering Lab | smsd,wy,srk |

LUNCH BREAK: 12.40 pm to 1.40 pm

Faculty Member:

- | |
|---|
| Smt C. Rajeswari |
| Dr.P.Anil Kumar |
| Dr R. Sudheer Babu |
| Smt A. Parvathi |
| Sri S. Ramesh Babu |
| Smt K. Udaya Kiran,G Amjad Khan |
| Sri T. Sarath Babu-C Rajeswari,G Amjad Khan,IV Rameswara Reddy, M Venkatesh |
| Dr R. Sudheer Babu,S Shamsheer Daula, K.Lakshmi Devi,D R Srinivas |
| Dr S.M.Shamsheer Daula |
| Smt W. Yasmeen, Sri K.Ramakrishna |

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by : PRINCIPAL

GPREC-F / CFD / TIME TABLE

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - VII semester - Section A(Scheme-20)

LECTURE HALL ECE - 7

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm -1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 5:00 pm
MON	FPGA&ASIC gvrs	IP pak	UHV-2 kuk	DES wy	LUNCH BREAK	CN grk	FPGA&ASIC gvrs	Student activitites/ Remedial classes
TUE	CMC pcpk	UHV-2 kuk	DS(P) Lab pcpk-mvrv,dro			FPGA&ASIC gvrs	DES wy	Student activitites/ Remedial classes
WED	CMC pcpk	FPGA&ASIC gvrs	PW kld-drs			IP pak	CMC pcpk	Student activitites/ Remedial classes
THU	CN grk	CMC pcpk	PW kld-md			UHV-2 kuk	IP pak	Student activitites/ Remedial classes
FRI	DS(P) Lab pcpk-pan,avsr		DES wy	CN grk		PW kld-md		Student activitites/ Remedial classes
SAT	IP pak	CN grk	DES wy	Mentoring		ITCA		Student activitites/ Remedial classes

TEA BREAK 10.40 am TO 11.00 am

LUNCH BREAK: 12.40 pm to 1.40 pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

- | | | | |
|---|---------|---|-----|
| 1 | EC401 | Cellular and Mobile Communications (Professional Elective-III) | --- |
| 2 | EC404 | Computer Networks(Professional Elective-IV) | --- |
| 3 | EC408 | FPGA and ASIC Architectures and Applications(Professional Elective-V) | --- |
| 4 | OE407 | Distributed Embedded Systems(Open Elective-III) | --- |
| 5 | OE415 | Image Processing(Open Elective-IV) | --- |
| 6 | HSSC701 | Universal Human Values:-2 | --- |

Practicals

- | | | | |
|---|--------|-----------------------|-----|
| 7 | SCEC05 | Data Science Lab(DSL) | --- |
| 8 | PROJ | Project Work(PW) | --- |

Faculty Member:

- | | |
|------|--|
| pcpk | Dr P.C. Praveen Kumar |
| grk | Sri. G. Rajesh Krishna |
| gvv | Dr G.V.R Sagar |
| wy | Smt. W.Yasmeen |
| pak | Dr P. Abdul Khayum |
| kuk | Smt K. Udaya Kiran |
| pcpk | Dr P.C. Praveen Kumar, M V R Vittal,
D Rohini, A Venkata Subba Reddy,P Anil Kumar |
| kld | Smt K. Lakshmi Devi, M Devendra, DR Srinivas |

Prepared by : **B. Siva Reddy**

Verified by: HOD (E.C.E.)

Approved by : **PRINCIPAL**

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TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - VII semester - Section B(Scheme-20)

LECTURE HALL/ECE - 8

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm -1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 5:00 pm
MON	DS(P) Lab avsr-krk,grk		IP rsb	UHV-2 kr	LUNCH BREAK	DES krk	Mentoring	Student activities/ Remedial classes
TUE	DES krk	CN tsb	IP rsb	FPGA&ASIC srb		DS(P) Lab avsr-kr,srb		Student activities/ Remedial classes
WED	UHV-2 kr	CN tsb	PW jstt-Kr			CMC jstt	CN tsb	Student activities/ Remedial classes
THU	FPGA&ASIC srb	IP rsb	DES krk	CMC jstt		PW jstt-pspr		Student activities/ Remedial classes
FRI	CMC jstt	UHV-2 kr	FPGA&ASIC srb	DES krk		CMC jstt	FPGA&ASIC srb	Student activities/ Remedial classes
SAT	CN tsb	IP rsb	PW jstt-ppck			ITCA		Student activities/ Remedial classes

TEA BREAK 10.40 am TO 11.00 am

LUNCH BREAK: 12.40 pm to 1.40 pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

1	EC401	Cellular and Mobile Communications (Professional Elective-III)	---
2	EC404	Computer Networks(Professional Elective-IV)	---
3	EC408	FPGA and ASIC Architectures and Applications(Professional Elective-V)	---
4	OE407	Distributed Embedded Systems(Open Elective-III)	---
5	OE415	Image Processing(Open Elective-IV)	---
6	HSSC701	Universal Human Values--2	---

Practicals

7	SCEC05	Data Science Lab(DSL)	---
8	PROJ	Project Work(PW)	---

Faculty Member:

jstt	Dr J.S.T.Thillagam
tsb	Sri T. Sarat Babu
srb	Sri S. Ramesh Babu
krk	Sri K. Ramakrishna
rsb	Dr R. Sudheer Babu
kr	Sri K. Raju
avsr	Sri A. Venkata Subba Reddy,K. Raju.
jstt	Ramakrishna, S Ramesh Babu,G Rajesh Krishna
	Dr J.S.T.Thillagam-P. Surya Prakash,PC Praveen Kumar

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - VII semester - Section C(Scheme-20)

LECTURE HALL ECE - 9

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm -1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 5:00 pm
MON	CMC mrvr	FPGA&ASIC dro	DS(P) Lab mrvr-pan,srb		LUNCH BREAK	CN dvsvb	DES smsd	Student activities/ Remedial classes
TUE	DS(P) Lab mrvr-jstt,wy		DES smsd	UHV-2 tsb		IP drj	FPGA&ASIC dro	Student activities/ Remedial classes
WED	CN dvsvb	DES smsd	CN dvsvb	CMC mrvr		PW dvsvb-avsr		Student activities/ Remedial classes
THU	FPGA&ASIC dro	IP drj	PW dvsvb-bsr			CMC mrvr	UHV-2 tsb	Student activities/ Remedial classes
FRI	DES smsd	CMC mrvr	FPGA&ASIC dro	UHV-2 tsb		CN dvsvb	IP drj	Student activities/ Remedial classes
SAT	IP drj	Mentoring	PW dvsvb-drj			ITCA		Student activities/ Remedial classes

TEA BREAK 10.10 am TO 10.40am

LUNCH BREAK: 12.20pm to 1.20pm

NOTE:FRS Attendance must be taken every day first hour

Theory:

1	EC401	Cellular and Mobile Communications (Professional Elective-III)	---
2	EC404	Computer Networks(Professional Elective-IV)	---
3	EC408	FPGA and ASIC Architectures and Applications(Professional Elective-V)	---
4	OE407	Distributed Embedded Systems(Open Elective-III)	---
5	OE415	Image Processing(Open Elective-IV)	---
6	HSSC701	Universal Human Values-2	---

Practicals

7	SCEC05	Data Science Lab(DSL)	---
8	PROJ	Project Work(PW)	---

Faculty Member:

mrvr	Dr M.V.R Vittal
dvsvb	Dr D. V. Srihari Babu
dro	Smt D. Rohini
smsd	Dr S.M.Shamsheer Daula
drj	Dr D. Rajasekhar
tsb	Sri T. Sarath Babu
mrvr	Dr M.V.R Vittal,JSTTilagam,W Yasmeen, S Ramesh Babu, P Anil Kumar
dvsvb	Dr D.V.SriHari Babu B Siva Reddy, AV Subba Reddy,D Rajasekhar

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - V semester - Minor in Embedded Systems & IOT (EIOT)-(Scheme-23)

Lecture Hall : ECE-1

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm -1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 4:10 pm	4:10 pm to 5:00 pm	6 P.M. to 7 P.M. (Online)
MON					LUNCH BREAK					
TUE										
WED								PIOT wy	EST krk	
THU								PIOT wy	EST krk	
FRI								PIOT wy	PIOT Lab wy	EST krk
SAT								PIOT Lab wy		

Theory:

- | | | | |
|---|-------|---------------------------------|-----|
| 1 | ME101 | Embedded System Technology(EST) | krk |
| 2 | ME102 | Principles of IOT | wy |
| 3 | ME103 | Principles of IOT Lab | wy |

Faculty Member:

Sri K. Ramakrishna
Smt.W.Yasmeen
Smt.W.Yasmeen

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - V semester - Minor in INDUSTRIAL ROBOTICS(Scheme-23)

Lecture Hall :ECE-4

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 4:10 pm	4:10 pm to 5:00 pm	6 P.M. to 7 P.M. (Online)
MON					LUNCH BREAK					
TUE										
WED								IR _{gr}	PLC _{sv}	
THU								IR _{gr}	PLC _{sv}	
FRI								IR _{gr}	RHC LAB _{gr}	PLC _{sv}
SAT								RHC LAB _{gr}		

Theory:

- 1 MIR01 Introduction to Robotics(IR)
- 2 MIR02 Programmable Logic Controllers(PLC)
- 3 MIR03 Robotics Hardware and Control Lab(RHCL)

gr
sv
gr

Faculty Member:

Dr.G. Ramesh
Dr S. Vyshali
Dr.G. Ramesh

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS :

B.Tech - V semester - Honors in ECE(Scheme-23)

Lecture Hall :ECE-7

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 4:10 pm	4:10 pm to 5:00 pm	6 P.M. to 7 P.M. (Online)
MON					LUNCH BREAK					
TUE										
WED								AICD gdp	DICD ivrr	
THU								AICD gdp	DICD ivrr	
FRI								AICD gdp	ADICD Lab gdp	DICD ivrr
SAT								ADICD Lab gdp		

- 1 HEC01
- 2 HEC02
- 3 HEC03

Theory:

Analog IC Design(AICD)
 Digital IC Design(DICD)
 Analog and Digital IC Design(ADICD)

gdp
 ivrr
 gdp

Faculty Member:

Smt.G.Divya Praneetha
 Sri.I.V. Rameshwar Reddy
 Smt.G.Divya Praneetha

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS :

B.Tech - VII semester - Minor in IOT(Scheme-20)

Lecture Hall :ECE-3

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 4:10 pm
MON					LUNCH BREAK			
TUE								
WED								IIT smsd
THU								IIT smsd
FRI								IIT smsd
SAT								

1 MIT04

Theory:
Industrial Internet of Things(IIT)

smsd

Faculty Member:
Dr S.M.Shamsheer Daula

Prepared by : B. Siva Reddy

K. S. Reddy
Verified by: HOD (E.C.E.)

Shamsheer Daula
Approved by :PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE for the Academic year 2025-2026 with effect from 23-06-2025

CLASS : B.Tech - VII semester - Minor in INDUSTRIAL ROBOTICS(Scheme-20)

Lecture Hall :ECE-6

Day / Time	09.00 am - 9.50 am	9.50 am - 10.40am	11.00 am - 11.50 pm	11.50 am - 12.40 pm	12.40 pm - 1.40 pm	1.40 pm - 2.30 pm	2.30 pm to 3.20pm	3.20 pm to 4:10 pm
MON					LUNCH BREAK			RPA krk
TUE								RPA krk
WED								RPA krk
THU								
FRI								
SAT								

1 MIR04

Theory:
 Robotic Process Automation(RPA)

kr

Faculty Member:
 Sri K. Ramakrishna

Prepared by : B. Siva Reddy

Verified by: HOD (E.C.E.)

Approved by :PRINCIPAL