

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):KURNOOL
NOTICE

Date:12/09/2017

The End examinations for M.Tech III Semester Regular (Scheme-2013) and M.Tech I & II Semester (Supplementary – Scheme-2013) will be conducted from 13/11/2017 onwards. Copy of Time-tables are displayed on college website and exam notice boards.

The important dates are as follows.

- | | | |
|---|---|-------------------|
| a) Registration for exams commences form | : | 13/09/2017 |
| b) Applications will be accepted from | : | 13/09/2017 |
| c) *Last date for submission of applications without fine | : | 07/10/2017 |
| d) Closure of nominal rolls | : | 30/10/2017 |

*** LAST DATE MEANS BOTH FOR PAYMENT OF FEE AND SUBMISSION OF APPLICATION**

The applications will be accepted with penal fee as prescribed (the details of which are notified separately in the exam section notice board) after the last date without fine till the closure of nominal rolls. **NO APPLICATION WILL BE ACCEPTED AFTER THE CLOSURE OF NOMINAL ROLLS.**

Procedure for online registration for end exams

1. Visit the college website (www.gprec.ac.in)
2. Click on Register for **"NOV-17 End exams"**.
3. Login using Username (Student Roll No) & Password (DOB)
4. Click on the check box (on the left side of the subject) to enroll for Supplementary Subjects/ Regular Exams.
5. Click on preview button to view the summary of the subjects registered. To make any changes in the registration click on **'Modify'** button.
6. Click on **'Submit'** button to save the application. No further changes shall be permitted once the application is submitted.
7. After successful submission, Exam fee can be paid either online or offline (through Challan at Andhra Bank, GPREC)
8. For online payment take the print of the
(1) Application Form and (2) Payment acknowledgement
9. For Offline payment take the print of the
(1) Application Form and (2) Challan. Pay the exam fee at Andhra Bank GPR Engg. College by submitting the above challan.
10. Affix Signature and paste latest color passport size photograph in the online application form.
11. Submit the online application form along with payment acknowledgement (in case of online payment) and fee payment challan (in case of offline payment) in the exam cell on or before due dates specified in the circular.


PRINCIPAL

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G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):KURNOOL

SCHEDULE

Date : 12/09/2017

Schedule for the payment of examination fee for M.Tech III Semester Regular (Scheme-13) and M.Tech I & II Semester (Supplementary – Scheme-2013) End examinations of November 2017 and Fee particulars are as follows.

Scheme – 2013 For REGULAR CANDIDATES:-

Examination Fee (Theory / Practical) : Rs. 1400.00

Scheme -2013 For Supplementary Candidates :-

1. For One Subject (Theory / Practical) : Rs. 500.00
2. For Two Subjects (Theory / Practical) : Rs.1000.00
3. For Three and More Subjects (Theory / Practical) : Rs.1400.00

Last date for submission of application without fine is 07/10/2017

THE PENAL FEE FOR THE APPLICATION RECEIVED AFTER 07/10/2017 IS AS GIVEN BELOW:

- A. On or before 14/10/2017 : Rs. 200.00
- B. On or before 21/10/2017 : Rs. 500.00
- C. On or before 30/10/2017 : Rs.1000.00

Note:-A student is required to complete the course of study satisfying the attendance requirements in all the two years of the course within a period of 4 academic years from the year of admission, failing which he/she shall be declared ineligible to pursue M.Tech degree course. Completing the course of study shall mean not only satisfying the attendance requirements but also passing of all the subjects including Project work within the stipulated period.


PRINCIPAL

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G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

M.Tech III – SEMESTER(Scheme-2013)

END EXAMINATIONS – November – 2017

Date	Day	Time	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI & Embedded Systems	Thermal Science and Energy Systems	CAD/CAM	Power Electronics
13-11-2017	MON	9 : 00 A.M to 12 : 00 Noon	MCP -Mobile Computing	SS- Stability of Structures	SSP - Statistical Signal Processing	EDAT : Electronic Design Automation Tools	FEA - Finite Element Analysis	IMS-Intelligent Manufacturing Systems	SMPC- Switched Mode Power Converters
15-11-2017	WED		Elective- III: SC Soft Computing	Elective-III : SO Structural Optimization	Elective-III: CTH- Coding Techniques	Elective-III: ASIC -Application Specific Integrated Circuits	Elective-III: DHE- Design of Heat Transfer Equipment	Elective-III: SMS- Simulation of Manufacturing Systems	Elective-III:IAPE- Industrial Applications of Power Electronics
17-11-2017	FRI		Elective- IV: NSC Network Security & Cryptography	Elective-IV: ACT Advanced Concrete Technology	Elective-IV: ADCA- Advanced Computer Architecture	Elective-IV: VS- VLSI Signal Processing	Elective-IV:GTT- Gas Turbine Theory	Elective-IV:RPTM- Rapid Prototyping, Tooling and manufacture	Elective-IV:RES- Renewable Energy Sources
			DS: Dissertation Phase – I	DS: Dissertation Phase – I	DS: Dissertation Phase – I	DS: Dissertation Phase – I	DS: Dissertation Phase – I	DS: Dissertation Phase – I	DS: Dissertation Phase – I

*DS : Dates will be intimated by the Department concerned

Note: 1. No Grace Period for entering into the examination hall. Students are allowed into halls 15 minutes before commencement of examinations.

2. A student is required to complete the course of study satisfying the attendance requirements in all the two years of the course within a period of 4 academic years from the year of admission, failing which he/she shall be declared ineligible to pursue M.Tech degree course. Completing the course of study shall mean not only satisfying the attendance requirements but also passing of all the subjects including Project work within the stipulated period.

ADDITIONAL CONTROLLER OF EXAMINATIONS

CONTROLLER OF EXAMINATIONS

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL
M.Tech I – SEMESTER (Scheme-2013)

END EXAMINATIONS (SUPPLEMENTARY) – November – 2017

Date	Day	Time	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI and Embedded Systems	Thermal Sciences and Energy Systems	CAD/CAM	Power Electronics
13-11-2017	MON	2:00 P.M TO 5:00 P.M	ADS –Advanced Data Structures	AEM - Advanced Engineering mathematics	DES -Detection and Estimation of Signals	APS -Advanced Problem Solving	PMA - Probability and Mathematical Analysis	CMT – Computational Methods	EMM - Electrical Machines Modeling
15-11-2017	WED		SE – Software Engineering	TE – Theory of Elasticity	ADSP - Advanced Digital Signal Processing	CMOS - CMOS VLSI Design	ATD - Advanced Thermodynamics	CIM - Computer integrated Manufacturing	SSPC1 - Solid State Power Converters-1
17-11-2017	FRI		WT – Web Technologies	ASA - Advanced Structural Analysis	ACM - Advanced Communications	MAD –HDL Modeling of Advanced Digital System design using HDL	RCG -Refrigeration and Cryogenics	CNCM – Computer Numerical Control Machines	SSPC2 - Solid State Power Converters-2
20-11-2017	MON		ADBMS – Advanced Database Management Systems	TAP - Theory and Analysis of Plates	MWC - Microwave Communications	AES-I – Advanced Embedded Systems -I	RES -Renewable Energy Sources	GM - Geometric modeling	DSP - Digital Signal Processing
22-11-2017	WED		DAA - Elective – I: Design and Analysis of Algorithms	BE-Elective – I: Bridge Engineering	ADMM - Elective – I: Advanced Microprocessors and Microcontrollers	FPGA -Elective – I: CPLDS and FPGA Design	ECT -Elective – I: Energy Conversion Technologies	AOT- Elective – I: Advanced Optimization Techniques	DCS – Elective– I: Digital Control Systems

- Note:**
1. While filling the subjects in Application and Hall-tickets use only the abbreviations given above and maintain the same order.
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ADDITIONAL CONTROLLER OF EXAMINATIONS

CONTROLLER OF EXAMINATIONS

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

M.Tech II – SEMESTER (Scheme-2013)
(SUPPLEMENTARY) END EXAMINATIONS – November – 2017

Date	Day	TIME	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI & Embedded Systems	Thermal Science and Energy Systems	CAD/CAM	Power Electronics
14-11-2017	TUE	9 : 00 A.M to 12 : 00 Noon	SQT - Software Quality and Testing	ARCD -Advanced Reinforced Concrete Design	DIPPR -Digital Image Processing and Pattern Recognition	AFV - Algorithms for VLSI	AHT -Advanced Heat and Mass Transfer	FEA -Finite Element Analysis	SDCD -Solid State DC Drives
16-11-2017	THU		ACN - Advanced Computer Networks	ASSD -Advanced Structural Steel Design	OCN -Optical Communications	AVLSI -Analog VLSI Design	AET -Advanced Energy Technologies	RBT - Robotics	SACD -Solid State AC Drives
18-11-2017	SAT		CC - Cloud Computing	FEM -Finite Element Methods	ASP - Adaptive Signal Processing	LVD - Low Power VLSI Design	EAM - Energy Auditing and Management	FMS - Flexible Manufacturing Systems	HVDC -HVDC and FACTS
21-11-2017	TUE		OOADDP -OOAD and Design Patterns	SD - Structural Dynamics	MCN - Mobile Communications	AES II - Advanced Embedded Systems – II	AF - Alternative Fuels	RD -Robust Design	SPES -Simulation of Power Electronic Systems
23-11-2017	THU		DOS - Elective- II: Distributed Operating System	ERDS-Elective- II: Earthquake Resistant Design of Structures	EMS-Elective- II: Embedded Systems	ADCA-Elective- II: Advanced Computer Architecture	DACS -Elective- II: Design of Air Conditioning Systems	FAD-Elective- II: Failure Analysis and Design.	NNFL-Elective- II: Neural Networks and Fuzzy Logic

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ADDITIONAL CONTROLLER OF EXAMINATIONS

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