

# ORGANIZING BY

Department of  
Electronics & Communication  
Engineering  
G. Pulla Reddy Engineering College  
(Autonomous)

Accredited by NBA of AICTE & NAAC  
of UGC with 'A' Grade.

G. Pulla Reddy Nagar, Nandyal Road,  
KURNOOL – 518 007, Andhra Pradesh,  
India.

## Objective of the FDP:

To provide insights into advanced concepts and recent research trends in the area of VLSI design using Artificial Intelligence, which is highly relevant for both academic researchers and industry professionals. Eminent experts from reputed institutions and industries like Entuple Technologies has been invited to deliver technical sessions during the program.

## Contents of the FDP:

- Overview of Frontend Design & Verification
- Hands on sessions on Digital block level design with FPGAs
- Sessions on Back end VLSI Design Flow
- Hands on sessions on backend EDA tools
- Role of Artificial Intelligence in VLSI and Case study
- Career opportunities in VLSI domain

## CHIEF PATRON

Sri P. Subba Reddy  
Chairman, GPREC

## PATRON

Dr. B. Sreenivasa Reddy  
Principal, GPREC

## CO-PATRONS

Dr. K. Suresh Reddy,  
Prof. & HOD of ECE, GPREC

Dr. S. Nagaraja Rao,  
Prof. & Associate Head of ECE, GPREC

## CONVENERS

Dr. M. Madhusudhan Reddy,  
Asst. Professor, ECE, GPREC

Smt. G. Divya Praneetha,  
Asst. Professor, ECE, GPREC

## CO-CONVENERS

Sri. S. Ramesh Babu,  
Asst. Professor, ECE, GPREC

Smt. D. Rohini,  
Asst. Professor, ECE, GPREC

One week National level Offline  
Faculty Development Program  
on

# Artificial Intelligence for Next-Generation VLSI

3<sup>rd</sup> – 7<sup>th</sup> February 2026



## ABOUT THE INSTITUTION:

The College is the brainchild of the renowned philanthropist, Sri G. Pulla Reddy. Established in 1985 and managed by G. Pulla Reddy Charities Trust, the college is affiliated to Jawaharlal Nehru Technological University Anantapuramu and approved by AICTE, accredited by NBA of AICTE and NAAC of UGC.

The College celebrated its Ruby Jubilee Celebrations during 2024-2025 and completed 40 years of its service in Engineering Education. The college offers Under Graduate Programs in Nine branches and Five Post Graduate courses. The college is situated on Kurnool-Chittoor National Highway No. 7. Kurnool is well connected by Rail and Road. The places of interest nearby are Srisailam and Belum Caves.

## ABOUT THE DEPARTMENT:

The Department of Electronics and Communication Engineering was established in the academic year 1984-85 with an intake of 40 and currently with 180 regular and 18 lateral entry students. The department is also offering one postgraduate programme with the specialization of VLSI and Embedded Systems with an intake of 18 students

## FDP DETAILS:

- The One-week FDP will be conducted by a team of resource persons from reputed institutions and industries like Entuple Technologies possessing extensive experience in the theme of the FDP.
- The first three days, Theory and hands on sessions will be conducted on Backend of VLSI Design using Cadence Virtuoso, and next two days' hands on sessions will be conducted on VLSI using with Artificial Intelligence within the ECE department of GPREC, Kurnool.
- certificates will be awarded to participants who attend over 80% of the FDP sessions.
- Mode of Conduction: Offline Mode.
- Session Timings (Tuesday to Saturday): 09:30 AM – 12:30 PM and 1:30 PM – 4:30 PM

## Resource Persons



**Pulavarthi Rajendra**

Application Engineer,  
Entuple Technologies Pvt. Ltd,  
Bangalore



**Dr. D. Asha Devi**

Professor, Dept. of ECE,  
Sreenidhi Institute of Science &  
Technology, Hyderabad



**Dr. M. Madhusudhan Reddy**

Asst. Professor, ECE Dept ,  
GPREC

## Registration Details

### **Eligibility**

Faculty members, Research scholars, and  
PG Students.

**Registration Fee : 1000/-**

Industry Professionals

**Registration Fee : 1500/-**

### **Registration Link**



<https://bit.ly/AlforVLSI>

### **Registration Deadline**

**25<sup>th</sup> January 2026**

### **Contact for other queries:**

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