G Pulla Reddy Engineering College (Autonomous)
Kurnool, Andhra Pradesh, India - 518007.

NBA
NATIONAL BOARD of ACCREDITATION

A+ GRADE

NIRF RANK 190 nirf
By
National Institutional Ranking Framework -2020
Ministry of Human Resource Development (MHRD)
Government of India

IUCEE

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Department of Electronics & Communication Engineering
is organizing

All India Council for Technical Education(AICTE) sponsored
One Week Online Short Term Training Program
(Ref. No. 34-66/304/FDC/STTP/Policy-1/2019-20)
on

**"Practical Exposure on Machine Learning
& Data Science (Phase-1)"**

📅 26th **April 2021** to 1st **May 2021**

⏰ 10:00 AM - 12:00 PM **and** 2:00 PM - 4:00 PM

📞 **9963326283, 8555974250**

REGISTER HERE
bit.ly/**ML_DS_STTP**

***No Registration Fee**

Speakers

Dr. D V L N Somayajulu
Director, IIITDM
Kurnool

Sri P Mohan
Sr.Data Scientist, Tech Mahindra
Hyderabad

Dr. Deepak Ranjan Nayak
Assistant Professor, MNIT
Rajasthan

Dr. P Radha Krishna
Professor & Head of CSE
NIT, Warangal

Dr. Tripti Swarnakar
Professor, ITEAR
Bhubaneshwar

ABOUT COLLEGE

G Pulla Reddy Engineering College is the brainchild of Late Sri G.Pulla Reddy, the renowned philanthropist and a great humanist. Established in 1984-85, it is one of the earliest private engineering colleges in A.P State. GPREC has been functioning as an autonomous institution since 2006.The college is being managed by G PullaReddy Charites trust, Hyderabad.The trust was instituted by Late Sri. G Pulla Reddy Garu in 1975 with the motto of rendering service to the society.

ABOUT DEPARTMENT

The **ECE department** was established in the academic year 1984-85 with an intake of 40 and currently with 180 regular and 36 lateral entry students. The department is offering a post graduate programme with the specialization of VLSI & Embedded systems with an intake of 18 students. The department is a recognized research center under JNTUA offering Ph. D program The department has highly qualified faculty having specialized in diversified areas of technology like Communications,Signal Processing, Microelectronics, Microprocessors, Instrumentation and control and Digital Electronics. The department is well equipped and has excellent laboratory facilities.

ABOUT STTP

The **STTP** aims to achieve the objective of promoting industry-institute interaction. For a meaningful and effective interaction, it proposes to bring together the industry/field experts and academicians, to discuss technological developments and advances. This STTP deals with the latest practices in the field of Machine Learning and provides a platform for showcasing their implementation and impact on industry and society in general. The STTP gives scope for arriving at solutions to several practical oriented problems.

Outcomes:

1.Understanding the perspective of presentation of data, information and knowledge.

2.Analyzing the various real world problems/scenarios and offering solutions.

3.Achieving capability of applying Machine Learning algorithms in practice.

4.Developing their own algorithms.

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