G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):KURNOOL
NOTICE

Date: 25/02/2015

The End examinations for M.Tech I-semester, M.Tech – II- Semester and M.Tech – III Semester will be conducted from 20/04/2015 onwards. Time-tables and fee particulars will be notified separately.

Students have to obtain the challan forms and Exam application form from the examination section on payment of Rs. 50/-. (for chalan, time-table, xeroxing the marks memo etc.,).

Afterwards they have to pay in the bank the required examination fee (using the challan) into SB Account no. 31255 of Principal, G.P.R.E.C., fill up the application form and submit the application and challan issued by the bank in the Examination Section before due date.

The important dates are as follows.

a)	Applications will be issued from	:	28/02/2015
b)	Applications will be accepted from	:	28/02/2015
c)	*Last date for submission of applications without fine	:	**17/03/2015**
d)	Closure of nominal rolls	:	06/04/2015

* LAST DATE MEANS BOTH FOR PAYMENT OF FEE AND SUBMISSION OF APPLICATION

The applications will be accepted with penal fee as prescribed (the details of which are notified separately in the exam section notice board) after the last date without fine till the closure of nominal rolls. **NO APPLICATION WILL BE ACCEPTED AFTER THE CLOSURE OF NOMINAL ROLLS.**

The students are advised to observe the following precautions while filling in the applications.

1. The 'Name of the candidate' both in the application and hall tickets should be the same as that appeared in the Intermediate marks memo.

2. The candidates are required to mention all the theory and practical subjects' abbreviations in the same order as given in the time-table for which they are applying even if they are appearing either for improvement or for whole examination.

3. The bank will issue two challan receipts, the student has to enclose one to the application and the other can be retained with the student.

4. While filling the challan, the amount in figures as well as in words should be the same. If at all there is any correction in the chalan it must be attested by the Bank Manager.

5. Candidates should enter the particulars of fee paid viz. amount, challan number, where paid, date etc. on the application.

6. Signature of the candidate should be identical in application as well as in hall-tickets.

7. Latest identical passport size photos should be affixed both on application and on hall tickets.

PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):KURNOOL

SCHEDULE

Date : 25/02/2015

Schedule for the payment of examination fee for M.Tech I-semester, M.Tech – II- Semester and M.Tech – III End Regular & Supplementary examinations of April– 2015 and Fee particulars are as follows.

Scheme – 2013 For REGULAR CANDIDATES:-

Examination Fee (Theory / Practical) : Rs. 1200.00

Scheme -2009 & Scheme -2013 For Supplementary Candidates :-

1. For One Subject (Theory / Practical) : Rs. 400.00
2. For Two Subjects (Theory / Practical) : Rs. 800.00
3. For Three and More Subjects (Theory / Practical) : Rs.1200.00

Last date for submission of application without fine is 17/03/2015

THE PENAL FEE FOR THE APPLICATION RECEIVED AFTER 17/03/2015 IS AS GIVEN BELOW:

A. On or before 19/03/2015 : Rs. 100.00
B. On or before 26/03/2015 : Rs. 200.00
C. On or before 01/04/2015 : Rs. 300.00
D. On or before 06/04/2015 : Rs. 1000.00

Note:-A student is required to complete the course of study satisfying the attendance requirements in all the two years of the course within a period of 4 academic years from the year of admission, failing which he/she shall be declared ineligible to pursue M.Tech degree course. Completing the course of study shall mean not only satisfying the attendance requirements but also passing of all the subjects including Project work within the stipulated period.

PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL
M.Tech I – SEMESTER (Scheme-2013)

(REGULAR & SUPPLEMENTARY) END EXAMINATIONS – April – 2015

Time : 9.00 A.M. to 12.00 Noon.

Date	Day	Paper No.	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI and Embedded Systems	Thermal Sciences and Energy Systems	CAD/CAM	Power Electronics
20.04.2015	MON	1	ADS –Advanced Data Structures	AEM- Advanced Engineering mathematics	DES -Detection and Estimation of Signals	APS-Advanced Problem Solving	PMA - Probability and Mathematical Analysis	CMT – Computational Methods	EMM - Electrical Machines Modeling
22.04.2015	WED	2	SE – Software Engineering	TE – Theory of Elasticity	ADSP - Advanced Digital Signal Processing	CMOS- CMOS VLSI Design	ATD - Advanced Thermodynamics	CIM - Computer integrated Manufacturing	SSPC1- Solid State Power Converters-1
24.04.2015	FRI	3	WT – Web Technologies	ASA- Advanced Structural Analysis	ACM - Advanced Communications	MAD –HDL Modeling of Advanced Digital System design using HDL	RCG -Refrigeration and Cryogenics	CNCM – Computer Numerical Control Machines	SSPC2- Solid State Power Converters-2
27.04.2015	MON	4	ADBMS – Advanced Data Base Management Systems	TAP- Theory and Analysis of Plates	MWC - Microwave Communications	AES-I – Advanced Embed.ded Systems -I	RES -Renewable Energy Sources	GM - Geometric modeling	DSP- Digital Signal Processing
29.04.2015	WED	5	DAA - Elective – I: Design and Analysis of Algorithms	BE- Elective – I: Bridge Engineering	ADMM - Elective – I: Advanced Microprocessors and Microcontrollers	FPGA -Elective – I: CPLDS and FPGA Design	ECT -Elective – I: Energy Conversion Technologies	AOT- Elective – I: Advanced Optimization Techniques	DCS – Elective– I: Digital Control Systems

Note: 1. While filling the subjects in Application and Hall-tickets use only the abbreviations given above and maintain the same order.

   2. No Grace Period for entering into the examination hall. Students are allowed into halls 15 minutes before commencement of examinations.

   3. A student is required to complete the course of study satisfying the attendance requirements in all the two years of the course within a period of 4 academic years from the year of admission, failing which he/she shall be declared ineligible to pursue M.Tech degree course. Completing the course of study shall mean not only satisfying the attendance requirements but also passing of all the subjects including Project work within the stipulated period.

ADDITIONAL CONTROLLER OF EXAMINATIONS

CONTROLLER OF EXAMINATIONS

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

M.Tech II – SEMESTER (Scheme-2013)
END(Supplementary) EXAMINATIONS – APRIL – 2015

Time : 9.00 A.M. to 12.00 Noon

Date	Day	Paper No.	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI & Embedded Systems	Thermal Science and Energy Systems	CAD/CAM	Power Electronics
21-04-2015	TUE	1	SQT - Software Quality and Testing	ARCD -Advanced Reinforced Concerete Design	DIPPR -Digital Image Processing and Pattern Recognition	AFV - Algorithms for VLSI	AHT -Advanced Heat and Mass Transfer	FEA -Finite Element Analysis	SDCD-Solid State DC Drives
23-04-2015	THU	2	ACN- Advanced Computer Networks	ASSD -Advanced Structural Steel Design	OCN -Optical Communications	AVLSI -Analog VLSI Design	AET -Advanced Energy Technologies	RBT- Robotics	SACD-Solid State AC Drives
25-04-2015	SAT	3	CC - Cloud Computing	FEM -Finite Element Methods	ASP - Adaptive Signal Processing	LVD - Low Power VLSI Design	EAM - Energy Auditing and Management	FMS - Flexible Manufacturing Systems	HVDC-HVDC and FACTS
28-04-2015	TUE	4	OOADDP -OOAD and Design Patterns	SD - Structural Dynamics	MCN - Mobile Communications	AES II- Advanced Embedded Systems – II	AF - Alternative Fuels	RD-Robust Design	SPES-Simulation of Power Electronic Systems
30-04-2015	THU	5	DOS - Elective– II: Distributed Operating System	ESA -Elective– II: Experimental Stress Analysis	EMS-Elective– II: Embedded Systems	ADCA- Elective– II: Advanced Computer Architecture	DACS -Elective– II: Design of Air Conditioning Systems	FAD-Elective– II: Failure Analysis and Design.	NNFL-Elective– II: Neural Networks and Fuzzy Logic

Note: 1. While filling the subjects in Application and Hall-tickets use only the abbreviations given above and maintain the same order.

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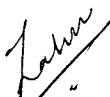
G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

M.Tech III – SEMESTER (Scheme-2013)
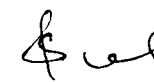
END (Supplementary) EXAMINATIONS – April– 2015

Time : 2.00 P.M. to 5.00 P.M

Date	Day	Paper No.	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI & Embedded Systems	Thermal Science and Energy Systems	CAD/CAM	Power Electronics
20-04-2015	MON	1	**MCP** -Mobile Computing	**SS**- Stability of Structures	**SSP** - Statistical Signal Processing	**EDAT :** Electronic Design Automation Tools	**FEA -** Finite Element Analysis	**IMS**-Intelligent Manufacturing Systems	**SMPC**- Switched Mode Power Converters
22-04-2015	WED	2	**Elective– III: SC** Soft Computing	**Elective-III : SO** Structural Optimization	**Elective-III: CTH-** Coding Techniques	**Elective-III: ASIC** -Application Specific Integrated Circuits	**Elective-III: DHE-** Design of Heat Transfer Equipment	**Elective-III: SMS-** Simulation of Manufacturing Systems	**Elective-III:IAPE-** Industrial Applications of Power Electronics
24-04-2015	FRI	3	**Elective– IV: NSC** Network Security & Cryptography	**Elective-IV: ACT** Advanced Concrete Technology	**Elective-IV: ADCA-** Advanced Computer Architecture	**Elective-IV: VS-** VLSI Signal Processing	**Elective-IV:GTT-** Gas Turbine Theory	**Elective-IV:RPTM-** Rapid Prototyping, Tolling and manufacture	**Elective-IV:RES-** Renewable Energy Sources

Note:**1.** While filling the subjects in Application and Hall-tickets use only the abbreviations given above and maintain the same order.

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ADDITIONAL CONTROLLER OF EXAMINATIONS

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G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

M.Tech I – SEMESTER (Scheme-2009)

END (SUPPLEMENTARY) EXAMINATIONS – APRIL – 2015

Time : 9.00 A.M. to 12.00 Noon

Date	Day	Paper No.	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	Thermal Sciences and Energy Systems	CAD/CAM	Power Electronics
20.04.2015	MON	1	DSC -Data Structures Through C++	AEM- Advanced Engineering mathematics	DES -Detection and Estimation of Signals	PMA - Probability and Mathematical Analysis	CAD - Computer Aided Design	EMM - Electrical Machines Modelling
22.04.2015	WED	2	COA - Computer Organization and Architecture	TE – Theory of Elasticity	ADSP - Advanced Digital Signal Processing	ATD - Advanced Thermodynamics	CIM - Computer integrated Manufacturing	SSPC1- Solid State Power Converters-1
24.04.2015	FRI	3	OS - Operating System	ASA- Advanced Structural Analysis	ACM - Advanced Communications	RCG -Refrigeration and Cryogenics	CNCM – Computer Numerical Control Machines	SSPC2- Solid State Power Converters-2
27.04.2015	MON	4	DBMS – Data Base Management Systems	BE- Bridge Engineering	MWC - Microwave Communications	RES -Renewable Energy Sources	GM - Geometric modeling	DSP- Digital Signal Processing
29.04.2015	WED	5	DAA - Elective – I: Design and Analysis of Algorithms	TAP - Elective – I: Theory and Analysis of plates	ADMM - Elective – I: Advanced Microprocessors and Microcontrollers	ECT -Elective – I: Energy Conversion Technologies	AOT- Elective – I: Advanced Optimization Techniques	DCS – Elective– I: Digital Control Systems
01-05-2015	FRI	6	WT – Elective II: Web Technology	ACT - Elective – II: Advanced Concrete Technology	EMS - Elective –II: Embedded Systems	DACS -Elective– II: Design of Air Conditioning Systems	FAD – Elective – II: Failure analysis and Design	NNFL – Elective –II: Neural Networks and Fuzzy Logic

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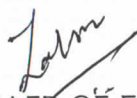
G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

M.Tech II – SEMESTER (Scheme-2009)

END (SUPPLEMENTARY) EXAMINATIONS – April – 2015

Time : 9.00 A.M. to 12.00 Noon

Date	Day	Paper No.	Computer Science and Engineering	Structural Engineering	VLSI & Embedded Systems	Power Electronics
21-04-2015	TUE	1	-------	------	-------	**SDCD -**Solid State DC Drives
23-04-2015	THU	2	------	-------	-------	**SACD –** Solid State AC Drives
25-04-2015	SAT	3	------	------	**LVD** Low Power VLSI Design	**HVDCFACT-** HVDC and FACTS
28-04-2015	TUE	4	------	**SD** - Structural Dynamics	**AES – II** Advanced Embedded Systems – II	**DEM** - Dynamics of Electrical Machines
30-04-2015	THU	5	-------	-------	**ASIC-Elective-III:** Application Specific Integrated Circuits	**SRVC-Elective – III :** Solid State Relay and static VAR Compensation
02-05-2015	SAT	6	**NSC- Elective – IV:** Network Security and Cryptography	------	**VS -Elective –IV:** VLSI Signal Processing	**ECS-Elective – IV:** Energy Conversion Systems

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