

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL
Four Year B. Tech Degree Examination: April-2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE IV - SEMESTER (S-23)

Scheme & Schedule for Regular Practical Examinations

BATCH	REGD. NOS							TOTAL
A	259XA04501	259XA04502	259XA04503	259XA04504	259XA04505	259XA04506	259XA04507	[13]
	259XA04508	259XA04509	259XA04510	259XA04511	259XA04512	259XA04513		
B	259XA04514	259XA04515	259XA04516	259XA04517	259XA04518	249XA04001	249XA04002	[13]
	249XA04003	249XA04004	249XA04005	249XA04006	249XA04007	249XA04008		
C	249XA04009	249XA04010	249XA04011	249XA04012	249XA04013	249XA04014	249XA04015	[13]
	249XA04016	249XA04017	249XA04018	249XA04019	249XA04020	249XA04021		
D	249XA04023	249XA04024	249XA04025	249XA04026	249XA04027	249XA04028	249XA04029	[13]
	249XA04030	249XA04031	249XA04032	249XA04033	249XA04034	249XA04035		
E	249XA04036	249XA04037	249XA04038	249XA04039	249XA04040	249XA04041	249XA04042	[13]
	249XA04043	249XA04044	249XA04045	249XA04046	249XA04047	249XA04048		
F	249XA04049	249XA04050	249XA04051	249XA04052	249XA04053	249XA04054	249XA04055	[13]
	249XA04056	249XA04057	249XA04058	249XA04059	249XA04060	249XA04061		
G	249XA04062	249XA04063	249XA04064	249XA04065	249XA04066	249XA04067	249XA04068	[13]
	249XA04069	249XA04070	249XA04071	249XA04072	249XA04073	249XA04074		
H	249XA04075	249XA04076	249XA04077	249XA04078	249XA04079	249XA04080	249XA04081	[13]
	249XA04082	249XA04083	249XA04084	249XA04085	249XA04086	249XA04087		
I	249XA04088	249XA04089	249XA04090	249XA04091	249XA04092	249XA04093	249XA04094	[13]
	249XA04095	249XA04096	249XA04097	249XA04098	249XA04099	249XA04100		
J	249XA04101	249XA04102	249XA04104	249XA04105	249XA04106	249XA04107	249XA04108	[13]
	249XA04109	249XA04110	249XA04111	249XA04112	249XA04113	249XA04114		
K	249XA04115	249XA04116	249XA04117	249XA04118	249XA04119	249XA04120	249XA04121	[13]
	249XA04122	249XA04123	249XA04124	249XA04125	249XA04126	249XA04127		
L	249XA04128	249XA04129	249XA04130	249XA04131	249XA04132	249XA04133	249XA04134	[13]
	249XA04135	249XA04136	249XA04137	249XA04138	249XA04139	249XA04141		
M	249XA04142	249XA04143	249XA04144	249XA04145	249XA04146	249XA04147	249XA04148	[13]
	249XA04149	249XA04150	249XA04151	249XA04152	249XA04153	249XA04154		
N	249XA04155	249XA04156	249XA04157	249XA04158	249XA04159	249XA04160	249XA04161	[14]
	249XA04162	249XA04163	249XA04164	249XA04165	249XA04166	249XA04167	249XA04168	
O	249XA04169	249XA04170	249XA04172	249XA04173	249XA04174	249XA04175	249XA04176	[14]
	249XA04177	249XA04178	249XA04179	249XA04180	249XA04181	249XA04182	249XA04183	
P	249XA04184	249XA04186	249XA04187	249XA04188	249XA04189	249XA04190	249XA04191	[14]
	249XA04193	249XA04194	249XA04195	249XA04196	249XA04197	249XA04198	239X1A04A7	

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Name of the Practical examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
Electronic Circuit Analysis Lab [ECA(P)]	AEC lab BLOCK 2	07-04-2026	A	B	C	D
		08-04-2026	E	F	G	H
		09-04-2026	I	J	K	L
		10-04-2026	M	N	O	P
Analog & Digital Communication lab [ADCM(P)]	Communications lab BLOCK 2	07-04-2026	M	N	O	P
		08-04-2026	A	B	C	D
		09-04-2026	E	F	G	H
		10-04-2026	I	J	K	L


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G.PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL
Four Year B. Tech Degree Examination: APRIL-2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE IV- SEMESTER (S-23)
Schedule for Soft Skills Viva-Voce Examination

BATCH	REGD. NOS							TOTAL
A	259XA04501	259XA04502	259XA04503	259XA04504	259XA04505	259XA04506	259XA04507	[18]
	259XA04508	259XA04509	259XA04510	259XA04511	259XA04512	259XA04513	259XA04514	
	259XA04515	259XA04516	259XA04517	259XA04518				
B	249XA04001	249XA04002	249XA04003	249XA04004	249XA04005	249XA04006	249XA04007	[18]
	249XA04008	249XA04009	249XA04010	249XA04011	249XA04012	249XA04013	249XA04014	
	249XA04015	249XA04016	249XA04017	249XA04018				
C	249XA04019	249XA04020	249XA04021	249XA04023	249XA04024	249XA04025	249XA04026	[18]
	249XA04027	249XA04028	249XA04029	249XA04030	249XA04031	249XA04032	249XA04033	
	249XA04034	249XA04035	249XA04036	249XA04037				
D	249XA04038	249XA04039	249XA04040	249XA04041	249XA04042	249XA04043	249XA04044	[18]
	249XA04045	249XA04046	249XA04047	249XA04048	249XA04049	249XA04050	249XA04051	
	249XA04052	249XA04053	249XA04054	249XA04055				
E	249XA04056	249XA04057	249XA04058	249XA04059	249XA04060	249XA04061	249XA04062	[18]
	249XA04063	249XA04064	249XA04065	249XA04066	249XA04067	249XA04068	249XA04069	
	249XA04070	249XA04071	249XA04072	249XA04073				
F	249XA04074	249XA04075	249XA04076	249XA04077	249XA04078	249XA04079	249XA04080	[18]
	249XA04081	249XA04082	249XA04083	249XA04084	249XA04085	249XA04086	249XA04087	
	249XA04088	249XA04089	249XA04090	249XA04091				
G	249XA04092	249XA04093	249XA04094	249XA04095	249XA04096	249XA04097	249XA04098	[18]
	249XA04099	249XA04100	249XA04101	249XA04102	249XA04104	249XA04105	249XA04106	
	249XA04107	249XA04108	249XA04109	249XA04110				
H	249XA04111	249XA04112	249XA04113	249XA04114	249XA04115	249XA04116	249XA04117	[18]
	249XA04118	249XA04119	249XA04120	249XA04121	249XA04122	249XA04123	249XA04124	
	249XA04125	249XA04126	249XA04127	249XA04128				
I	249XA04129	249XA04130	249XA04131	249XA04132	249XA04133	249XA04134	249XA04135	[18]
	249XA04136	249XA04137	249XA04138	249XA04139	249XA04141	249XA04142	249XA04143	
	249XA04144	249XA04145	249XA04146	249XA04147				
J	249XA04148	249XA04149	249XA04150	249XA04151	249XA04152	249XA04153	249XA04154	[18]
	249XA04155	249XA04156	249XA04157	249XA04158	249XA04159	249XA04160	249XA04161	
	249XA04162	249XA04163	249XA04164	249XA04165				
K	249XA04166	249XA04167	249XA04168	249XA04169	249XA04170	249XA04172	249XA04173	[18]
	249XA04174	249XA04175	249XA04176	249XA04177	249XA04178	249XA04179	249XA04180	
	249XA04181	249XA04182	249XA04183	249XA04184				
L	249XA04186	249XA04187	249XA04188	249XA04189	249XA04190	249XA04191	249XA04193	[13]
	249XA04194	249XA04195	249XA04196	249XA04197	249XA04198	239X1A04A7		

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Name of the Practical examination	Date	Venue	9.15AM -10.45AM	10.45AM -12.15PM	1.15PM 2.45PM	2.45PM 4.15PM
Soft Skills Viva Voce	13-04-2026	R&D Lab	A	B	C	D
		VLSI Lab	E	F	G	H
		IOT Lab	I	J	K	L

Venue details:

- 1. R&D Lab, Ground Floor, ECE Block - 3**
- 2. VLSI Lab, Ground Floor, ECE Block -1**
- 3. IOT Lab, Second Floor, ECE Block - 3**


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G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL
Four Year B. Tech Degree Examination: April-2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE VI- SEMESTER (S-23)
Scheme & Schedule for Regular Practical Examinations

<u>BATCH</u>	<u>REGD. NOS</u>							<u>TOTAL</u>
A	249X5A04L1	249X5A04L5	249X5A04M2	239X1A0419	239X1A0424	239X1A0452	239X1A0466	[11]
	239X1A0476	239X1A0477	239X1A04F4	239X1A04H9				
B	249X5A04K9	249X5A04L0	249X5A04L2	249X5A04L3	249X5A04L4	249X5A04L6	249X5A04L7	[13]
	249X5A04L8	249X5A04L9	249X5A04M0	249X5A04M1	249X5A04M3	249X5A04M4		
C	249X5A04M5	249X5A04M6	239X1A0401	239X1A0402	239X1A0403	239X1A0404	239X1A0405	[13]
	239X1A0406	239X1A0407	239X1A0408	239X1A0409	239X1A0410	239X1A0411		
D	239X1A0412	239X1A0413	239X1A0414	239X1A0415	239X1A0416	239X1A0417	239X1A0418	[13]
	239X1A0420	239X1A0421	239X1A0422	239X1A0423	239X1A0425	239X1A0426		
E	239X1A0427	239X1A0428	239X1A0429	239X1A0430	239X1A0431	239X1A0432	239X1A0433	[13]
	239X1A0434	239X1A0435	239X1A0436	239X1A0438	239X1A0439	239X1A0440		
F	239X1A0441	239X1A0442	239X1A0443	239X1A0444	239X1A0445	239X1A0446	239X1A0447	[13]
	239X1A0448	239X1A0449	239X1A0450	239X1A0451	239X1A0454	239X1A0455		
G	239X1A0456	239X1A0457	239X1A0458	239X1A0459	239X1A0460	239X1A0461	239X1A0462	[13]
	239X1A0463	239X1A0464	239X1A0465	239X1A0467	239X1A0468	239X1A0469		
H	239X1A0470	239X1A0471	239X1A0473	239X1A0474	239X1A0475	239X1A0478	239X1A0479	[13]
	239X1A0480	239X1A0481	239X1A0482	239X1A0483	239X1A0484	239X1A0485		
I	239X1A0486	239X1A0487	239X1A0488	239X1A0489	239X1A0490	239X1A0491	239X1A0492	[13]
	239X1A0493	239X1A0494	239X1A0495	239X1A0496	239X1A0497	239X1A0498		
J	239X1A0499	239X1A04A0	239X1A04A1	239X1A04A2	239X1A04A3	239X1A04A6	239X1A04A9	[13]
	239X1A04B0	239X1A04B1	239X1A04B2	239X1A04B3	239X1A04B4	239X1A04B5		
K	239X1A04B6	239X1A04B7	239X1A04B8	239X1A04B9	239X1A04C1	239X1A04C2	239X1A04C3	[13]
	239X1A04C4	239X1A04C5	239X1A04C6	239X1A04C7	239X1A04C8	239X1A04C9		
L	239X1A04D0	239X1A04D1	239X1A04D2	239X1A04D3	239X1A04D4	239X1A04D5	239X1A04D6	[13]
	239X1A04D7	239X1A04D8	239X1A04D9	239X1A04E0	239X1A04E1	239X1A04E2		
M	239X1A04E3	239X1A04E4	239X1A04E5	239X1A04E6	239X1A04E7	239X1A04E8	239X1A04E9	[13]
	239X1A04F0	239X1A04F1	239X1A04F2	239X1A04F3	239X1A04F5	239X1A04F6		
N	239X1A04F7	239X1A04F9	239X1A04G0	239X1A04G1	239X1A04G2	239X1A04G3	239X1A04G4	[13]
	239X1A04G5	239X1A04G6	239X1A04G7	239X1A04G8	239X1A04G9	239X1A04H0		
O	239X1A04H1	239X1A04H2	239X1A04H3	239X1A04H4	239X1A04H7	239X1A04H8	239X1A04J0	[13]
	239X1A04J1	239X1A04J2	239X1A04J4	239X1A04J5	239X1A04J6	239X1A04J7		
P	239X1A04J8	239X1A04J9	239X1A04K0	239X1A04K1	239X1A04K2	239X1A04K3	239X1A04K4	[13]
	239X1A04K5	239X1A04K6	239X1A04K7	239X1A04K8	229X1A04A0	229X1A04G1		

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Name of the Practical examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
MICROWAVE AND OPTICAL COMMUNICATIONS LAB (MWOC(P))	MICROWAVE LAB BLOCK 2	08-04-2026	A	B	C	D
		09-04-2026	E	F	G	H
		10-04-2026	I	J	K	L
		11-04-2026	M	N	O	P
VLSI DESIGN LAB (VLSID (P))	VLSI Lab BLOCK 1	08-04-2026	M	N	O	P
		09-04-2026	A	B	C	D
		10-04-2026	E	F	G	H
		11-04-2026	I	J	K	L
MACHINE LEARNING AND DSP LAB (ML&DSP (P))	IOT LAB BLOCK 3	08-04-2026	I	J	K	L
		09-04-2026	M	N	O	P
		10-04-2026	A	B	C	D
		11-04-2026	E	F	G	H


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G. PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL

Four Year B. Tech Degree Examination: April-2026

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT

Honors in ECE

B. Tech VI- SEMESTER (S-23)

Scheme & Schedule for Regular End Practical Examinations

	Lab name	Date & Time	Venue
Honors in ECE	Physical Design & Automation Lab (PDA(P))	11/04/2026 09:00 to 12:00 Noon	VLSI Lab (ECE Block 1)

REGD. NOS

TOTAL

239X1A0419 239X1A0424 239X1A0452 239X1A0466

[11]

239X1A0476 239X1A0477 239X1A04F4 239X1A04H9

249X5A04M2 249X5A04L1 249X5A04L5


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G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL
Four-year B. Tech Degree Examination: April-2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. TECH ECE VI- SEMESTER (S-20)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>ROLL NO'S</u>	<u>TOTAL</u>
A	219X1A04F7, 219X1A04J9	02

Name of the Practical examination	Venue	Date	Time
			(9.00AM - 12.00 Noon)
ECAD Lab [ECAD(P)]	VLSI lab	07.04.2026	A


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G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL
Four-year B. Tech Degree Examination: April-2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. TECH ECE III- SEMESTER (S-23)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	259XA04517, 239X1A04A4, 239X1A04A7	03

Name of the Practical examination	Venue	Date	Time
			(9.00AM - 12.00 Noon)
Python Programming Lab PPL(P)	IoT LAB BLOCK 3	12.04.2026	A

Prasanna
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G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL
Four-year B. Tech Degree Examination: April-2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. TECH ECE III- SEMESTER (S-23)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	249XA04034, 249XA04044, 249XA04048, 249XA04049, 249XA04050, 249XA04051, 249XA04054, 249XA04063, 249XA04100, 249XA04101, 249XA04141, 249XA04167, 249XA04170, 249XA04175, 249XA04196, 239X1A04A4	16

Name of the Practical examination	Venue	Date	Time
			(2.00 PM - 5.00 PM)
Digital Circuits & Signal Simulation Lab [DCSS(P)]	Digital Circuit Lab BLOCK 2	11.04.2026	A

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G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL
Four-year B. Tech Degree Examination: April-2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. TECH ECE V SEMESTER (S-23)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	239X1A0415 239X1A0426 239X1A04K7	03

Name of the Practical examination	Venue	Date	Time
			(9.00AM - 12.00 Noon)
Analog and Digital IC Applications Lab (ADICA(P))	LICA LAB BLOCK 2	13.04.2026	A


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G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL
Four-year B. Tech Degree Examination: April-2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. TECH ECE V SEMESTER (S-23)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	239X1A04K6	01

Name of the Practical examination	Venue	Date	Time
			(9.00AM - 12.00 Noon)
Microprocessors and Microcontrollers Lab (MPMC(P))	MPMC LAB BLOCK 2	13.04.2026	A


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