

G.PULLA REDDY ENGINEERING COLLEGE (AUTONOMOUS) :: KURNOOL
FOUR YEAR B.TECH DEGREE EXAMINATION: JUNE/JULY- 2022
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE IV- SEMESTER (S-20)
SCHEME & SCHEDULE FOR REGULAR PRACTICAL EXAMINATIONS

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	219X5A04K4 219X5A04K5 219X5A04K6 219X5A04K7 219X5A04K8 219X5A04K9 219X5A04L0 219X5A04L1 219X5A04L2 219X5A04L3 219X5A04L4 219X5A04L5 219X5A04L6	(13)
B	219X5A04L7 219X5A04L9 219X5A04M0 219X5A04M1 209X1A0401 209X1A0402 209X1A0403 209X1A0404 209X1A0405 209X1A0406 209X1A0407 209X1A0408 209X1A0409	(13)
C	209X1A0410 209X1A0411 209X1A0412 209X1A0413 209X1A0414 209X1A0415 209X1A0416 209X1A0417 209X1A0418 209X1A0419 209X1A0420 209X1A0421 209X1A0422	(13)
D	209X1A0423 209X1A0424 209X1A0425 209X1A0426 209X1A0427 209X1A0428 209X1A0429 209X1A0430 209X1A0431 209X1A0432 209X1A0433 209X1A0434 209X1A0435	(13)
E	209X1A0436 209X1A0437 209X1A0438 209X1A0439 209X1A0440 209X1A0441 209X1A0442 209X1A0443 209X1A0444 209X1A0445 209X1A0446 209X1A0447 209X1A0448	(13)
F	209X1A0449 209X1A0450 209X1A0451 209X1A0452 209X1A0453 209X1A0454 209X1A0455 209X1A0456 209X1A0457 209X1A0458 209X1A0459 209X1A0460 209X1A0461	(13)
G	209X1A0462 209X1A0463 209X1A0464 209X1A0465 209X1A0466 209X1A0467 209X1A0468 209X1A0469 209X1A0470 209X1A0471 209X1A0472 209X1A0473 209X1A0474	(13)
H	209X1A0475 209X1A0476 209X1A0477 209X1A0478 209X1A0479 209X1A0480 209X1A0481 209X1A0482 209X1A0483 209X1A0484 209X1A0485 209X1A0486 209X1A0487	(13)
I	209X1A0489 209X1A0490 209X1A0491 209X1A0492 209X1A0493 209X1A0494 209X1A0495 209X1A0496 209X1A0497 209X1A0498 209X1A0499 209X1A04A0 209X1A04A1	(13)
J	209X1A04A2 209X1A04A3 209X1A04A4 209X1A04A5 209X1A04A6 209X1A04A7 209X1A04A8 209X1A04A9 209X1A04B0 209X1A04B1 209X1A04B2 209X1A04B3 209X1A04B4	(13)
K	209X1A04B5 209X1A04B6 209X1A04B7 209X1A04B8 209X1A04B9 209X1A04C0 209X1A04C1 209X1A04C2 209X1A04C3 209X1A04C4 209X1A04C5 209X1A04C6 209X1A04C7	(13)
L	209X1A04C8 209X1A04C9 209X1A04D0 209X1A04D1 209X1A04D2 209X1A04D3 209X1A04D4 209X1A04D5 209X1A04D6 209X1A04D7 209X1A04D8 209X1A04D9 209X1A04E0	(13)
M	209X1A04E1 209X1A04E2 209X1A04E3 209X1A04E4 209X1A04E5 209X1A04E6 209X1A04E7 209X1A04E8 209X1A04E9 209X1A04F0 209X1A04F1 209X1A04F2 209X1A04F3	(13)
N	209X1A04F4 209X1A04F5 209X1A04F6 209X1A04F7 209X1A04F8 209X1A04F9 209X1A04G0 209X1A04G1 209X1A04G2 209X1A04G3 209X1A04G4 209X1A04G5 209X1A04G6	(13)
O	209X1A04G7 209X1A04G8 209X1A04G9 209X1A04H0 209X1A04H1 209X1A04H2 209X1A04H3 209X1A04H4 209X1A04H5 209X1A04H6 209X1A04H7 209X1A04H8 209X1A04H9	(13)
P	209X1A04J0 209X1A04J1 209X1A04J2 209X1A04J3 209X1A04J4 209X1A04J5 209X1A04J7 209X1A04J8 209X1A04J9 209X1A04K0 209X1A04K1 209X1A04K2 209X1A04K3	(13) Total: 208

Name of The Practical Examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
Analog Electronic Circuits Lab (AEC(P))	Analog Circuits Lab (ECE Block-2)	27.06.2022	A	B	C	D*
		28.06.2022	E	F	G	H
		29.06.2022	I	J	K	L
		30.06.2022	M	N	O	P
Analog and Digital Communication Lab (ADCM(P))	Communications Lab (ECE Block-2)	27.06.2022	E	F	G	H
		28.06.2022	I	J	K	L
		29.06.2022	M	N	O	P
		30.06.2022	A	B	C	D
Networks And Transmission Lines Lab (NTL(P))	EEE Dept	27.06.2022	I	J	K	L
		28.06.2022	M	N	O	P
		29.06.2022	A	B	C	D
		30.06.2022	E	F	G	H


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ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE VI- SEMESTER (S-17)

SCHEME & SCHEDULE FOR REGULAR PRACTICAL EXAMINATIONS

BATCH	REGD. NOS	TOTAL
A	209X5A04K4 209X5A04K5 209X5A04K6 209X5A04K7 209X5A04K8 209X5A04K9 209X5A04L0 209X5A04L1 209X5A04L2 209X5A04L3 209X5A04L4 209X5A04L5 209X5A04L6	(13)
B	209X5A04L7 209X5A04L8 209X5A04L9 209X5A04M0 199X1A0401 199X1A0402 199X1A0403 199X1A0404 199X1A0405 199X1A0406 199X1A0407 199X1A0408 199X1A0409	(13)
C	199X1A0410 199X1A0411 199X1A0412 199X1A0413 199X1A0414 199X1A0415 199X1A0416 199X1A0417 199X1A0418 199X1A0419 199X1A0420 199X1A0421 199X1A0422	(13)
D	199X1A0423 199X1A0424 199X1A0426 199X1A0427 199X1A0428 199X1A0429 199X1A0430 199X1A0431 199X1A0432 199X1A0433 199X1A0434 199X1A0435 199X1A0436	(13)
E	199X1A0437 199X1A0438 199X1A0439 199X1A0440 199X1A0441 199X1A0442 199X1A0443 199X1A0444 199X1A0445 199X1A0446 199X1A0447 199X1A0448 199X1A0449	(13)
F	199X1A0450 199X1A0451 199X1A0452 199X1A0453 199X1A0454 199X1A0455 199X1A0456 199X1A0457 199X1A0458 199X1A0459 199X1A0460 199X1A0461	(12)
G	199X1A0462 199X1A0463 199X1A0464 199X1A0465 199X1A0466 199X1A0467 199X1A0468 199X1A0469 199X1A0470 199X1A0471 199X1A0472 199X1A0473 199X1A0474	(13)
H	199X1A0475 199X1A0476 199X1A0477 199X1A0478 199X1A0479 199X1A0480 199X1A0481 199X1A0482 199X1A0483 199X1A0484 199X1A0485 199X1A0486	(12)
I	199X1A0487 199X1A0488 199X1A0489 199X1A0490 199X1A0491 199X1A0492 199X1A0493 199X1A0495 199X1A0496 199X1A0497 199X1A0498 199X1A0499 199X1A04A0	(13)
J	199X1A04A1 199X1A04A3 199X1A04A4 199X1A04A5 199X1A04A6 199X1A04A7 199X1A04A8 199X1A04B0 199X1A04B2 199X1A04B3 199X1A04B4 199X1A04B5	(12)
K	199X1A04B6 199X1A04B7 199X1A04B8 199X1A04B9 199X1A04C0 199X1A04C1 199X1A04C2 199X1A04C3 199X1A04C4 199X1A04C5 199X1A04C6 199X1A04C7 199X1A04C8	(13)
L	199X1A04C9 199X1A04D0 199X1A04D1 199X1A04D2 199X1A04D3 199X1A04D4 199X1A04D5 199X1A04D6 199X1A04D7 199X1A04D8 199X1A04D9 199X1A04E0	(12)
M	199X1A04E1 199X1A04E2 199X1A04E3 199X1A04E4 199X1A04E5 199X1A04E6 199X1A04E7 199X1A04E8 199X1A04E9 199X1A04F0 199X1A04F1 199X1A04F2 199X1A04F3	(13)
N	199X1A04F4 199X1A04F5 199X1A04F6 199X1A04F7 199X1A04F9 199X1A04G0 199X1A04G1 199X1A04G2 199X1A04G3 199X1A04G4 199X1A04G5 199X1A04G6	(12)
O	199X1A04G7 199X1A04G8 199X1A04G9 199X1A04H0 199X1A04H1 199X1A04H2 199X1A04H3 199X1A04H4 199X1A04H5 199X1A04H6 199X1A04H7 199X1A04H8 199X1A04H9	(13)
P	199X1A04J0 199X1A04J1 199X1A04J2 199X1A04J3 199X1A04J4 199X1A04J5 199X1A04J6 199X1A04J8 199X1A04J9 199X1A04K0 199X1A04K2 199X1A04K3	(12)

Total:202

Name of The Practical Examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
Digital Signal Processing Lab (DSP(P))	DSP Lab	29.06.2022	A	B	C	D*
		30.06.2022	E	F	G	H
		01.07.2022	I	J	K	L
		02.07.2022	M	N	O	P
VLSI & Embedded System Lab (VES(P))	Advanced VLSI Lab	29.06.2022	E	F	G	H
		30.06.2022	I	J	K	L
		01.07.2022	M	N	O	P
		02.07.2022	A	B	C	D


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 ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
 B. Tech ECE III- SEMESTER (S-17)
 SCHEME & SCHEDULE FOR END SUPPLEMENTARY PRACTICAL EXAMINATIONS**

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	199X1A04A3, 199X1A04J1	02

Name of the Practical Examination	Venue	Date	Time
			(10.00AM - 1.00 PM)
Basic Simulation lab(BS(P))	BS Lab ECE Block3	01.07.2022	A

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 ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
 B. Tech ECE IV- SEMESTER (S-17)
 SCHEME & SCHEDULE FOR END SUPPLEMENTARY PRACTICAL EXAMINATIONS**

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	199X1A04C0, 189X1A04E7	(02)
B	199X1A04C0	(01)

Name of the Practical Examination	Venue	Date	Batch	Time
Analog Electronic Circuits Lab (AEC(P))	AEC lab ECE Block-1	27.06.2022	D*	2.30PM-5.30PM
Electrical Circuits and Machines Lab (ECM(P))	EEE lab	02.07.2022	B	2:00 PM-05:00PM

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B. Tech ECE IV- SEMESTER (S-17)
SCHEME & SCHEDULE FOR END SUPPLEMENTARY PRACTICAL EXAMINATIONS

BATCH	REGD. NOS	TOTAL
A	199X1A0202	(01)

Name of the Practical Examination	Venue	Date	Batch	Time
Electronics Engineering-II EE 2(P)	ECE BLOCK 1	02-07-2022	A	10:00 AM -01:00 PM


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B. Tech ECE V- SEMESTER (S-17)
SCHEME & SCHEDULE FOR END SUPPLEMENTARY PRACTICAL EXAMINATIONS

BATCH	REGD. NOS	TOTAL
A	199X1A0241, 199X1A0254	(02)

Name of the Practical Examination	Venue	Date	Batch	Time
IC & Microprocessors Lab ICM (P) (ECE BLOCK-1)	ECE BLOCK 1	01-07-2022	A	10:00 AM -01:00 PM


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B. Tech ECE VI- SEMESTER (S-17)
SCHEME & SCHEDULE FOR END SUPPLEMENTARY PRACTICAL EXAMINATIONS**

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	189X1A0439, 189X1A0456	(02)

Name of the Practical Examination	Venue	Date	Batch	Time
Digital Signal Processing Lab (DSP(P))	DSP lab ECE Block-1	29.06.2022	D*	2.30PM - 5.30 PM


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<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	209X1A04C5,209X1A04J8	(02)
B	209X1A04C5	(01)
C	209X1A04D7	(01)

Name of the Practical Examination	Venue	Date	Batch	Time
Electronics & Devices Circuits Lab (EDC(P))	Analog Circuits Lab (ECE Block-2)	01.07.2022	A	10.00AM - 1.00 PM
Digital System Design Lab (DSD(P))	Pulse and Digital Circuits Lab (ECE Block-2)	01.07.2022	B	2.00PM - 5.00PM
Basic Simulation Lab (BS(P))	BS Lab (ECE Block-3)	01.07.2022	C	10.00AM - 1.00 PM


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