

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL
Four Year B.Tech Degree Examination: Oct/ Nov-2025
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B.Tech ECE III- SEMESTER (S-23)

Scheme & Schedule for Regular Practical Examinations

BATCH	REGD. NOS							TOTAL
A	259XA04501	259XA04502	259XA04503	259XA04504	259XA04505	259XA04506	259XA04507 259XA04508	[12]
B	259XA04513	259XA04514	259XA04515	259XA04516	259XA04517	259XA04518	249XA04001 249XA04002	[12]
C	249XA04007	249XA04008	249XA04009	249XA04010	249XA04011	249XA04012	249XA04013 249XA04014	[12]
D	249XA04019	249XA04020	249XA04021	249XA04023	249XA04024	249XA04025	249XA04026 249XA04027	[12]
E	249XA04032	249XA04033	249XA04034	249XA04035	249XA04036	249XA04037	249XA04038 249XA04039	[12]
F	249XA04044	249XA04045	249XA04046	249XA04047	249XA04048	249XA04049	249XA04050 249XA04051	[12]
G	249XA04056	249XA04057	249XA04058	249XA04059	249XA04060	249XA04061	249XA04062 249XA04063	[12]
H	249XA04068	249XA04069	249XA04070	249XA04071	249XA04072	249XA04073	249XA04074 249XA04075	[12]
I	249XA04080	249XA04081	249XA04082	249XA04083	249XA04084	249XA04085	249XA04086 249XA04087	[12]
J	249XA04092	249XA04093	249XA04094	249XA04095	249XA04096	249XA04097	249XA04098 249XA04099	[13]
K	249XA04106	249XA04107	249XA04108	249XA04109	249XA04110	249XA04111	249XA04112 249XA04113	[12]
L	249XA04118	249XA04119	249XA04120	249XA04121	249XA04122	249XA04123	249XA04124 249XA04125	[12]
M	249XA04130	249XA04131	249XA04132	249XA04133	249XA04134	249XA04135	249XA04136 249XA04137	[12]
N	249XA04143	249XA04144	249XA04145	249XA04146	249XA04147	249XA04148	249XA04149 249XA04150	[12]
O	249XA04155	249XA04156	249XA04157	249XA04158	249XA04159	249XA04160	249XA04161 249XA04162	[12]
P	249XA04167	249XA04168	249XA04169	249XA04170	249XA04172	249XA04173	249XA04174 249XA04175	[12]
Q	249XA04180	249XA04181	249XA04182	249XA04183	249XA04184	249XA04185	249XA04186 249XA04187	[11]
R	249XA04191	249XA04192	249XA04193	249XA04194	249XA04195	249XA04196	249XA04197 249XA04198	[10+1]
	239X1A04A4	239X1A04A7	239X1A0453*					

TOTAL: 214+1=215

Name of the Practical examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
Digital Circuits & Signal Simulation Lab (DCSS (P))	VLSI LAB BLOCK 1	29-10-2025	A	B	C	D
		30-10-2025	E	F	G	H
		31-10-2025	I	J	K	L
		01-11-2025	M	N	O	P
		02-11-2025	Q	R*	-	-
Electronic Devices & Circuits Lab (EDC (P))	AEC LAB BLOCK 2	29-10-2025	E	F	G	H
		30-10-2025	I	J	K	L
		31-10-2025	M	N	O	P
		01-11-2025	Q	R	A	B
		02-11-2025	C	D	-	-
Python Programming Lab (PPL (P))	IOT LAB BLOCK 3	29-10-2025	I	J	K	L
		30-10-2025	M	N	O	P
		31-10-2025	Q	R*	A	B
		01-11-2025	C	D	E	F
		02-11-2025	G	H	-	-


PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL
Four Year B.Tech Degree Examination: Oct/ Nov-2025
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B.Tech ECE V- SEMESTER (S-23)
Scheme & Schedule for Regular Practical Examinations

BATCH	REGD. NOS							TOTAL
A.	249X5A04K9	249X5A04L0	249X5A04L1	249X5A04L2	249X5A04L3	249X5A04L4	249X5A04L5 249X5A04L6	[14]
B.	249X5A04M1	249X5A04M2	249X5A04M3	249X5A04M4	249X5A04M5	249X5A04M6	239X1A0401 239X1A0402	[14]
C.	239X1A0407	239X1A0408	239X1A0409	239X1A0410	239X1A0411	239X1A0412	239X1A0413 239X1A0414	[12]
D.	239X1A0420	239X1A0421	239X1A0422	239X1A0423	239X1A0425	239X1A0426	239X1A0427 239X1A0428	[10]
E.	239X1A0431	239X1A0432	239X1A0433	239X1A0434	239X1A0435	239X1A0436	239X1A0438 239X1A0439	[13]
F.	239X1A0444	239X1A0445	239X1A0446	239X1A0447	239X1A0448	239X1A0449	239X1A0450 239X1A0451	[12]
G.	239X1A0457	239X1A0458	239X1A0459	239X1A0460	239X1A0461	239X1A0462	239X1A0463 239X1A0464	[11]
H.	239X1A0469	239X1A0470	239X1A0471	239X1A0473	239X1A0474	239X1A0475	239X1A0478 239X1A0479	[10]
I.	239X1A0482	239X1A0483	239X1A0484	239X1A0485	239X1A0486	239X1A0487	239X1A0488 239X1A0489	[12]
J.	239X1A0494	239X1A0495	239X1A0496	239X1A0497	239X1A0498	239X1A0499	239X1A04A0 239X1A04A1	[12]
K.	239X1A04A9	239X1A04B0	239X1A04B1	239X1A04B2	239X1A04B3	239X1A04B4	239X1A04B5 239X1A04B6	[12]
L.	239X1A04C2	239X1A04C3	239X1A04C4	239X1A04C5	239X1A04C6	239X1A04C7	239X1A04C8 239X1A04C9	[12]
M.	239X1A04D4	239X1A04D5	239X1A04D6	239X1A04D7	239X1A04D8	239X1A04D9	239X1A04E0 239X1A04E1	[11]
N.	239X1A04E5	239X1A04E6	239X1A04E7	239X1A04E8	239X1A04E9	239X1A04F0	329X1A04F1 239X1A04F2	[11]
O.	239X1A04F6	239X1A04F7	239X1A04F8	239X1A04F9	239X1A04G0	239X1A04G1	239X1A04G2 239X1A04G3	[11]
P.	239X1A04G7	239X1A04G8	239X1A04G9	239X1A04H0	239X1A04H1	239X1A04H2	239X1A04H3 239X1A04H4	[12]
Q.	239X1A04J0	239X1A04J1	239X1A04J2	239X1A04J3	239X1A04J4	239X1A04J5	239X1A04J6 239X1A04J7	[11]
R.	239X1A04K1	239X1A04K2	239X1A04K4	239X1A04K5	239X1A04K6	239X1A04K7	239X1A04K8 229X1A04A0	[10]

TOTAL 210

Name of the Practical examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
Analog and Digital IC Applications Lab (ADICA (P))	IC Applications Lab ECE BLOCK-2	29-10-2025	A	B	C	D
		30-10-2025	E	F	G	H
		31-10-2025	I	J	K	L
		01-11-2025	M	N	O	P
		02-11-2025	Q	R	-	-
Microprocessors and Microcontrollers Lab (MPMC (P))	Microprocessors Lab ECE BLOCK-2	29-10-2025	E	F	G	H
		30-10-2025	I	J	K	L
		31-10-2025	M	N	O	P
		01-11-2025	Q	R	A	B
		02-11-2025	C	D	-	-
PCB Design and Prototype Development (PCBD (P))	DSP LAB ECE BLOCK-1	29-10-2025	I	J	K	L
		30-10-2025	M	N	O	P
		31-10-2025	Q	R	A	B
		01-11-2025	C	D	E	F
		02-11-2025	G	H	-	-


PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL
Four Year B.Tech Degree Examination: Oct/Nov-2025
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B.Tech ECE V- SEMESTER (S-23)

Scheme & Schedule for Regular Practical Examinations

BATCH	REGD. NOS							TOTAL
A	249X5A04K9	249X5A04L0	249X5A04L1	249X5A04L2	249X5A04L3	249X5A04L4	249X5A04L5 249X5A04L6 249X5A04L7 249X5A04L8 249X5A04L9 249X5A04M0 249X5A04M1 249X5A04M2 249X5A04M3 249X5A04M4 249X5A04M5 249X5A04M6 239X1A0401 239X1A0402	[20]
B	239X1A0403	239X1A0404	239X1A0405	239X1A0406	239X1A0407	239X1A0408	239X1A0409 239X1A0410 239X1A0411 239X1A0412 239X1A0413 239X1A0414 239X1A0415 239X1A0416 239X1A0417 239X1A0418 239X1A0419 239X1A0420 239X1A0421 239X1A0422	[20]
C	239X1A0423	239X1A0424	239X1A0425	239X1A0426	239X1A0427	239X1A0428	239X1A0429 239X1A0430 239X1A0431 239X1A0432 239X1A0433 239X1A0434 239X1A0435 239X1A0436 239X1A0438 239X1A0439 239X1A0440 239X1A0441 239X1A0442 239X1A0443	[20]
D	239X1A0444	239X1A0445	239X1A0446	239X1A0447	239X1A0448	239X1A0449	239X1A0450 239X1A0451 239X1A0452 239X1A0453 239X1A0454 239X1A0455 239X1A0456 239X1A0457 239X1A0458 239X1A0459 239X1A0460 239X1A0461 239X1A0462 239X1A0463 239X1A0464	[20]
E	239X1A0465	239X1A0466	239X1A0467	239X1A0468	239X1A0469	239X1A0470	239X1A0471 239X1A0473 239X1A0474 239X1A0475 239X1A0476 239X1A0477 239X1A0478 239X1A0479 239X1A0480 239X1A0481 239X1A0482 239X1A0483 239X1A0484 239X1A0485	[20]
F	239X1A0486	239X1A0487	239X1A0488	239X1A0489	239X1A0490	239X1A0491	239X1A0492 239X1A0493 239X1A0494 239X1A0495 239X1A0496 239X1A0497 239X1A0498 239X1A0499 239X1A04A0 239X1A04A1 239X1A04A2 239X1A04A3 239X1A04A5 239X1A04A6	[20]
G	239X1A04A9	239X1A04B0	239X1A04B1	239X1A04B2	239X1A04B3	239X1A04B4	239X1A04B5 239X1A04B6 239X1A04B7 239X1A04B8 239X1A04B9 239X1A04C1 239X1A04C2 239X1A04C3 239X1A04C4 239X1A04C5 239X1A04C6 239X1A04C7 239X1A04C8 239X1A04C9	[20]
H	239X1A04D0	239X1A04D1	239X1A04D2	239X1A04D3	239X1A04D4	239X1A04D5	239X1A04D6 239X1A04D7 239X1A04D8 239X1A04D9 239X1A04E0 239X1A04E1 239X1A04E2 239X1A04E3 239X1A04E4 239X1A04E5 239X1A04E6 239X1A04E7 239X1A04E8 239X1A04E9	[20]
I	239X1A04F0	239X1A04F1	239X1A04F2	239X1A04F3	239X1A04F4	239X1A04F5	239X1A04F6 239X1A04F7 239X1A04F8 239X1A04F9 239X1A04G0 239X1A04G1 239X1A04G2 239X1A04G3 239X1A04G4 239X1A04G5 239X1A04G6 239X1A04G7 239X1A04G8 239X1A04G9	[20]
J	239X1A04H0	239X1A04H1	239X1A04H2	239X1A04H3	239X1A04H4	239X1A04H7	239X1A04H8 239X1A04H9 239X1A04J0 239X1A04J1 239X1A04J2 239X1A04J3 239X1A04J4 239X1A04J5 239X1A04J6 239X1A04J7 239X1A04J8 239X1A04J9 239X1A04K0 239X1A04K1	[20]
K	239X1A04K2	239X1A04K3	239X1A04K4	239X1A04K5	239X1A04K6	239X1A04K7	239X1A04K8 229X1A04A0 229X1A04G1 229X1A04J1	[10]

Total : 210

Name of the Practical examination	Venue	Date	9.00AM to 11.00AM	10.00AM to 12.00PM	11.00PM to 1.00PM	12.00PM to 2.00PM	1.00 PM to 3.00 PM	2.00 PM to 4.00 PM
Tinkering Lab (TL(P))	Tinkering Lab - CSM Block	03/11/2025	A	B	C	D	E	F
		04/11/2025	G	H	I	J	K	-


PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL
Four Year B.Tech Degree Examination: OCT/NOV- 2025
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE VII- SEMESTER (S-20)
Scheme & Schedule for Regular Practical Examinations

<u>BATCH</u>	<u>REGD. NOS</u>						<u>TOTAL</u>
A	239X5A04J4	239X5A04J5	239X5A04J6	239X5A04J7	239X5A04J8	239X5A04J9	[35]
	239X5A04K0	239X5A04K1	239X5A04K2	239X5A04K3	239X5A04K4	239X5A04K5	
	239X5A04K6	239X5A04K7	239X5A04K8	239X5A04K9	239X5A04L0	239X5A04L1	
	239X5A04L2	239X5A04L3	239X5A04L4	239X5A04L6	239X5A04L7	239X5A04L8	
	239X5A04L9	239X5A04M0	239X5A04M1	239X5A04M2	239X5A04M3	239X5A04M4	
	239X5A04M5	239X5A04M6	229X1A0401	229X1A0402	229X1A0403		
B	229X1A0404	229X1A0405	229X1A0406	229X1A0407	229X1A0408	229X1A0409	[35]
	229X1A0410	229X1A0411	229X1A0412	229X1A0413	229X1A0414	229X1A0415	
	229X1A0416	229X1A0417	229X1A0418	229X1A0419	229X1A0420	229X1A0421	
	229X1A0422	229X1A0423	229X1A0424	229X1A0425	229X1A0426	229X1A0427	
	229X1A0428	229X1A0429	229X1A0430	229X1A0431	229X1A0432	229X1A0433	
	229X1A0434	229X1A0436	229X1A0437	229X1A0438	229X1A0439		
C	229X1A0440	229X1A0441	229X1A0442	229X1A0443	229X1A0444	229X1A0446	[35]
	229X1A0447	229X1A0448	229X1A0449	229X1A0450	229X1A0451	229X1A0452	
	229X1A0453	229X1A0454	229X1A0455	229X1A0456	229X1A0457	229X1A0458	
	229X1A0459	229X1A0460	229X1A0461	229X1A0462	229X1A0463	229X1A0464	
	229X1A0465	229X1A0466	229X1A0467	229X1A0468	229X1A0469	229X1A0470	
	229X1A0471	229X1A0472	229X1A0473	229X1A0474	229X1A0475		
D	229X1A0476	229X1A0477	229X1A0478	229X1A0479	229X1A0480	229X1A0481	[35]
	229X1A0482	229X1A0483	229X1A0484	229X1A0485	229X1A0486	229X1A0488	
	229X1A0489	229X1A0490	229X1A0491	229X1A0492	229X1A0493	229X1A0494	
	229X1A0495	229X1A0496	229X1A0497	229X1A0498	229X1A0499	229X1A04A1	
	229X1A04A2	229X1A04A3	229X1A04A4	229X1A04A5	229X1A04A6	229X1A04A7	
	229X1A04A8	229X1A04A9	229X1A04B0	229X1A04B1	229X1A04B2		
E	229X1A04B3	229X1A04B4	229X1A04B5	229X1A04B6	229X1A04B7	229X1A04B8	[36]
	229X1A04B9	229X1A04C0	229X1A04C1	229X1A04C2	229X1A04C3	229X1A04C4	
	229X1A04C5	229X1A04C6	229X1A04C7	229X1A04C8	229X1A04C9	229X1A04D0	
	229X1A04D1	229X1A04D2	229X1A04D3	229X1A04D4	229X1A04D5	229X1A04D6	
	229X1A04D7	229X1A04D8	229X1A04D9	229X1A04E0	229X1A04E1	229X1A04E2	
	229X1A04E3	229X1A04E4	229X1A04E5	229X1A04E6	229X1A04E7	229X1A04E8	
F	229X1A04E9	229X1A04F0	229X1A04F1	229X1A04F2	229X1A04F3	229X1A04F4	[35]
	229X1A04F5	229X1A04F6	229X1A04F7	229X1A04F8	229X1A04F9	229X1A04G0	
	229X1A04G2	229X1A04G3	229X1A04G4	229X1A04G5	229X1A04G6	229X1A04G7	
	229X1A04G8	229X1A04G9	229X1A04H0	229X1A04H1	229X1A04H2	229X1A04H3	
	229X1A04H4	229X1A04H5	229X1A04H6	229X1A04H7	229X1A04H8	229X1A04H9	
	229X1A04J0	229X1A04J2	229X1A04J3	219X1A04F7	219X1A04G4		

Total:211

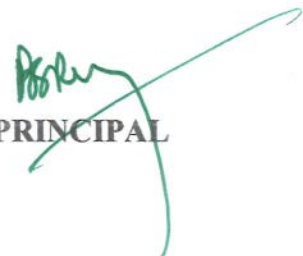
Name of the Practical examination	Venue	Date	8.30AM-11.30AM	11.30AM-2.30PM	2.30PM-5.30PM
DATA SCIENCE (DS(P))	IoT LAB BLOCK 3 ECE DEPT	29/10/2025	A	B	C
		30/10/2025	D	E	F


PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL
Four year B. Tech Degree Examination: November, 2025
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B.TECH ECE VI- SEMESTER (S-20)
Scheme of schedule for Supplementary Practical examinations

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
A	229X1A04B4 229X1A04C4 229X1A04F2 229X1A04G9 229X1A04H6 219X1A04F7 219X1A04J9	07
B	239X5A04L2 239X5A04M0 239X5A04M4 229X1A04J3 219X1A04F7	05

Name of the Practical examination	Venue	Date	Time
			(9:00 AM – 12:00 Noon)
E-CAD Lab (E-CAD(P))	VLSI LAB BLOCK 1	03/11/2025	A
Digital Signal Processing Lab (DSP(P))	DSP LAB BLOCK 1	04/11/2025	B


PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL

Four Year B.Tech Degree Examination: November-2025

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT

Honors in ECE

B. Tech V- SEMESTER (S-23)

Scheme & Schedule for Regular Practical Examinations

	Lab name	Date & Time	Venue
Honors in ECE	Analog and Digital IC Design Lab (ADID(P))	02/11/2025 12:30 to 3:30 PM	VLSI Lab (ECE Block 1)

REGD. NOS

TOTAL

239X1A0419	239X1A0424	239X1A0452
239X1A0466	239X1A0476	239X1A0477
239X1A04F4	239X1A04H9	249X5A04L1
249X5A04L5	249X5A04M1	249X5A04M2

[12]


PRINCIPAL