

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): Kurnool
Four Year B. Tech Degree End Practical Examinations: May/June- 2023
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE VI- SEMESTER (S-20)

Scheme & Schedule for Regular End Practical Examinations

Name of the Lab: E-CAD. DSP, MOC, MADESP

Date:09-06-2023

Batch	Roll Numbers								STR
A	219X5A04K4	219X5A04K5	219X5A04K6	219X5A04K7	219X5A04K8	219X5A04K9	219X5A04L0	219X5A04L1	13
B	219X5A04L7	219X5A04L8	219X5A04L9	219X5A04M0	219X5A04M1	209X1A0401	209X1A0402	209X1A0403	13
C	209X1A0409	209X1A0410	209X1A0411	209X1A0412	209X1A0413	209X1A0414	209X1A0415	209X1A0416	13
D	209X1A0423	209X1A0424	209X1A0425	209X1A0426	209X1A0427	209X1A0428	209X1A0429	209X1A0430	13
E	209X1A0436	209X1A0437	209X1A0438	209X1A0439	209X1A0440	209X1A0441	209X1A0442	209X1A0443	13
F	209X1A0449	209X1A0450	209X1A0451	209X1A0452	209X1A0453	209X1A0454	209X1A0455	209X1A0456	13
G	209X1A0462	209X1A0463	209X1A0464	209X1A0465	209X1A0466	209X1A0467	209X1A0468	209X1A0469	13
H	209X1A0475	209X1A0476	209X1A0477	209X1A0478	209X1A0479	209X1A0480	209X1A0481	209X1A0482	13
I	209X1A0488	209X1A0489	209X1A0490	209X1A0491	209X1A0492	209X1A0493	209X1A0494	209X1A0495	13
J	209X1A04A1	209X1A04A2	209X1A04A3	209X1A04A4	209X1A04A5	209X1A04A6	209X1A04A7	209X1A04A8	13
K	209X1A04B4	209X1A04B5	209X1A04B6	209X1A04B7	209X1A04B8	209X1A04B9	209X1A04C0	209X1A04C1	13
L	209X1A04C7	209X1A04C8	209X1A04C9	209X1A04D0	209X1A04D1	209X1A04D2	209X1A04D3	209X1A04D4	13
M	209X1A04E0	209X1A04E1	209X1A04E2	209X1A04E3	209X1A04E4	209X1A04E5	209X1A04E6	209X1A04E7	13
N	209X1A04F3	209X1A04F4	209X1A04F5	209X1A04F6	209X1A04F7	209X1A04F8	209X1A04F9	209X1A04G0	13
O	209X1A04G6	209X1A04G7	209X1A04G8	209X1A04G9	209X1A04H0	209X1A04H1	209X1A04H2	209X1A04H3	13
P	209X1A04H9	209X1A04J0	209X1A04J1	209X1A04J2	209X1A04J3	209X1A04J4	209X1A04J5	209X1A04J7	14
Total									209

Name of the Practical Examination	Venue	Date	08:30 AM-11:30AM	10:30AM -01:30PM	12:30PM-03:30PM	02:30PM-05:30PM
Electronic Computer Aided Design Lab (E-CAD(P))	AVLSI LAB	15-06-23	A	B	C	D
		16-06-23	E	F	G	H
		17-06-23	I	J	K	L
		18-06-23	M	N	O	P
Digital Signal Processing Lab (DSP(P))	DSP LAB	15-06-23	M	N	O	P
		16-06-23	A	B	C	D
		17-06-23	E	F	G	H
		18-06-23	I	J	K	L
Microwave and Optical Communications lab (MOC(P))	MOC LAB	15-06-23	I	J	K	L
		16-06-23	M	N	O	P
		17-06-23	A	B	C	D
		18-06-23	E	F	G	H
Mobile App Development and Embedded System Programming Lab (MADESP(P))	MADESP LAB	15-06-23	E	F	G	H
		16-06-23	I	J	K	L
		17-06-23	M	N	O	P
		18-06-23	A	B	C	D


PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): Kurnool
Four Year B. Tech Degree Supplementary Practical Examinations: June- 2023
Electronics & Communication Engineering Department
B. Tech ECE V- SEMESTER (S-20)
Internal & External Examiners Schedule for Supplementary Practical Examinations.

Date: 08.06.2023

Submitted to Principal:

Sub: ECE department- Lab Schedule- May/June - 2023-Reg.

The following is the schedule for conducting Supplementary Practical Examinations, May/June-23 and the list of appointed examiners. This is for your kind perusal and approval of the same

Class & Sem	Scheme	Name of the lab	Date &Time of Examination
Supplementary Practical Examinations			
B.Tech V SEM ECE	S-20	Microprocessors & Microcontrollers Lab (MPMC(P)) 219X5A04L0,219X5A04L5 209X1A0431,209X1A0439	14-06-2023 02:00 PM-05:00PM


PRINCIPAL