G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL

Four Year B.Tech Degree Examination: November-2026

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT

B. Tech ECE III- SEMESTER (S-23)

Scheme & Schedule for Regular Practical Examinations

BATCH	REGD. NOS	TOTAL
A	269XA04501 269XA04502 269XA04503 269XA04504 269XA04505 269XA04506 269XA04507 269XA04508 269XA04509 269XA04510 269XA04511 269XA04512	[12]
B	269XA04513 269XA04514 269XA04515 269XA04516 269XA04517 269XA04518 269XA04519 269XA04520 269XA04521 259XA04001 259XA04002 259XA04003	[12]
C	259XA04004 259XA04005 259XA04006 259XA04007 259XA04008 259XA04009 259XA04010 259XA04011 259XA04012 259XA04013 259XA04014 259XA04015	[12]
D	259XA04016 259XA04017 259XA04018 259XA04019 259XA04020 259XA04021 259XA04022 259XA04023 259XA04024 259XA04025 259XA04026 259XA04027	[12]
E	259XA04028 259XA04029 259XA04030 259XA04031 259XA04032 259XA04033 259XA04034 259XA04035 259XA04036 259XA04037 259XA04038 259XA04039	[12]
F	259XA04040 259XA04042 259XA04043 259XA04044 259XA04045 259XA04046 259XA04047 259XA04048 259XA04049 259XA04050 259XA04051 259XA04052	[12]
G	259XA04053 259XA04054 259XA04055 259XA04056 259XA04057 259XA04058 259XA04059 259XA04060 259XA04061 259XA04063 259XA04064 259XA04067	[12]
H	259XA04068 259XA04069 259XA04071 259XA04072 259XA04073 259XA04074 259XA04075 259XA04076 259XA04077 259XA04078 259XA04079 259XA04080	[12]
I	259XA04081 259XA04082 259XA04083 259XA04084 259XA04085 259XA04086 259XA04087 259XA04088 259XA04089 259XA04090 259XA04091 259XA04092	[12]
J	259XA04093 259XA04094 259XA04095 259XA04096 259XA04097 259XA04098 259XA04099 259XA04100 259XA04101 259XA04102 259XA04103 259XA04104	[12]
K	259XA04105 259XA04106 259XA04107 259XA04108 259XA04109 259XA04110 259XA04111 259XA04112 259XA04113 259XA04114 259XA04115 259XA04116	[12]
L	259XA04117 259XA04118 259XA04120 259XA04121 259XA04123 259XA04124 259XA04125 259XA04126 259XA04127 259XA04128 259XA04129 259XA04130	[12]
M	259XA04131 259XA04132 259XA04133 259XA04134 259XA04135 259XA04136 259XA04137 259XA04138 259XA04139 259XA04140 259XA04141 259XA04142	[12]
N	259XA04143 259XA04144 259XA04145 259XA04146 259XA04147 259XA04148 259XA04149 259XA04150 259XA04151 259XA04152 259XA04153 259XA04154	[12]
O	259XA04155 259XA04156 259XA04157 259XA04158 259XA04159 259XA04160 259XA04161 259XA04163 259XA04164 259XA04165 259XA04166 259XA04167	[12]
P	259XA04168 259XA04169 259XA04170 259XA04171 259XA04173 259XA04174 259XA04175 259XA04176 259XA04177 259XA04178 259XA04179 259XA04180	[12]
Q	259XA04181 259XA04182 259XA04183 259XA04184 259XA04186 259XA04187 259XA04188 259XA04189 259XA04190 259XA04191 259XA04192 259XA04193	[12]
R	259XA04194 259XA04195 259XA04196 259XA04197 249XA04140 249XA04175* 249XA04196*	[7]
S	249XA04172*	[1]

TOTAL = 212

Name of the Practical examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
Digital Circuits & Signal Simulation Lab (DCSS(P))	VLSI LAB BLOCK 1	09-11-2026	A	B	C	D
		10-11-2026	E	F	G	H
		11-11-2026	I	J	K	L
		12-11-2026	M	N	O	P
		13-11-2026	Q	R,S*	-	-
Electronic Devices & Circuits Lab (EDC(P))	AEC LAB BLOCK 2	09-11-2026	E	F	G	H
		10-11-2026	I	J	K	L
		11-11-2026	M	N	O	P,S*
		12-11-2026	Q	R	A	B
		13-11-2026	C	D	-	-
Python Programming Lab (PPL(P))	IOT LAB BLOCK 3	09-11-2026	I	J	K	L
		10-11-2026	M	N	O	P
		11-11-2026	Q	R	A	B
		12-11-2026	C,S*	D	E	F
		13-11-2026	G	H	-	-

PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL

Four Year B.Tech Degree Examination: Nov-2026

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT

B.Tech ECE V- SEMESTER (S-23)

Scheme & Schedule for Regular Practical Examinations

BATCH	REGD. NOS	TOTAL
A.	259XA04501 259XA04502 259XA04503 259XA04504 259XA04505 259XA04506 259XA04507 259XA04508 259XA04509 259XA04510 259XA04511 259XA04512 259XA04513	[13]
B.	259XA04514 259XA04515 259XA04516 259XA04517 259XA04518 249XA04001 249XA04002 249XA04003 249XA04004 249XA04005 249XA04006 249XA04007 249XA04008	[13]
C.	249XA04009 249XA04010 249XA04011 249XA04012 249XA04013 249XA04014 249XA04015 249XA04016 249XA04017 249XA04018 249XA04019 249XA04020 249XA04021	[13]
D.	249XA04023 249XA04024 249XA04025 249XA04026 249XA04027 249XA04028 249XA04029 249XA04030 249XA04031 249XA04032 249XA04033 249XA04034 249XA04035	[13]
E.	249XA04036 249XA04037 249XA04038 249XA04039 249XA04040 249XA04041 249XA04042 249XA04043 249XA04044 249XA04045 249XA04046 249XA04047 249XA04048	[13]
F.	249XA04049 249XA04050 249XA04051 249XA04052 249XA04053 249XA04054 249XA04055 249XA04056 249XA04057 249XA04058 249XA04059 249XA04061 249XA04062	[13]
G.	249XA04064 249XA04065 249XA04066 249XA04067 249XA04068 249XA04069 249XA04070 249XA04071 249XA04072 249XA04073 249XA04074 249XA04075 249XA04076	[13]
H.	249XA04077 249XA04078 249XA04079 249XA04080 249XA04081 249XA04082 249XA04083 249XA04084 249XA04085 249XA04086 249XA04087 249XA04088 249XA04089	[13]
I.	249XA04090 249XA04091 249XA04092 249XA04093 249XA04094 249XA04095 249XA04096 249XA04097 249XA04098 249XA04099 249XA04100 249XA04101 249XA04102	[13]
J.	249XA04104 249XA04105 249XA04106 249XA04107 249XA04108 249XA04109 249XA04110 249XA04111 249XA04112 249XA04113 249XA04114 249XA04115 249XA04116	[13]
K.	249XA04117 249XA04118 249XA04119 249XA04120 249XA04121 249XA04122 249XA04123 249XA04124 249XA04125 249XA04126 249XA04127 249XA04128 249XA04129	[13]
L.	249XA04130 249XA04131 249XA04132 249XA04133 249XA04134 249XA04135 249XA04136 249XA04137 249XA04138 249XA04139 249XA04141 249XA04142 249XA04143	[13]
M.	249XA04144 249XA04145 249XA04146 249XA04147 249XA04148 249XA04149 249XA04150 249XA04151 249XA04152 249XA04153 249XA04154 249XA04155 249XA04156	[13]
N.	249XA04157 249XA04158 249XA04159 249XA04160 249XA04161 249XA04162 249XA04163 249XA04164 249XA04165 249XA04166 249XA04167 249XA04168 249XA04169 249XA04170	[14]
O.	249XA04172 249XA04173 249XA04174 249XA04175 249XA04176 249XA04177 249XA04178 249XA04179 249XA04180 249XA04181 249XA04182 249XA04183 249XA04184 249XA04186	[14]
P.	249XA04187 249XA04188 249XA04189 249XA04190 249XA04191 249XA04193 249XA04194 249XA04195 249XA04196 249XA04197 249XA04198 239X1A0453 239X1A04A7 239X1A04H6	[14]

TOTAL 211

Name of the Practical examination	Venue	Date	8.30AM-11.30AM	10.30AM-1.30PM	12.30PM-3.30PM	2.30PM-5.30PM
Analog and Digital IC Applications Lab(ADICA(P))	IC Applications Lab ECE BLOCK-2	09-11-2026	A	B	C	D
		10-11-2026	E	F	G	H
		11-11-2026	I	J	K	L
		12-11-2026	M	N	O	P
Microprocessors and Microcontrollers Lab (MPMC(P))	Microprocessors Lab ECE BLOCK-2	09-11-2026	M	N	O	P
		10-11-2026	A	B	C	D
		11-11-2026	E	F	G	H
		12-11-2026	I	J	K	L
PCB Design and Prototype Development (PCBD(P))	DSP LAB ECE BLOCK-1	09-11-2026	I	J	K	L
		10-11-2026	M	N	O	P
		11-11-2026	A	B	C	D
		12-11-2026	E	F	G	H

PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL
Four Year B.Tech Degree Examination: Nov 2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B.Tech ECE V- SEMESTER (S-23)
Scheme & Schedule for Regular Practical Examinations

BATCH	REGD. NOS	TOTAL
A	249XA04002 249XA04003 249XA04004 249XA04005 249XA04006 249XA04017 249XA04019 249XA04047 249XA04057 249XA04067 249XA04069 249XA04070 249XA04077 249XA04087 249XA04089 249XA04099 249XA04107 249XA04120 249XA04125 249XA04130	[20]
B	249XA04142 249XA04148 249XA04149 249XA04158 249XA04165 249XA04173 249XA04187 249XA04193 249XA04198 259XA04502 259XA04503 259XA04504 259XA04507 259XA04508 259XA04509 259XA04510 259XA04512 259XA04514 259XA04515	[19]
C	259XA04501 259XA04505 259XA04506 259XA04511 259XA04513 259XA04516 259XA04517 259XA04518 249XA04001 249XA04007 249XA04008 249XA04009 249XA04010 249XA04011 249XA04012 249XA04013 249XA04014 249XA04015 249XA04016 249XA04018	[20]
D	249XA04020 249XA04021 249XA04023 249XA04024 249XA04025 249XA04026 249XA04027 249XA04028 249XA04029 249XA04030 249XA04031 249XA04032 249XA04033 249XA04034 249XA04035 249XA04036 249XA04037 249XA04038 249XA04039 249XA04040	[20]
E	249XA04041 249XA04042 249XA04043 249XA04044 249XA04045 249XA04046 249XA04048 249XA04049 249XA04050 249XA04051 249XA04052 249XA04053 249XA04054 249XA04055 249XA04056 249XA04058 249XA04059 249XA04061 249XA04062 249XA04064	[20]
F	249XA04065 249XA04066 249XA04068 249XA04071 249XA04072 249XA04073 249XA04074 249XA04075 249XA04076 249XA04078 249XA04079 249XA04080 249XA04081 249XA04082 249XA04083 249XA04084 249XA04085 249XA04086 249XA04088 249XA04090	[20]
G	249XA04091 249XA04092 249XA04093 249XA04094 249XA04095 249XA04096 249XA04097 249XA04098 249XA04100 249XA04101 249XA04102 249XA04104 249XA04105 249XA04106 249XA04108 249XA04109 249XA04110 249XA04111 249XA04112 249XA04113	[20]
H	249XA04114 249XA04115 249XA04116 249XA04117 249XA04118 249XA04119 249XA04121 249XA04122 249XA04123 249XA04124 249XA04126 249XA04127 249XA04128 249XA04129 249XA04131 249XA04132 249XA04133 249XA04134 249XA04135 249XA04136	[20]
I	249XA04137 249XA04138 249XA04139 249XA04141 249XA04143 249XA04144 249XA04145 249XA04146 249XA04147 249XA04150 249XA04151 249XA04152 249XA04153 249XA04154 249XA04155 249XA04156 249XA04157 249XA04159 249XA04160 249XA04161	[20]
J	249XA04162 249XA04163 249XA04164 249XA04166 249XA04167 249XA04168 249XA04169 249XA04170 249XA04172 249XA04174 249XA04175 249XA04176 249XA04177 249XA04178 249XA04179 249XA04180 249XA04181 249XA04182 249XA04183 249XA04184	[20]
K	249XA04186 249XA04188 249XA04189 249XA04190 249XA04191 249XA04194 249XA04195 249XA04196 249XA04197 239X1A0453 239X1A04A7 239X1A04H6	[12]

Total : 211

Name of the Practical examination	Venue	Date	9.00AM to 11.00AM	10.00AM to 12.00PM	11.00PM to 1.00PM	12.00PM to 2.00PM	1.00 PM to 3.00 PM	2.00 PM to 4.00 PM
Tinkering Lab (TL(P))	Tinkering Lab - CSM Block	13/11/2026	A	B	C	D	E	F
		14/11/2026	G	H	I	J	K	-

PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL

Four Year B.Tech Degree Examination: November-2026

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT

Honors in ECE

B. Tech V- SEMESTER (S-23)

Scheme & Schedule for Regular End Practical Examinations

	Lab name	Date & Time	Venue
Honors in ECE	Analog and Digital IC Design Lab (ADICD(P))	14/11/2026 2:00PM-5:00PM	VLSI Lab (ECE Block 1)

BATCH	REGD. NOS	TOTAL
A	249XA04002 249XA04003 249XA04004 249XA04017 249XA04067 249XA04069 249XA04070 249XA04077 249XA04130 249XA04149 249XA04165 259XA04502 259XA04504 259XA04507 259XA04508 259XA04510 259XA04512 259XA04514 259XA04515	19

PRINCIPAL

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):: KURNOOL
Four Year B.Tech Degree Examination: NOVEMBER- 2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. Tech ECE VII- SEMESTER (S-23)
Scheme & Schedule for Regular Practical Examinations

BATCH	REGD. NOS	TOTAL
A	249X5A04K9 249X5A04L0 249X5A04L1 249X5A04L2 249X5A04L3 249X5A04L4 249X5A04L5 249X5A04L6 249X5A04L7 249X5A04L8 249X5A04L9 249X5A04M0 249X5A04M1 249X5A04M2 249X5A04M3 249X5A04M4 249X5A04M5 249X5A04M6 239X1A0401 239X1A0402 239X1A0403 239X1A0404 239X1A0405 239X1A0406 239X1A0407 239X1A0408 239X1A0409 239X1A0410 239X1A0411 239X1A0412 239X1A0413 239X1A0414 239X1A0415 239X1A0416 239X1A0417	[35]
B	239X1A0418 239X1A0419 239X1A0420 239X1A0421 239X1A0422 239X1A0423 239X1A0424 239X1A0425 239X1A0426 239X1A0427 239X1A0428 239X1A0429 239X1A0430 239X1A0431 239X1A0432 239X1A0433 239X1A0434 239X1A0435 239X1A0436 239X1A0438 239X1A0439 239X1A0440 239X1A0441 239X1A0442 239X1A0443 239X1A0444 239X1A0445 239X1A0446 239X1A0447 239X1A0448 239X1A0449 239X1A0450 239X1A0451 239X1A0452 239X1A0454	[35]
C	239X1A0455 239X1A0456 239X1A0457 239X1A0458 239X1A0459 239X1A0460 239X1A0461 239X1A0462 239X1A0463 239X1A0464 239X1A0465 239X1A0466 239X1A0467 239X1A0468 239X1A0469 239X1A0470 239X1A0471 239X1A0473 239X1A0474 239X1A0475 239X1A0476 239X1A0477 239X1A0478 239X1A0479 239X1A0480 239X1A0481 239X1A0482 239X1A0483 239X1A0484 239X1A0485 239X1A0486 239X1A0487 239X1A0488 239X1A0489 239X1A0490	[35]
D	239X1A0491 239X1A0492 239X1A0493 239X1A0494 239X1A0495 239X1A0496 239X1A0497 239X1A0498 239X1A0499 239X1A04A0 239X1A04A1 239X1A04A2 239X1A04A3 239X1A04A6 239X1A04A9 239X1A04B0 239X1A04B1 239X1A04B2 239X1A04B3 239X1A04B4 239X1A04B5 239X1A04B6 239X1A04B7 239X1A04B8 239X1A04B9 239X1A04C1 239X1A04C2 239X1A04C3 239X1A04C4 239X1A04C5 239X1A04C6 239X1A04C7 239X1A04C8 239X1A04C9 239X1A04D0	[35]
E	239X1A04D1 239X1A04D2 239X1A04D3 239X1A04D4 239X1A04D5 239X1A04D6 239X1A04D7 239X1A04D8 239X1A04D9 239X1A04E0 239X1A04E1 239X1A04E2 239X1A04E3 239X1A04E4 239X1A04E6 239X1A04E7 239X1A04E8 239X1A04E9 239X1A04F0 239X1A04F1 239X1A04F2 239X1A04F3 239X1A04F4 239X1A04F5 239X1A04F6 239X1A04F7 239X1A04F8 239X1A04F9 239X1A04G0 239X1A04G1 239X1A04G2 239X1A04G3 239X1A04G4 239X1A04G5 239X1A04G6	[35]
F	239X1A04G7 239X1A04G8 239X1A04G9 239X1A04H0 239X1A04H1 239X1A04H2 239X1A04H3 239X1A04H4 239X1A04H7 239X1A04H8 239X1A04H9 239X1A04J0 239X1A04J1 239X1A04J2 239X1A04J4 239X1A04J5 239X1A04J6 239X1A04J7 239X1A04J8 239X1A04J9 239X1A04K0 239X1A04K1 239X1A04K2 239X1A04K3 239X1A04K4 239X1A04K5 239X1A04K6 239X1A04K7 239X1A04K8 229X1A04A0 229X1A04G1	[31]

Total: 206

Name of the Practical examination	Venue	Date	8.30AM-11.30AM	11.30AM-2.30PM	2.30PM-5.30PM
RF System Design Tools (RFSDT(P))	IoT LAB BLOCK 3 ECE DEPT	11-11-2026	A	B	C
		12-11-2026	D	E	F

PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL

Four-year B. Tech Degree Examination: Nov 2026

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT

B. TECH ECE IV- SEMESTER (S-23)

Scheme of schedule for Supplementary Practical examinations

BATCH	ROLL NOS	TOTAL
A	249XA04172	01

Name of the Practical examination	Venue	Date	Time (9.00AM - 12.00 Noon)
Electronic Circuit Analysis Lab (ECA(P))	Basic Electronics Lab Block 2	10/11/2026	A

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL

Four-year B. Tech Degree Examination: Nov 2026

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT

B. TECH ECE IV- SEMESTER (S-23)

Scheme of schedule for Supplementary Practical examinations

BATCH	ROLL NOS			TOTAL
A	249XA04037	249XA04038	249XA04041	09
	249XA04042	249XA04048	249XA04049	
	249XA04054	249XA04058	249XA04172	

Name of the Practical examination	Venue	Date	Time (9.00AM - 12.00 Noon)
Analog & Digital Communications Lab (ADC(P))	Communications Lab Block 2	09/11/2026	A

PRINCIPAL

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
Four-year B. Tech Degree Examination: Nov 2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. TECH ECE VI- SEMESTER (S-23)
Scheme of schedule for Supplementary Practical examinations

BATCH	REGD. NOS		TOTAL
A	239X1A0467	239X1A0496	02

Name of the Practical examination	Venue	Date	Time (2.00PM - 05.00 PM)
Microwave & Optical Communications Lab (MWOC(P))	Microwave LAB BLOCK 2	10/11/2026	A

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
Four-year B. Tech Degree Examination: Nov 2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. TECH ECE VI- SEMESTER (S-23)
Scheme of schedule for Supplementary Practical examinations

BATCH	REGD. NOS			TOTAL
A	239X1A0463	239X1A0467	239X1A0496	03

Name of the Practical examination	Venue	Date	Time (9.00AM - 12.00 Noon)
VLSI Design Lab (VLSID(P))	VLSI LAB BLOCK 1	14/11/2026	A

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL
Four-year B. Tech Degree Examination: Nov 2026
ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
B. TECH ECE VI- SEMESTER (S-23)
Scheme of schedule for Supplementary Practical examinations

BATCH	REGD. NOS		TOTAL
A	239X1A0467	239X1A0496	02

Name of the Practical examination	Venue	Date	Time (9.00AM - 12.00 Noon)
Machine Learning & DSP Lab (ML&DSP(P))	IOT LAB BLOCK 3	13/11/2026	A

PRINCIPAL