

**G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) :: KURNOOL**  
**Four year B. Tech Degree Examination: October, 2020**  
**ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT**  
**B.TECH ECE III- SEMESTER (S-17)**  
**Scheme of schedule for Supplementary Practical examinations**

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
<b>A</b>	199X5A04J6, 189X1A0405, 189X1A0407, 189X1A0409, 189X1A0412, 189X1A0425, 189X1A0427, 189X1A0434, 189X1A0438, 189X1A0440, 189X1A0445, 189X1A0447, 189X1A0456, 189X1A0457, 189X1A0459	(15)

Name of the Practical examination	Venue	Date	Time
			(10.00AM - 1.00 PM)
Basic Simulation lab(BS(P))	BS Lab	20-10-2020	A

*B. S. Reddy*

**PRINCIPAL**

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**B.TECH ECE V- SEMESTER (S-17)**  
**Scheme of schedule for Supplementary Practical examinations**

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
<b>A</b>	189X5A04K4, 179X1A04G1	(02)

Name of the Practical examination	Venue	Date	Batch	Time
Analog& Digital communication Lab (ADCM(P))	CM lab	20-10-2020	A	10.00AM - 1.00 PM
Microprocessors and Microcontrollers Lab (MPMC(P))	MP Lab	21-10-2020	A	10.00AM - 1.00 PM
Linear IC Applications Lab(LICA(P))	ICA Lab	22-10-2020	A	10.00AM - 1.00 PM

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**B.TECH ECE VII- SEMESTER (S-13)**  
**Scheme of schedule for Supplementary Practical examinations**

<u>BATCH</u>	<u>REGD. NOS</u>	<u>TOTAL</u>
<b>A</b>	179X5A04J4, 169X1A04F2, 169X1A04G7, 169X1A04G9	(04)
<b>B</b>	149X1A04F9	(01)

Name of the Practical examination	Venue	Date	Batch	Time
Microwave & Fiber Optics Lab (MWFO(P))	Microwave lab	20-10-2020	B	10.00AM - 1.00 PM
VLSI Lab (VLSI (P))	DSP Lab	21-10-2020	A,B	09.00AM - 12.00Noon
Project Work preliminary PWP(P)		21-10-2020	B	12.30PM - 1.00 PM

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**PRINCIPAL**