

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL
M.Tech I – SEMESTER (Scheme-2017)
SECOND SESSIONAL EXAMINATIONS – MAY – 2022

Time : 10.00 P.M. to 12.00 Noon.

Date	Day	Paper No.	Structural Engineering	VLSI and Embedded Systems
16-05-2022	MON	1	AEM- Advanced Engineering mathematics	AICD- CMOS Analog IC Design
17-05-2022	TUE	2	ToE – Theory of Elasticity	ADSD- Advanced Digital System Design using Verilog
18-05-2022	WED	3	ASA- Advanced Structural Analysis	ERTOS- Embedded Real Time Operating System
19-05-2022	THU	4	TAP- Theory and Analysis of Plates	AES – Advanced Embedded Systems
20-05-2022	FRI	5	BE-Elective – I: Bridge Engineering	DICD -Elective – I: CMOS Digital IC Design
21-05-2022	SAT	6	ACT- Elective – I: Advanced Concrete Technology	ESA -Elective – II: Embedded System Architecture

**Note: - No grace period for entering into the examination hall during the Sessional and End exams.
Students are allowed into halls 15 minutes before commencement of examinations.**


PRINCIPAL
S/C