

# G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

## M.Tech I – SEMESTER (Scheme-2017)

### END (Supplementary) EXAMINATIONS – OCTOBER– 2020

**Time : 9.00 A.M. to 12.00 NOON**

Date	Day	Paper No.	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI and Embedded Systems	Thermal Sciences and Energy Systems	CAD/CAM	Power Electronics
01-10-2020	THU	1	<b>ADSA</b> –Advanced Data Structures & Algorithms	<b>AEM</b> - Advanced Engineering mathematics	<b>DET</b> -Detection & Estimation Theory	<b>AICD</b> - CMOS Analog IC Design	<b>PMA</b> - Probability and Mathematical Analysis	<b>CMT</b> – Computational Methods	<b>EMM</b> - Electrical Machines Modeling
03-10-2020	SAT	2	<b>SPM</b> – Software Project Management	<b>ToE</b> – Theory of Elasticity	<b>ASP</b> - Adaptive Signal Processing	<b>ADSD</b> - Advanced Digital System Design using Verilog	<b>ATD</b> - Advanced Thermodynamics	<b>CIM</b> - Computer integrated Manufacturing	<b>APC</b> - Analysis of Power Converters
04-10-2020	SUN	3	<b>ACN</b> – Advanced Computer Networks	<b>ASA</b> - Advanced Structural Analysis	<b>ADCM</b> - Advanced Digital Communications	<b>ERTOS</b> -Embedded Real Time Operating System	<b>RES</b> - Renewable Energy Sources	<b>CNCM</b> – Computer Numerical Control Machines	<b>SSPC</b> - Solid State Power Converters
05-10-2020	MON	4	<b>ADBMS – Elective – I:</b> Advanced Data Base Management Systems	<b>TAP</b> - Theory and Analysis of Plates	<b>MCN</b> - Mobile Communications	<b>AES</b> – Advanced Embedded Systems	<b>AF</b> - Alternative Fuels	<b>GM</b> - Geometric modeling	<b>DSP</b> - Digital Signal Processing
06-10-2020	TUE	5	<b>ACA</b> - <b>Elective – II:</b> Advanced Computer Architecture	<b>BE</b> - <b>Elective – I:</b> Bridge Engineering	<b>CTH</b> - <b>Elective – I:</b> Coding Techniques	<b>DICD</b> - <b>Elective – I:</b> CMOS Digital Integrated Circuit Design	<b>ECT</b> - <b>Elective – I:</b> Energy Conversion Technologies	<b>AOT</b> - <b>Elective – I:</b> Advanced Optimization Techniques	<b>DCS</b> – <b>Elective– I:</b> Digital Control Systems
07-10-2020	WED	6	---	<b>ACT</b> - <b>Elective – II:</b> Advanced Concrete Technology	<b>NNFL</b> - <b>Elective – II:</b> Neural Networks & Fuzzy logic	<b>ESA</b> - <b>Elective – II:</b> Embedded System Architecture	<b>RCG</b> - <b>Elective –II:</b> Refrigeration & Cryogenics	<b>FAD</b> - <b>Elective – II:</b> Failure Analysis & Design	<b>NNFL</b> - <b>Elective – II:</b> Neural Networks & Fuzzy logic

**Note: 1. No Grace Period for entering into the examination hall. Students are allowed into halls 15 minutes before commencement of examinations.**

**2. A student is required to complete the course of study satisfying the attendance requirements in all the two years of the course within a period of 4 academic years from the year of admission, failing which he/she shall be declared ineligible to pursue M.Tech degree course. Completing the course of study shall mean not only satisfying the attendance requirements but also passing of all the subjects including Project work within the stipulated period.**

  
ADDITIONAL CONTROLLER OF EXAMINATIONS

  
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