

**G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):KURNOOL**  
**NOTICE**

Date: 01/12/2021

Notification for M.Tech – II – Semester (Regular and Supplementary - Scheme-17) and M.Tech – I – Semester (Supplementary- Scheme-2017) - End Examinations.

**The important dates are as follows.**

- |  |              |
|--|--------------|
| a) Registration for exams commences from                                       | : 01/12/2021 |
| b) *Last date for online submission of applications without fine               | : 13/12/2021 |
| c) *Last date for online submission of applications with fine <b>Rs.1000/-</b> | : 16/12/2021 |
| d) Closure of nominal rolls  | : 18/12/2021 |

**\* LAST DATE MEANS BOTH FOR PAYMENT OF EVALUATION FEE AND ONLINE SUBMISSION OF APPLICATION**

The applications will be accepted with penal fee as prescribed (the details of which are notified separately in the exam section notice board) after the last date without fine till the closure of nominal rolls. **NO APPLICATION WILL BE ACCEPTED AFTER THE CLOSURE OF NOMINAL ROLLS.**

Students shall follow the procedure detailed below and complete the registration process and fee payment online only within the stipulated time.

**Procedure for online registration for end exams**

1. Visit the college website (**www.gprec.ac.in**)
2. Click on Register for **"M.Tech - JAN-22 End Exams"**.
3. Login using Username (Student Roll No) & Password (DOB)
4. Click on the check box (on the left side of the subject) to enroll for Supplementary Subjects/ Regular Exams.
5. Click on preview button to view the summary of the subjects registered. To make any changes in the registration click on **'Modify'** button.
6. Click on **'Submit'** button to save the application. No further changes shall be permitted once the application is submitted.
7. After successful submission, Evaluation fee can be paid through online only.
8. Students are advised to keep the online payment acknowledgement safely for future reference.
9. Students need not submit the online generated application in the exam cell.

  
PRINCIPAL

Copy to all NBs. Exams section, file.

**G.PULLA REDDY ENGINEERING COLLEGE (Autonomous):KURNOOL**

**SCHEDULE**

Date : 01/12/2021

Schedule for the payment of examination fee for M.Tech II Semester (Regular & Supplementary - Scheme-17) and M.Tech I- Semester (Supplementary - Scheme-2017) End examinations of January 2022 and Fee particulars are as follows.

**Scheme – 2017 For REGULAR CANDIDATES:-**

Examination Fee (Theory / Practical) : Rs. 1500.00

**For Supplementary Candidates :-**

1. For One Subject (Theory / Practical) : Rs. 600.00
2. For Two Subjects (Theory / Practical) : Rs.1200.00
3. For Three and More Subjects (Theory / Practical) : Rs.1500.00

**Last date for submission of application without fine is 13/12/2021**

**THE PENAL FEE FOR THE APPLICATION RECEIVED AFTER 13/12/2021 IS AS GIVEN BELOW:**

- A. On or before 16/12/2021 : Rs. 1000.00
- B. On or before 18/12/2021 : Rs. 3000.00

**Note: A student is required to complete the course of study satisfying the attendance requirements in all the two years of the course within a period of 4 academic years from the year of admission, failing which he/she shall be declared ineligible to pursue M.Tech degree course. Completing the course of study shall mean not only satisfying the attendance requirements but also passing of all the subjects including project work within the stipulated period**

  
PRINCIPAL

Copy to All NBs.  
Exams Section.  
File.

# G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

## M.Tech II – SEMESTER (Scheme-2017)

### END EXAMINATIONS ( Regular & Supplementary)- January- 2022

**Time : 9:00 A.M. to 12:00 NOON**

Date	Day	Paper No.	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI and Embedded Systems	Thermal Sciences and Energy Systems	CAD/CAM	Power Electronics
03-01-2022	MON	1	<b>AI-</b> Artificial Intelligence	<b>ARCD-</b> Advanced Reinforced Concrete Design	<b>DIPPR-</b> Digital Image processing and Pattern Recognition	<b>LVD-</b> Low Power VLSI Design	<b>FEA –</b> Finite Element Analysis	<b>FEA –</b> Finite Element Analysis	<b>SDCD –</b> Solid State DC Drives
05-01-2022	WED	2	<b>CC –</b> Cloud Computing	<b>ASSD –</b> Advanced Structural Steel Design	<b>OCN-</b> Optical Communication	<b>VDFT-</b> VLSI Design for Testability	<b>AHT –</b> Advanced Heat Transfer	<b>RBT –</b> Robotics	<b>SACD –</b> Solid State AC Drives
07-01-2022	FRI	3	<b>DS–</b> Data Science	<b>SS-</b> Stability of Structures	<b>SSP –</b> Statistical Signal Processing	<b>VLSIA-</b> VLSI Architecture	<b>GTT –</b> Gas Turbine Theory	<b>FMS –</b> Flexible Manufacturing Systems	<b>HVDC-</b> HVDC and FACTS
10-01-2022	MON	4	<b>SQT-Elective-III:</b> Software Quality and Testing	<b>SD-</b> Structural Dynamics	<b>MWC –</b> Microwave Communications	<b>MESD –</b> Microcontrollers for Embedded System Design	<b>ECM –</b> Energy Conservation & Management	<b>RD –</b> Robust Design	<b>ASPES-</b> Advance simulation of Power Electronic Systems
12-01-2022	WED	5	<b>FOSS-Elective IV:</b> Free and open source software	<b>FEM-Elective –III:</b> Finite Element Methods	<b>CR- Elective – III:</b> Cognitive Radio	<b>EDAT-Elective -III:</b> Electronic Design Automation Tools	<b>DHE -Elective-III:</b> Design of Heat Transfer Equipment	<b>SMS- Elective -III:</b> Simulation of Manufacturing Systems	<b>IAPE–Elective-III:</b> Industrial Applications of Power Electronics
17-01-2022	MON	6	---	<b>ERDS-Elective-IV:</b> Earthquake Resistant Design of Structures	<b>DDPS- Elective – IV:</b> Design of Digital Signal Processing Systems	<b>HSC -Elective – IV:</b> Hardware Software Co-Design	<b>DACS -Elective-IV:</b> Design of Air Conditioning Systems	<b>RPTM- Elective IV:</b> Rapid Prototyping Tooling and Manufacture	<b>RES- Elective – IV:</b> Renewable Energy Sources

**Note: 1. No Grace Period for entering into the examination hall. Students are allowed into halls 15 minutes before commencement of examinations.**

**2. A student is required to complete the course of study satisfying the attendance requirements in all the two years of the course within a period of 4 academic years from the year of admission, failing which he/she shall be declared ineligible to pursue M.Tech degree course. Completing the course of study shall mean not only satisfying the attendance requirements but also passing of all the subjects including Project work within the stipulated period**

  
ADDITIONAL CONTROLLER OF EXAMINATIONS

  
CONTROLLER OF EXAMINATIONS

# G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL

## M.Tech I – SEMESTER (Scheme-2017)

### END (SUPPLEMENTARY) EXAMINATIONS – January – 2022

**Time :09:00 A.M. to 12:00 Noon**

Date	Day	Paper No.	Computer Science and Engineering	Structural Engineering	Communications and Signal Processing	VLSI and Embedded Systems	Thermal Sciences and Energy Systems	CAD/CAM	Power Electronics
04-01-2022	TUE	1	<b>ADSA</b> –Advanced Data Structures & Algorithms	<b>AEM-</b> Advanced Engineering mathematics	<b>DET</b> -Detection & Estimation Theory	<b>AICD-</b> CMOS Analog IC Design	<b>PMA</b> - Probability and Mathematical Analysis	<b>CMT</b> – Computational Methods	<b>EMM</b> - Electrical Machines Modeling
06-01-2022	THU	2	<b>SPM</b> – Software Project Management	<b>ToE</b> – Theory of Elasticity	<b>ASP</b> - Adaptive Signal Processing	<b>ADSD-</b> Advanced Digital System Design using Verilog	<b>ATD</b> - Advanced Thermodynamics	<b>CIM</b> - Computer integrated Manufacturing	<b>APC</b> - Analysis of Power Converters
08-01-2022	SAT	3	<b>ACN</b> – Advanced Computer Networks	<b>ASA-</b> Advanced Structural Analysis	<b>ADCM</b> - Advanced Digital Communications	<b>ERTOS</b> -Embedded Real Time Operating System	<b>RES</b> - Renewable Energy Sources	<b>CNCM</b> – Computer Numerical Control Machines	<b>SSPC-</b> Solid State Power Converters
11-01-2022	TUE	4	<b>ADBMS – Elective – I:</b> Advanced Data Base Management Systems	<b>TAP-</b> Theory and Analysis of Plates	<b>MCN</b> - Mobile Communications	<b>AES</b> – Advanced Embedded Systems	<b>AF</b> - Alternative Fuels	<b>GM</b> - Geometric modeling	<b>DSP-</b> Digital Signal Processing
13-01-2022	THU	5	<b>ACA-</b> Elective – II: Advanced Computer Architecture	<b>BE-</b> Elective – I: Bridge Engineering	<b>CTH-</b> Elective – I: Coding Techniques	<b>DICD -Elective – I:</b> CMOS Digital IC Design	<b>ECT - Elective – I:</b> Energy Conversion Technologies	<b>AOT- Elective – I:</b> Advanced Optimization Techniques	<b>DCS – Elective– I:</b> Digital Control Systems
18-01-2022	TUE	6	---	<b>ACT- Elective – II:</b> Advanced Concrete Technology	<b>NNFL- Elective – II:</b> Neural Networks & Fuzzy logic	<b>ESA -Elective – II:</b> Embedded System Architecture	<b>RCG - Elective –II:</b> Refrigeration & Cryogenics	<b>FAD- Elective – II:</b> Failure Analysis & Design	<b>NNFL - Elective – II:</b> Neural Networks & Fuzzy logic

**Note: 1. No Grace Period for entering into the examination hall. Students are allowed into halls 15 minutes before commencement of examinations.**

**2. A student is required to complete the course of study satisfying the attendance requirements in all the two years of the course within a period of 4 academic years from the year of admission, failing which he/she shall be declared ineligible to pursue M.Tech degree course. Completing the course of study shall mean not only satisfying the attendance requirements but also passing of all the subjects including Project work within the stipulated period**

  
ADDITIONAL CONTROLLER OF EXAMINATIONS

  
CONTROLLER OF EXAMINATIONS