

**G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL**  
**M.Tech I – SEMESTER (Scheme-2022)**  
**FIRST SESSIONAL EXAMINATIONS – DECEMBER – 2024**

**Time : 10:00 A.M. to 12.00 NOON.**

Date	Day	Paper No.	Computer Science & Engineering	Structural Engineering	VLSI and Embedded Systems
16-12-2024	MON	1	<b>ADSA</b> –Advanced Data Structures & Algorithms	<b>ToE</b> – Theory of Elasticity	<b>DVD</b> - Digital VLSI Design
17-12-2024	TUE	2	<b>SEA</b> - Software Engineering and Applications	<b>ASA</b> - Advanced Structural Analysis	<b>AES</b> - Advanced Embedded Systems
18-12-2024	WED	3	<b>AI</b> - Artificial Intelligence	<b>TOP</b> -Theory of Plates	<b>ADSD</b> - Advanced Digital System Design using Verilog
19-12-2024	THU	4	<b>CC Elective – I</b> – Cloud Computing	<b>RRS-Elective – I</b> : Rehabilitation and Retrofitting Structures	<b>HACA - Elective – I</b> Hardware Algorithms for Computer Arithmetic
20-12-2024	FRI	5	<b>NLP Elective –I I</b> Natural Language Processing	<b>BE-Elective –I I</b> : Bridge Engineering	<b>ESA -Elective – II</b> : Embedded System Architecture

**Note: - No grace period for entering into the examination hall during the Sessional and End exams.  
Students are allowed into halls 15 minutes before commencement of examinations.**

  
**PRINCIPAL**