

**G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL**  
**M.Tech II – SEMESTER (Scheme-2025)**  
**FIRST SESSIONAL EXAMINATIONS – JUNE – 2026**

**Time : 10:00 A.M. to 12.00 NOON.**

Date	Day	Paper No.	Computer Science & Engineering	VLSI and Embedded Systems
15-06-2026	MON	1	<b>ASE</b> - Advanced in Software Engineering	<b>CAICD</b> - CMOS Analog IC Design
16-06-2026	TUE	2	<b>ADB</b> – Advanced Databases	<b>ESD</b> – Embedded Systems Design
17-06-2026	WED	3	<b>Elective –III - DLA</b> – Deep Learning and Applications	<b>Elective -III - PLES</b> - Programming Languages for Embedded software
18-06-2026	THU	4	<b>Elective – IV - GAI</b> - Generative AI	<b>Elective - IV – PDA</b> – Physical Design Automation
19-06-2026	FRI	5	<b>QTA</b> - Quantum Technologies and applications	<b>QTA</b> - Quantum Technologies and applications

**Note: - No grace period for entering into the examination hall during the Sessional and End exams.  
Students are allowed into halls 15 minutes before commencement of examinations.**

  
**PRINCIPAL**