# G.PULLA REDDY ENGINEERING COLLEGE (Autonomous) : KURNOOL
## B.TECH VI-SEMESTER (SCHEME – 2017) END EXAMINATIONS: NOVEMBER-2020

## TIME-TABLE

**Time: 9.00 AM to 12.00 NOON**

| Date | Day | Computer Science & Engineering | Civil Engineering | Electronics & Communication Engineering | Mechanical Engineering | Electrical and Electronics Engineering |
|------|-----|-------------------------------|-------------------|----------------------------------------|------------------------|----------------------------------------|
| 23-11-2020 | MON | ML– Machine Learning | SSDD–Steel Structures Design and Drawing | ESP– Embedded System Programming | OR – Operation Research | PEL2 –Power electronics-II |
| 24-11-2020 | TUE | CD– Compiler Design | WRE2–Water Resources Engineering-II | DSP–Digital Signal Processing | HT – Heat Transfer | PS3 – Power Systems-III |
| 25-11-2020 | WED | MCP–Mobile Computing | SMECH –Soil Mechanics | AWP–Antenna & Wave Propagation | DMM-II–Design of Machine Members-II | -------------- |
| 26-11-2020 | THU | Professional Elective:-II MMA–Multimedia and Animation | Professional Elective:-I PSC–Prestressed Concrete | LCS–Linear Control Systems | FEM–Finite Element Methods | Professional Elective:-I ISS–Introduction to Signals & Systems |
| 27-11-2020 | FRI | Professional Elective:-III ADBMS/DP–Advanced Database Management Systems/ Design Patterns | Professional Elective:-II SE–Sanitary Engineering | Professional Elective:-II IOT–Internet of Things | Professional Elective:-II TD–Tool Design | Professional Elective:-II UEP–Utilization of Electrical Power |
| 28-11-2020 | SAT | *Open Elective-II | *Open Elective-II | *Open Elective-II | *Open Elective-II | *Open Elective-II |

**PRACTICALS**

| | | Computer Science & Engineering | Civil Engineering | Electronics & Communication Engineering | Mechanical Engineering | Electrical and Electronics Engineering |
|------|-----|-------------------------------|-------------------|----------------------------------------|------------------------|----------------------------------------|
| | | ML(P) –Machine Learning Lab | EE(P)–Environmental Engineering Lab | DSP(P)–Digital Signal Processing Lab | EMT(P)–Engineering Metrology Lab | PS(P) –Power Systems Lab |
| | | CD(P)–Compiler Design Lab | TE(P)–Transportation Engineering Lab | VES(P)– VLSI & Embedded Systems Lab | DBC(P)–Database & Computations Lab | PE (P)– Power Electronics Lab |
| | | -------------- | GIS(P)–Geographical Information Systems Lab | MP-I– Mini Project-I | HT(P) –Heat Transfer Lab | MC(P)–Microcontrollers lab |
| | | -------------- | -------------- | -------------- | -------------- | MP-I–Mini Project –I |

**Note: 1**. No grace period for entering into the examination hall. Students are allowed into halls 15 minutes before commencement of Examinations.
**2.** All end practical exams will be held after end theory exams. For exact dates students are required to see the department notice board.

**\*Open Elective-II: Object Oriented Programming Through JAVA, Scientific Programming with Python, Building Information Modeling, Product Life Cycle Management, Industrial Safety.**

Verified

Addl. Controller of Examinations

CONTROLLER OF EXAMINATIONS