

Contact

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Top Skills

Software Troubleshooting
Pranayama
Yoga Instruction

Languages

Sanskrit (Elementary)
Hindi (Elementary)
Tamil (Elementary)
English (Professional Working)
Telugu (Limited Working)
Kannada (Native or Bilingual)

Certifications

Practical GitHub Copilot
Learning Cloud Computing: Public Cloud Platforms
Product Assurance and Security White Belt
What Is Generative AI?
Learning Cloud Computing: Core Concepts

Honors-Awards

Star performer of the Month
Start of the month award
Star performer of the Month
Best performer of the year

Publications

A Novel Approach to Detect BIOS Knobs Configuration Issues Early in the Platform Milestones
Advanced Python Scripting Capabilities at UEFI for Platform/Firmware Validation
Workspace Allocation and Management System with Realtime Feedback from IoT Sensors

Jayaprakash Nevara

Senior System Software Architect at Intel Corporation
Bengaluru, Karnataka, India

Summary

I am an Embedded Systems Professional having close to 18 years of experience in the design and development of products spanning multiple domains. I have worked on complete product development life cycle starting from Requirement analysis till Release and post release maintenance in Semiconductor, Healthcare and M2M communications industries.

Specialties: Embedded Systems, C, Linux, Device Drivers, ARM Processor & PIC processors, BSP, Thermal Handheld Printer Driver and application, Card readers, M2M communications, Power Management, Boot loaders, File System implementation on SD card, Compact Flash etc

Experience

Intel Corporation

12 years 8 months

Senior System Software Architect

April 2022 - Present (4 years)

Bengaluru, Karnataka, India

Software Architect

April 2018 - March 2022 (4 years)

Bengaluru, Karnataka, India

Software Development Engineer

August 2013 - March 2018 (4 years 8 months)

Bengaluru Area, India

CISCO Systems, Bangalore

Sr. Software Engineer

April 2012 - August 2013 (1 year 5 months)

Bangalore

Usage and Impact of - a Comprehensive OS Agnostic software solution for power thermal validation and debug of Intel platforms

Latency Measurement Tool for Lid Controller Hub Latency Key Performance Indicators Validation

I have worked as a senior software engineer in L2 layer of the network stack on Catalyst 6500 multi layer switching platform.

My roles and responsibilities include:

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Design and Development of new feature requirements on L2 security related features on Cat6k platform

Analyzing and fixing the real time customer issues

Polycom R&D center, Hyderabad

Lead Engineer

September 2011 - April 2012 (8 months)

Hyderabad, India

Worked as Lead Engineer on the development of Platform for the soft phone client device named as GobiGrande.

Roles and Responsibilities:

Design and Development of Features

- 1) Composite USB (with Ethernet, Audio, HID functions) driver for GobiGrande a soft phone client
- 2) Porting of Polycom Management Shell
- 3) Logging feature for debugging the device

Digi M2M solutions India Pvt :td

Lead Engineer

May 2010 - August 2011 (1 year 4 months)

bangalore

Joined Digi as Sr Software Engineer, I worked on Power management aspects of the Digi - Vehicle tracking and Taxi Dispatch systems based products.

My roles here include:

Comparative study of Power management micro controllers select the processor meeting our requirements

Requirement analysis for Power management

Design of Power management firmware

Coding - development of I2C, UART, RTCC, GPIO, Flash Drivers and application logic

Unit Testing

Integrating the Power Management processor with the application processor

Integration testing

Co-ordinating with cross-functional teams
Configuration Management of the Power Management Firmware
Release management

Philips Medical Systems

Technical Specialist

January 2008 - August 2009 (1 year 8 months)

Worked on enhancing the features of an existing medical device known as "Patient Monitor". Initially worked in Florida, USA at the client location to get the project to Bangalore. After successful execution of few feature enhancements there we got the client confidence and the project was awarded to the Bangalore development center.

Worked on Features like Trend Arrows, which shows the trend of vital signs graphically on the display.

Integrated the Patient Monitoring Device with the HL7 server using Serial to Ethernet converter as the Patient Monitor device was lacking the Ethernet connectivity. This effort of mine was well recognized by the Philips partners "Invivo" and was awarded with "Star of the Month".

Also my over all contribution to the project was recognized by Philips India and was awarded with the "Star Performer" award .

Zilog

Senior Software Engineer

February 2006 - January 2008 (2 years)

Worked on Linux Kernel Modules (Device Drivers) for ARM processor, BSP, Thermal Handheld Printer Driver driver, SPI driver, CF driver, FAT file system on CF, SD card, Magnetic Card Reader Driver also worked on USB stack development.

TCS

Assistant Systems Engineer

August 2003 - February 2006 (2 years 7 months)

I started my career with TCS, Bangalore after attending 3 months training at TCS, Thiruvananthapuram.

I have worked on Mainframes technology in Banking domain as a developer of mainframes applications.

I was working for AMEX client on Electronic Communication Suite. ECS is the main application which sends out all forms of communications from Amex to its card members which includes Alerts like Payment Received, Payment Due, E-Statement Ready, Balance Update, Fraud Attempt etc which are sent out in Various languages including English, French, Spanish etc. These alerts are delivered to the registered customers as per their requests to various kinds of devices like PDA, Mobile or Email.

I worked on Enabling these alerts which were there only for USA customers, to international customers. This effort of mine was recognized and was awarded with "Star Performer of the Month Award" by TCS, Bangalore.

Education

National Institute of Technology Karnataka

BE, Computer Engineering · (1999 - 2003)